

FIG. 1A

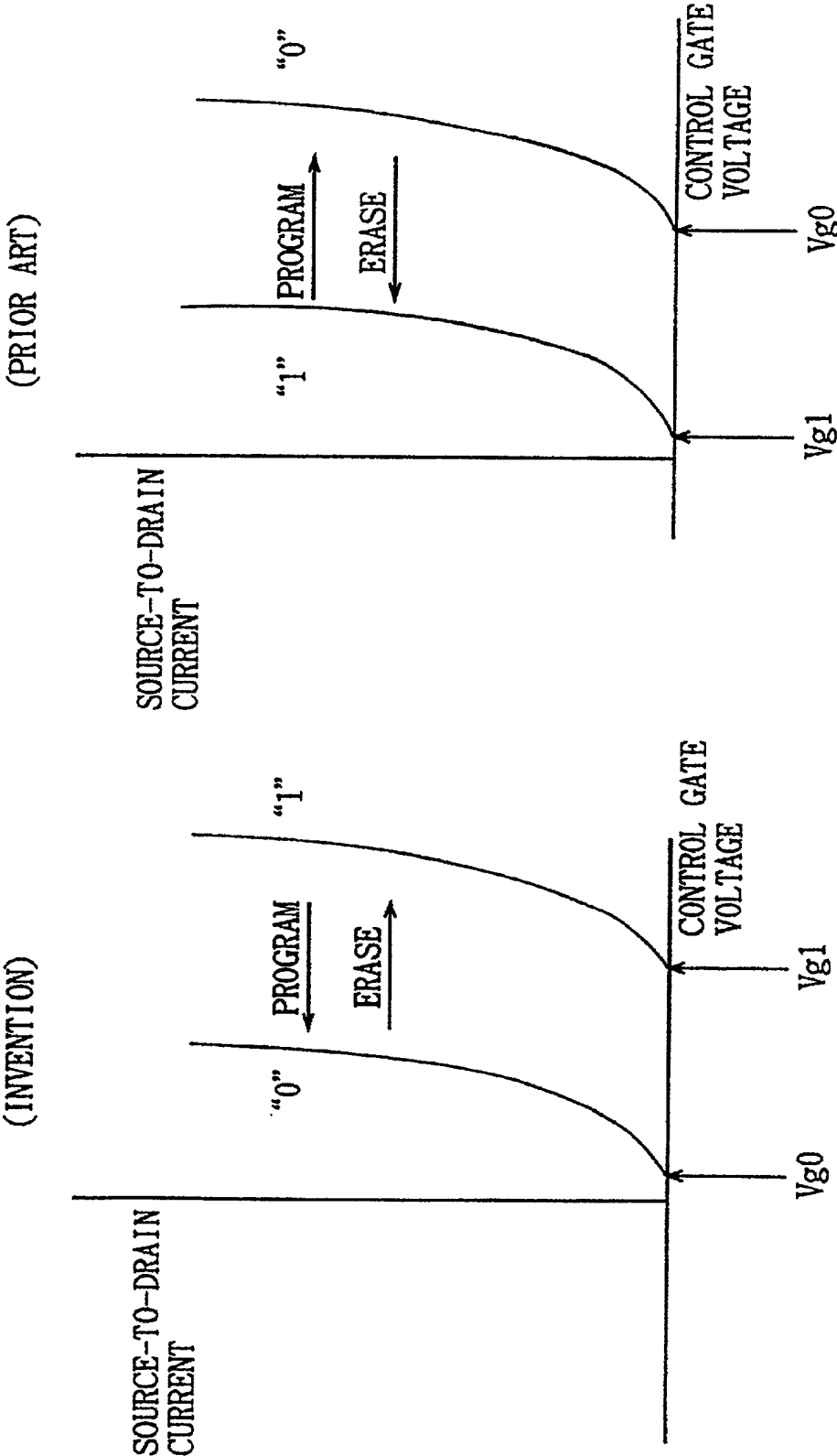


FIG. 1B

FIG. 2A  
(INVENTION)

	STORED DATA	FLOATING GATE	THRESHOLD VOLTAGE
ERASED STATE	"1"	INJECTION	HIGH
PROGRAMMED STATE	"0"	EMISSION	LOW

FIG. 2B  
(PRIOR ART)

	STORED DATA	FLOATING GATE	THRESHOLD VOLTAGE
ERASED STATE	"1"	EMISSION	LOW
PROGRAMMED STATE	"0"	INJECTION	HIGH

FIG. 3

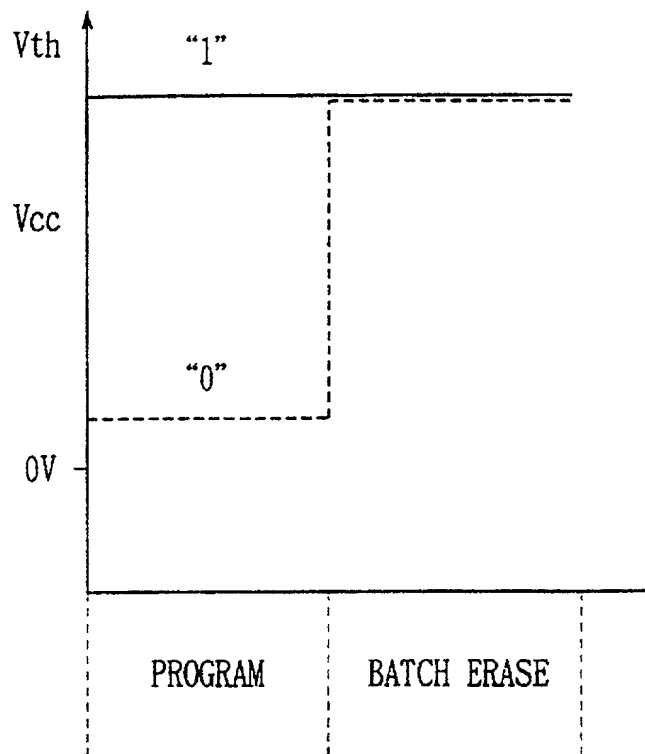


FIG. 4

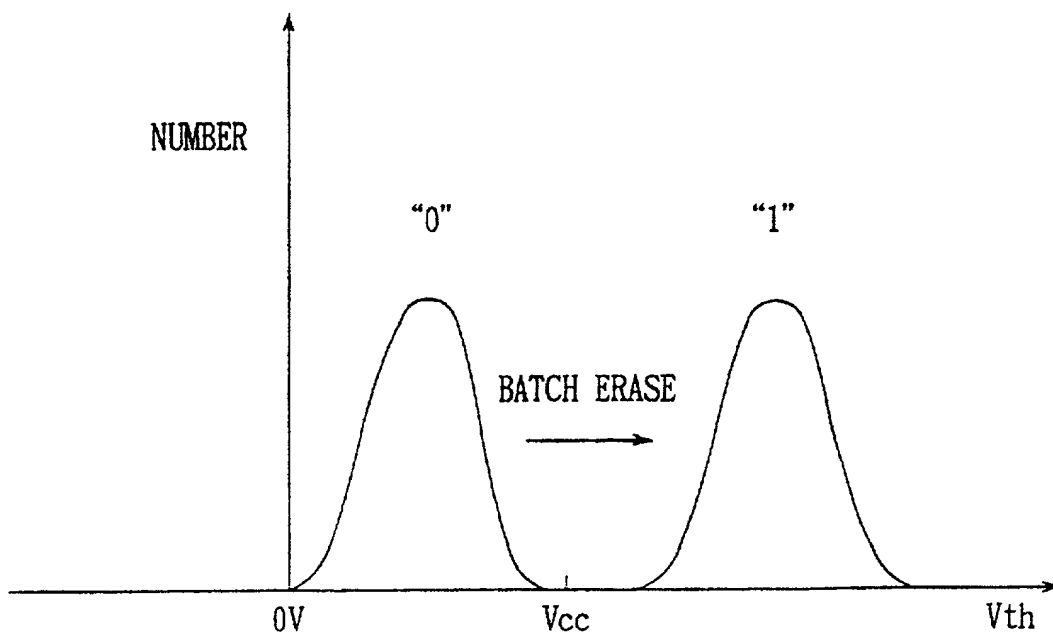


FIG. 5

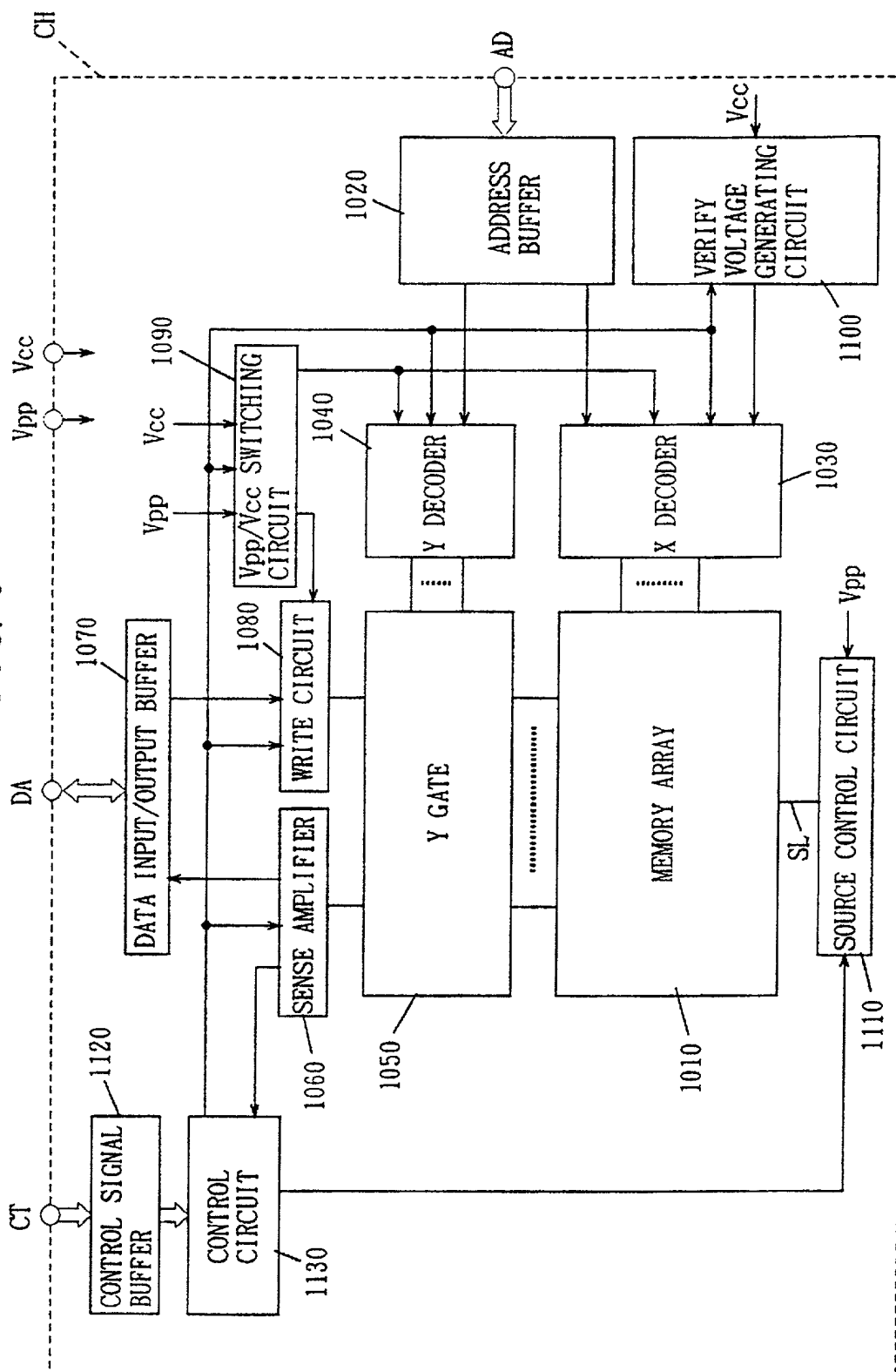


FIG. 6A

(PROGRAM)

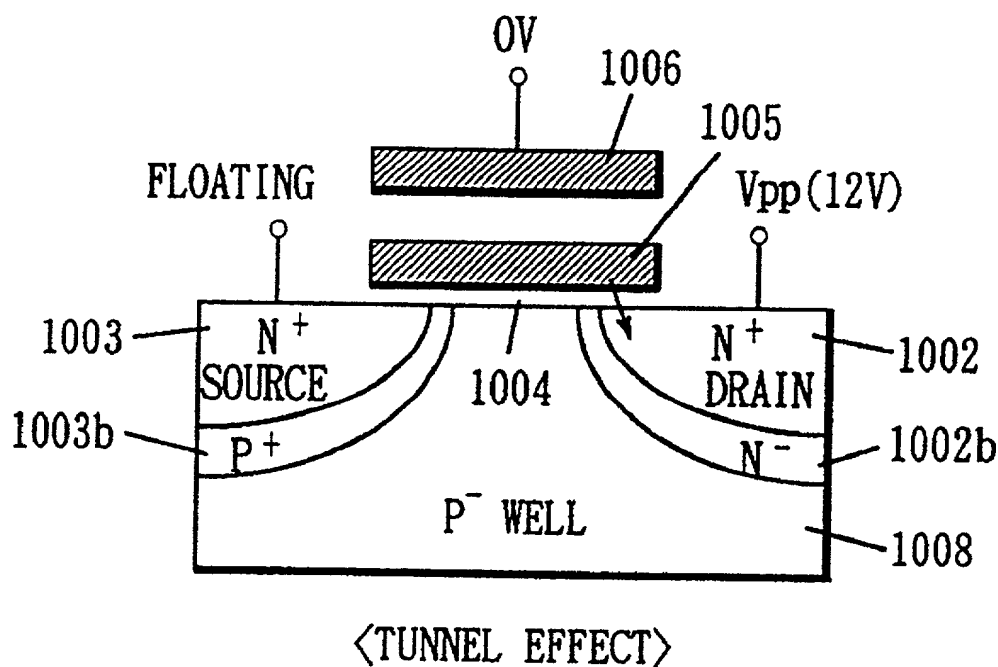
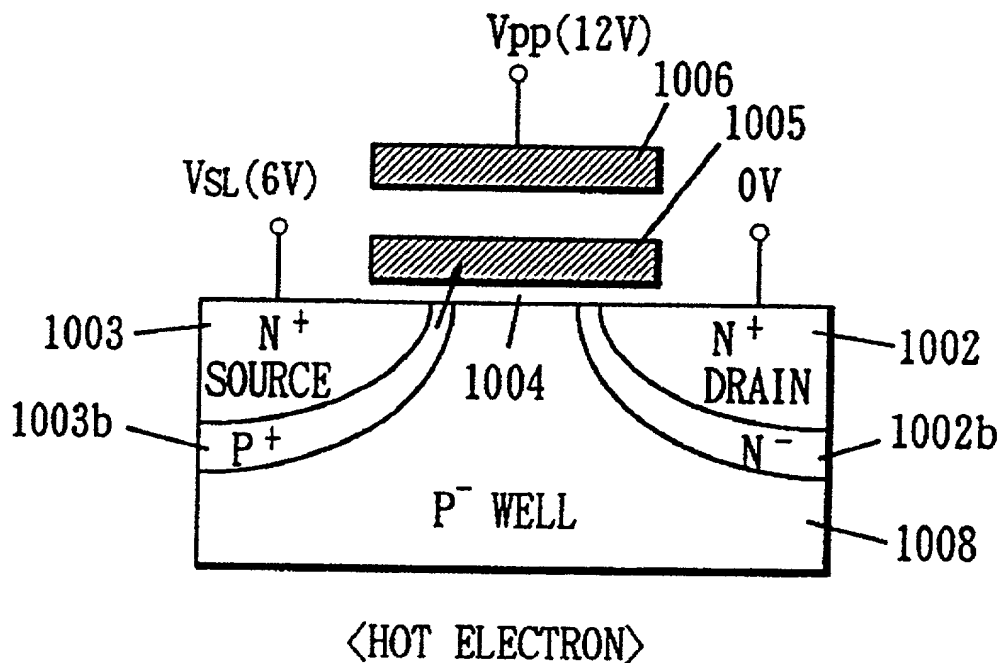


FIG. 6B

(ERASE)



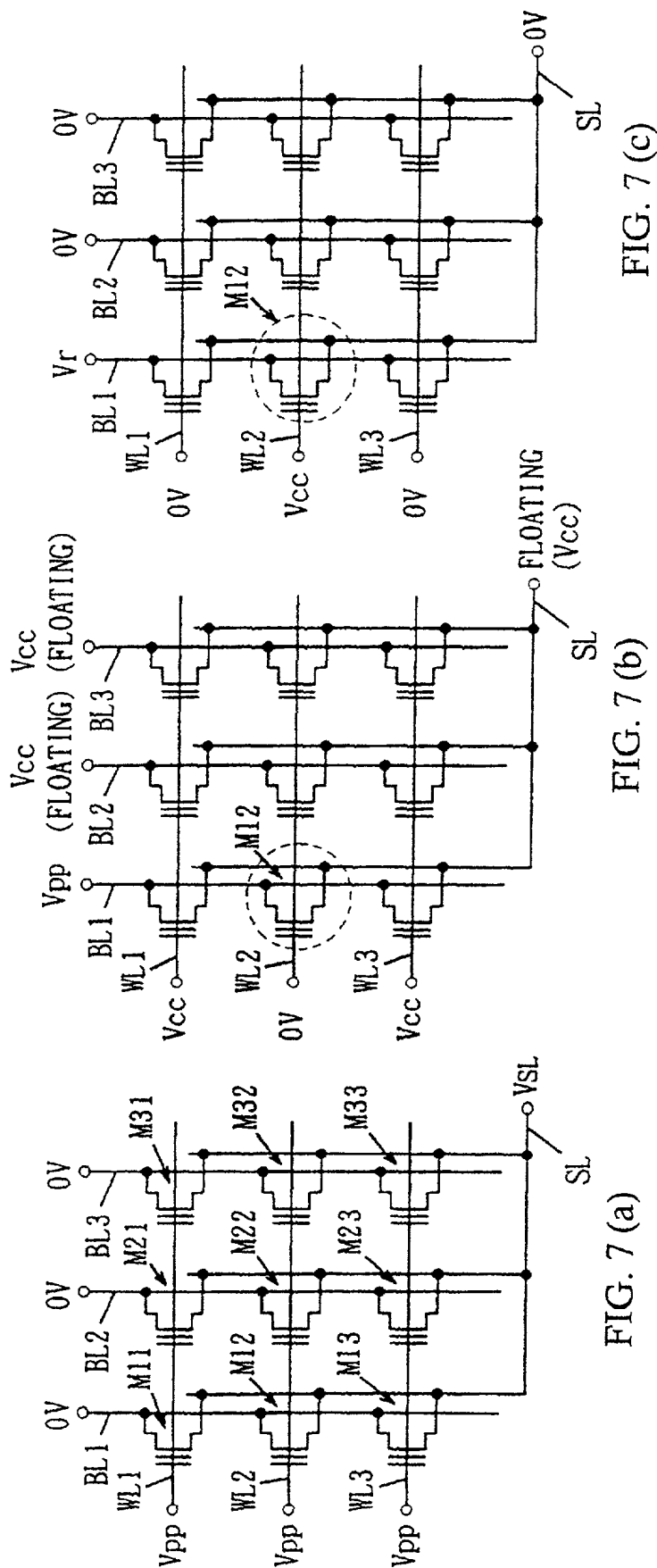
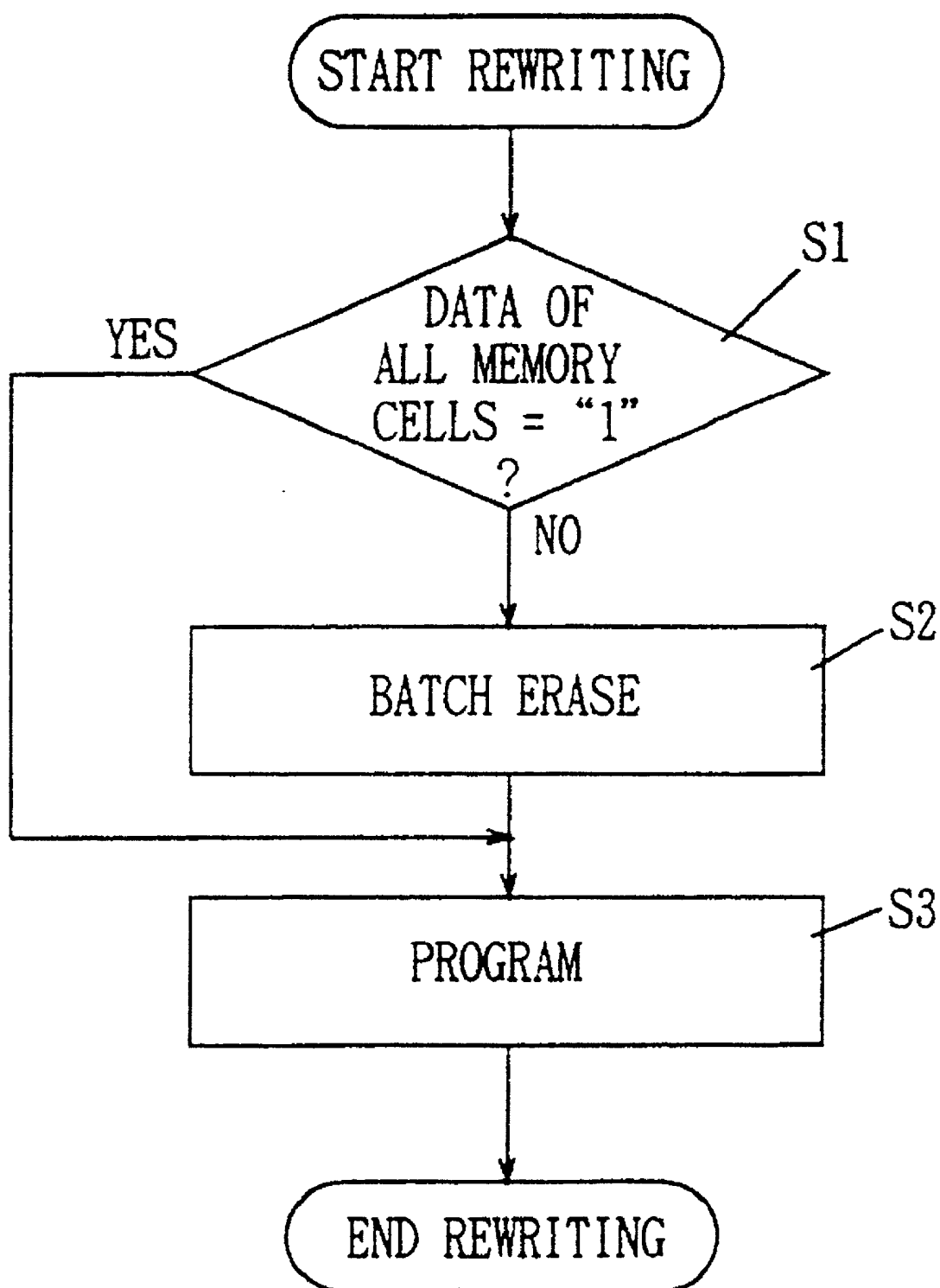


FIG. 7 (a)

FIG. 7 (b)

FIG. 7 (c)

FIG. 8



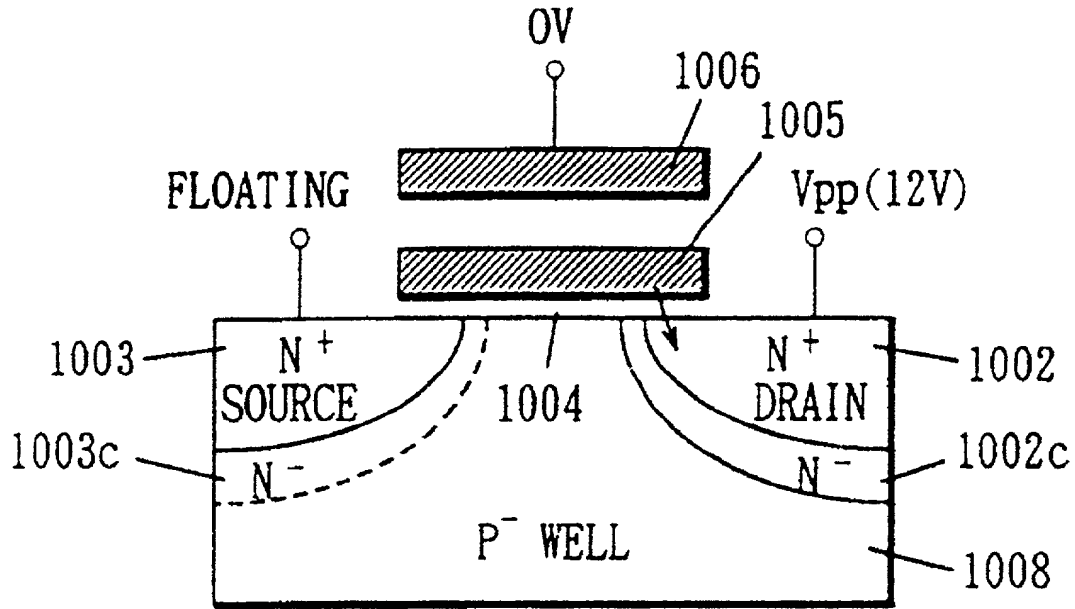


FIG. 9 (a)

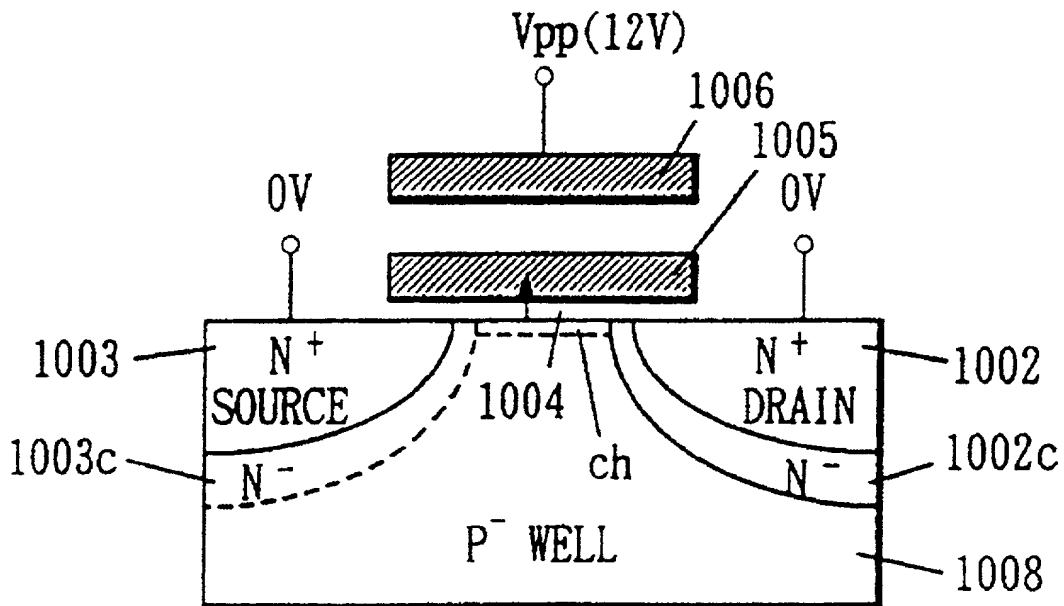


FIG. 9 (b)

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FOI/200504260



FIG. 10A

FIG. 10B

FIG. 10C

BATCH ERASE OPERATION

PROGRAM OPERATION

READ OPERATION

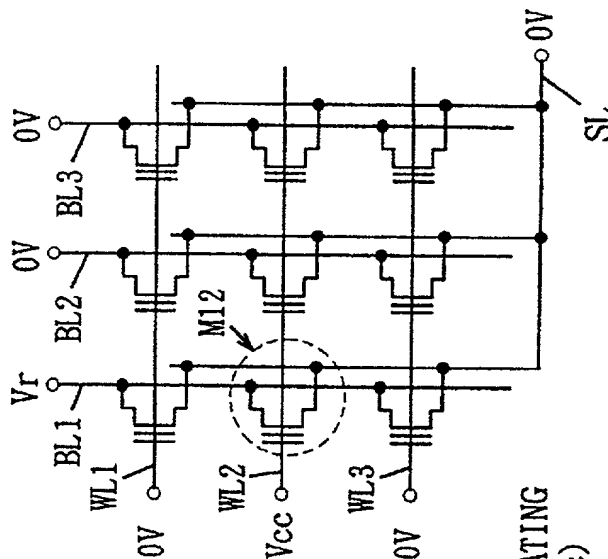
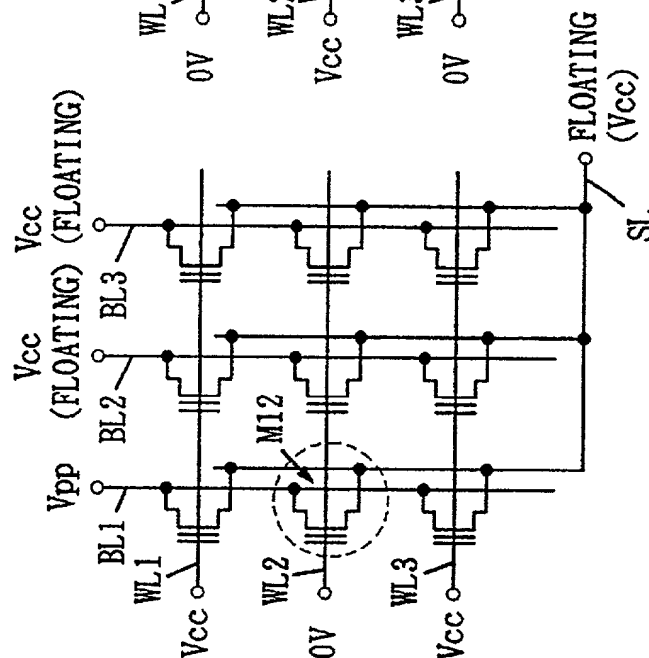
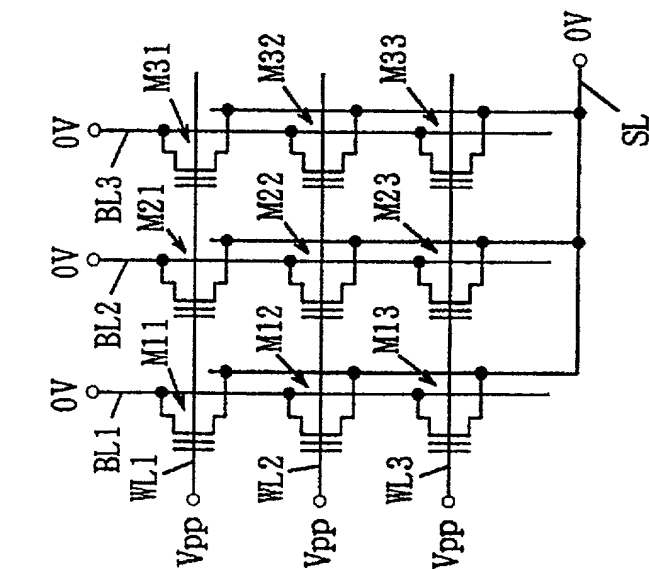


FIG. 11

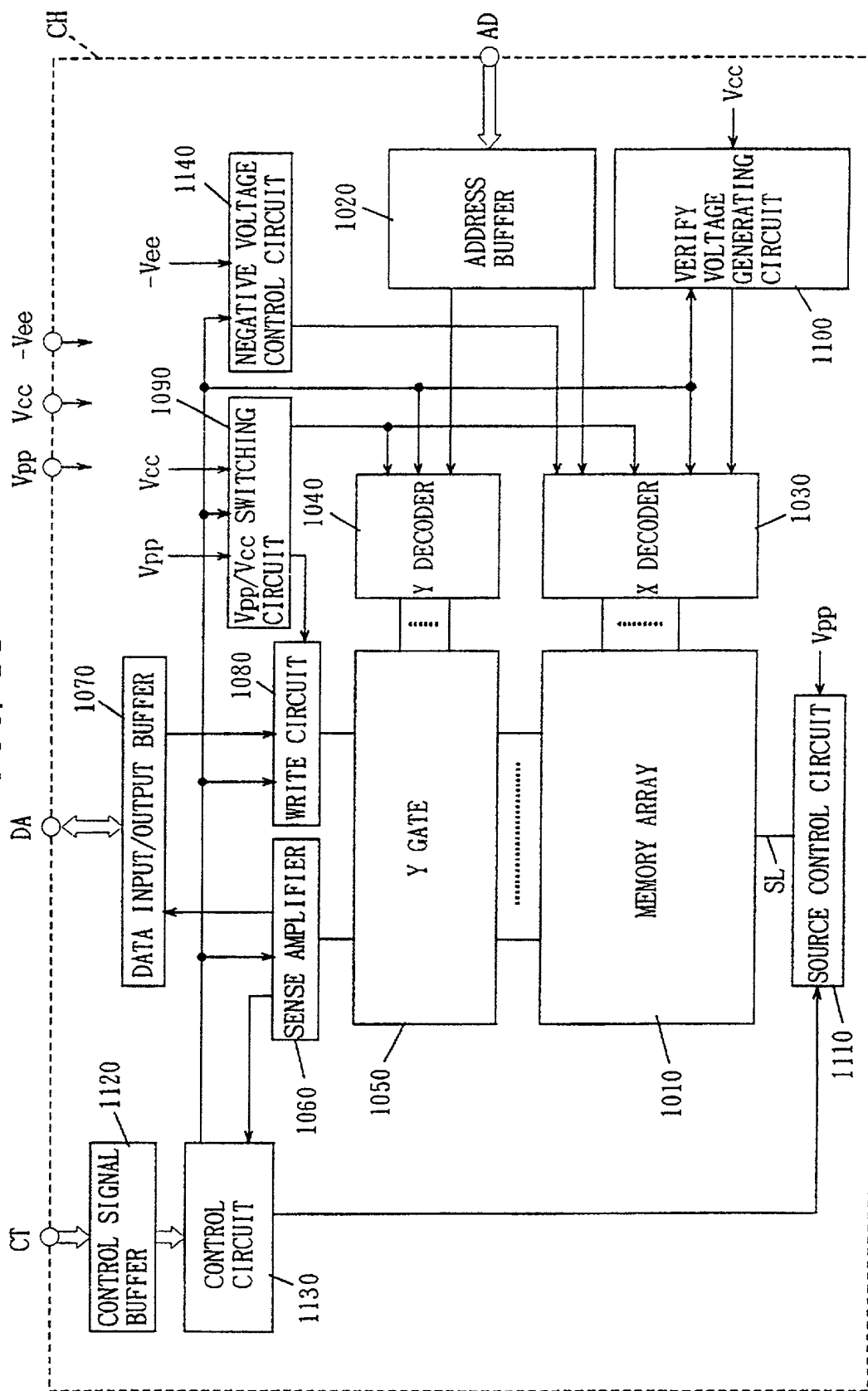


FIG. 12

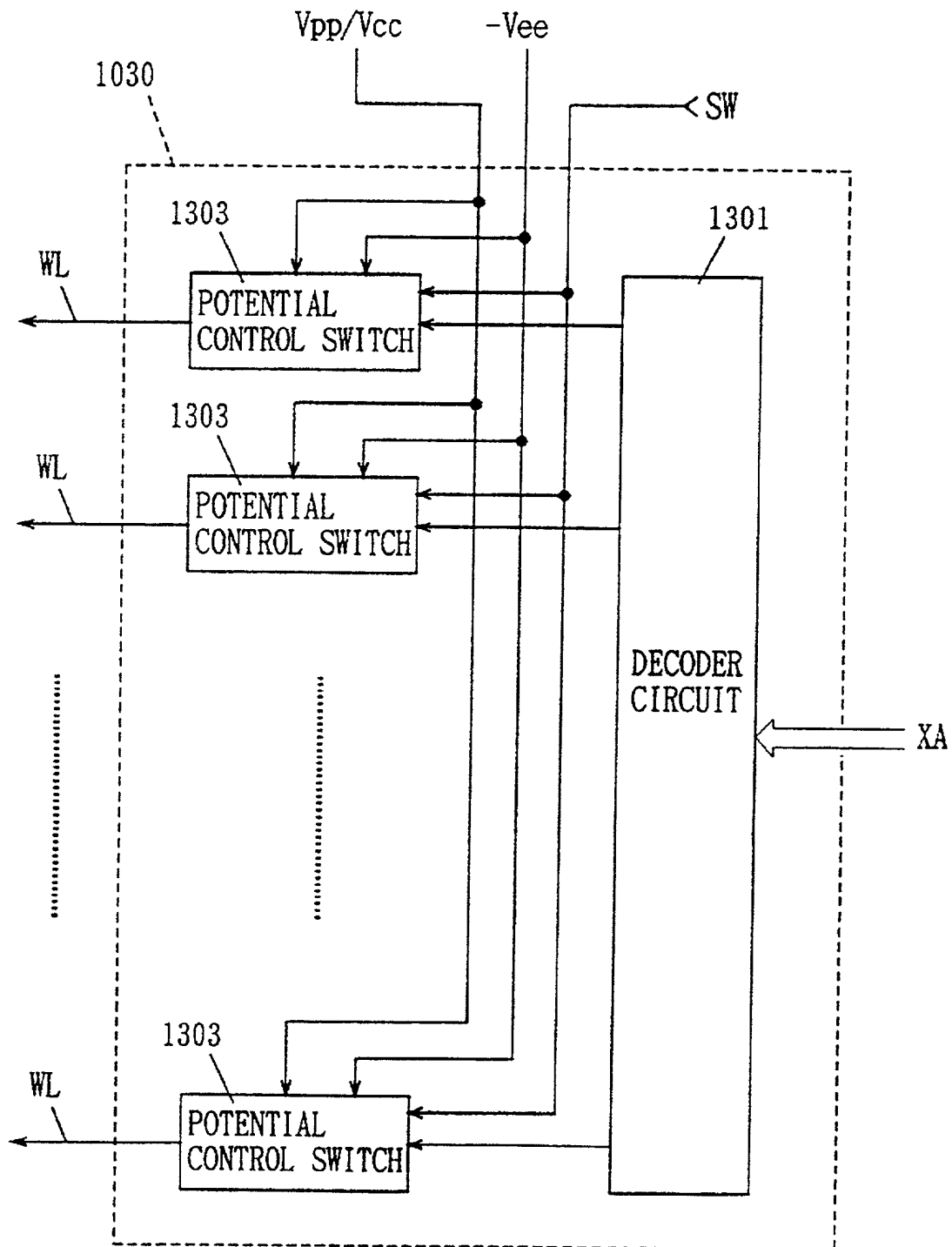


FIG. 13A

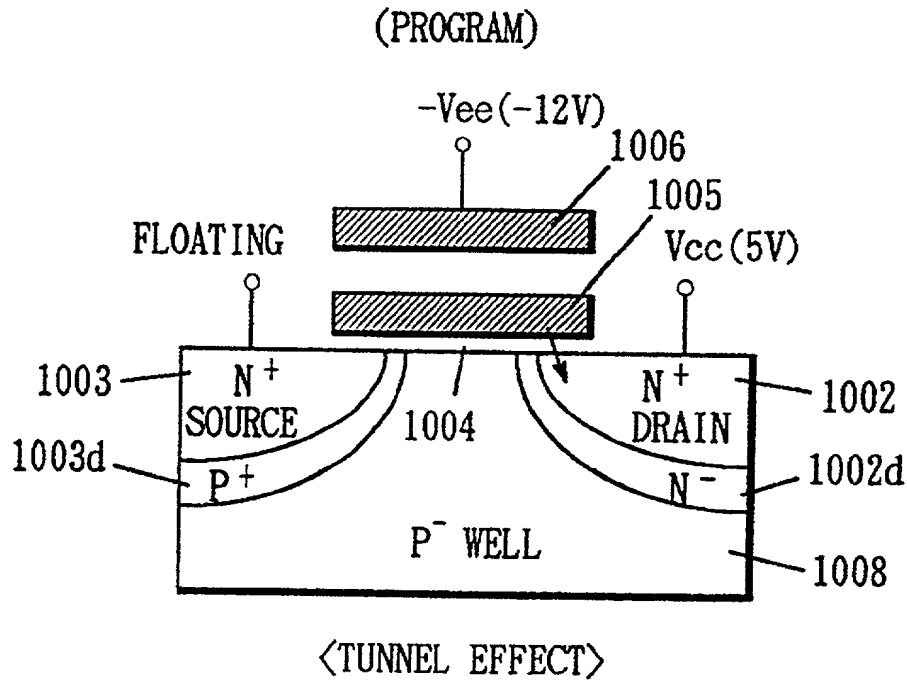


FIG. 13B

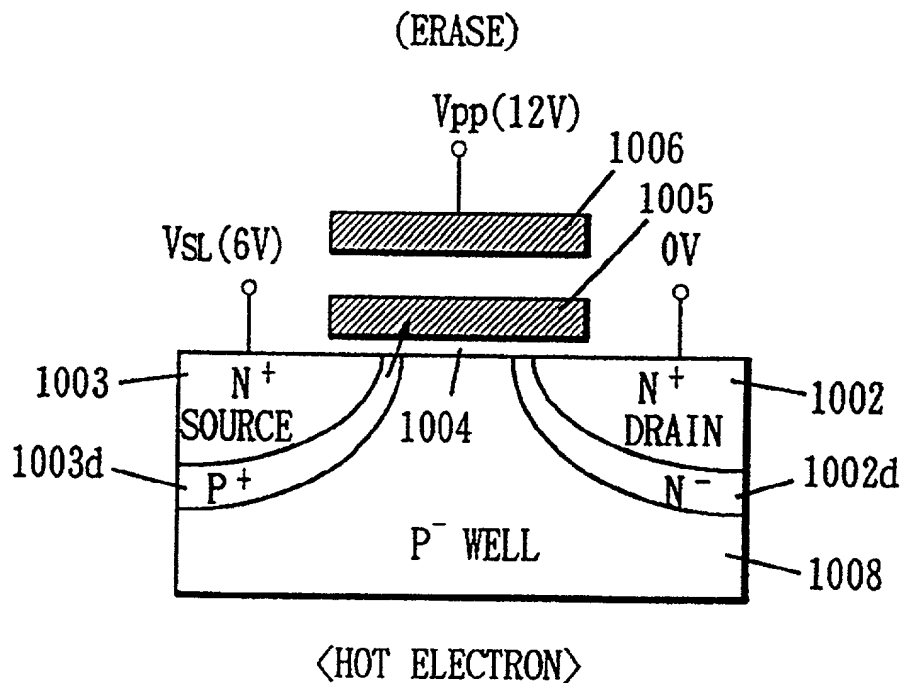


FIG. 14A

BATCH ERASE OPERATION

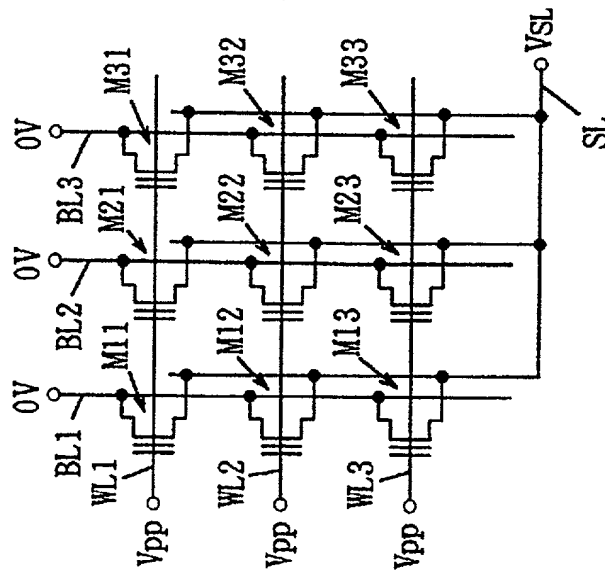


FIG. 14B

PROGRAM OPERATION

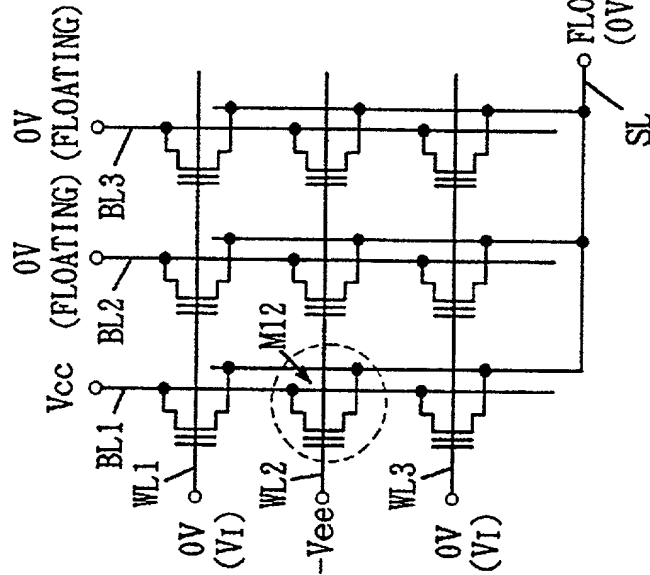


FIG. 14C

READ OPERATION

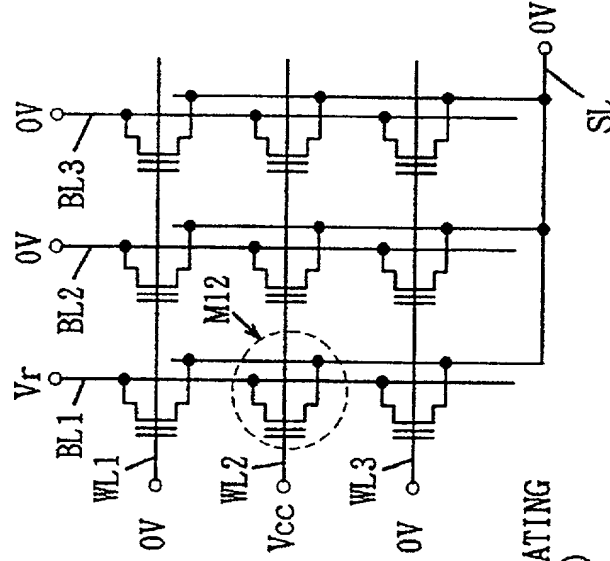


FIG. 15A

FIG. 15B

FIG. 15C

BATCH PAGE ERASE OPERATION

PROGRAM OPERATION

READ OPERATION

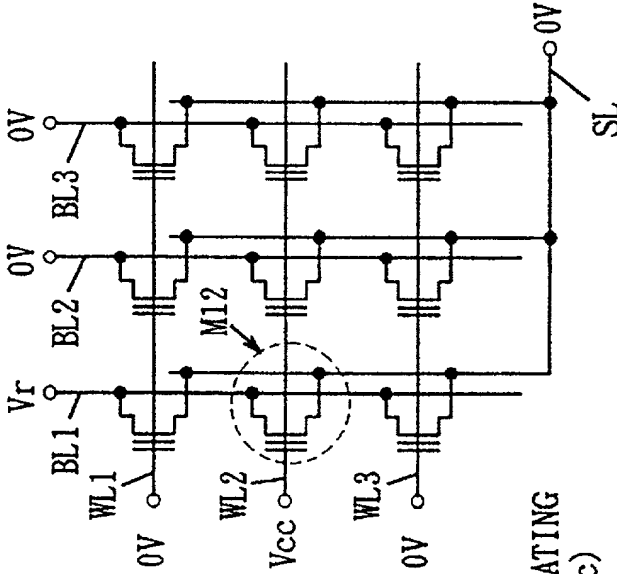
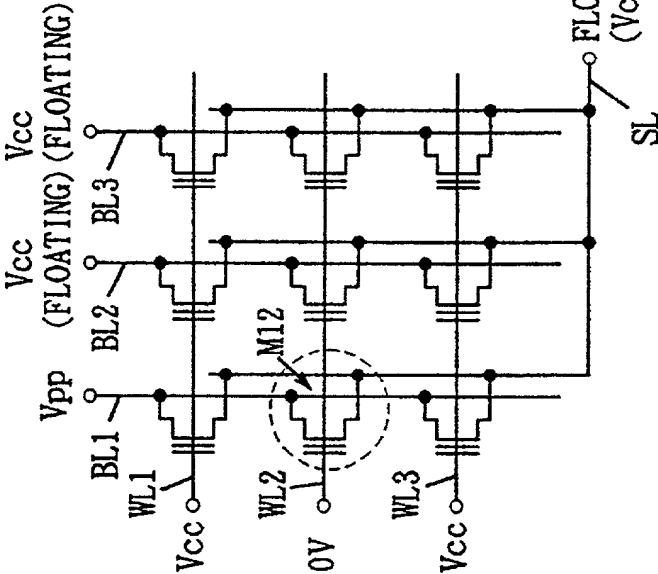
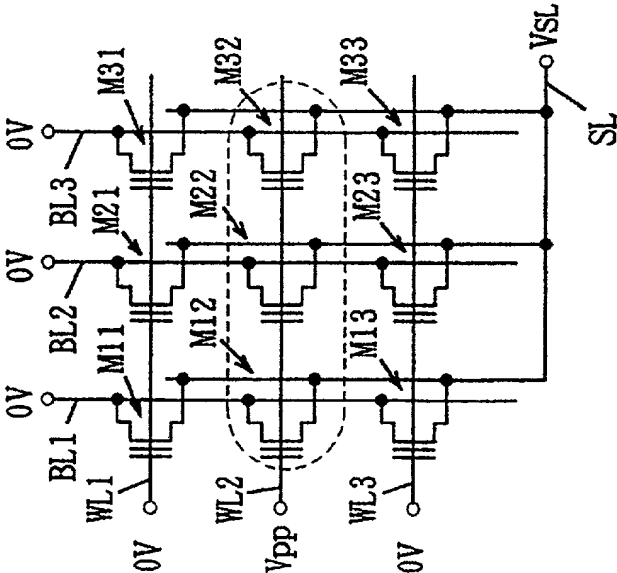


FIG. 16

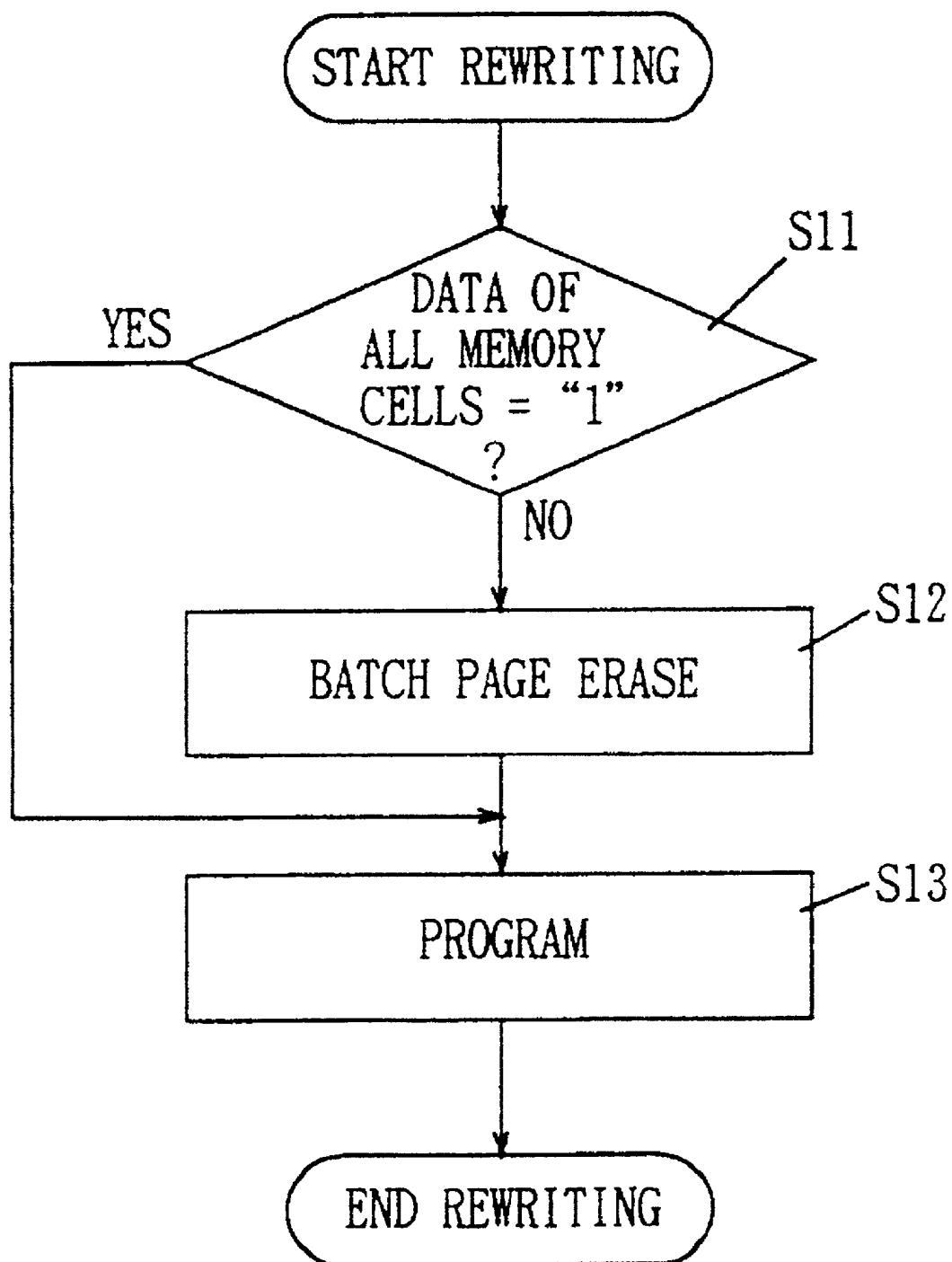


FIG. 17A

BATCH PAGE ERASE OPERATION

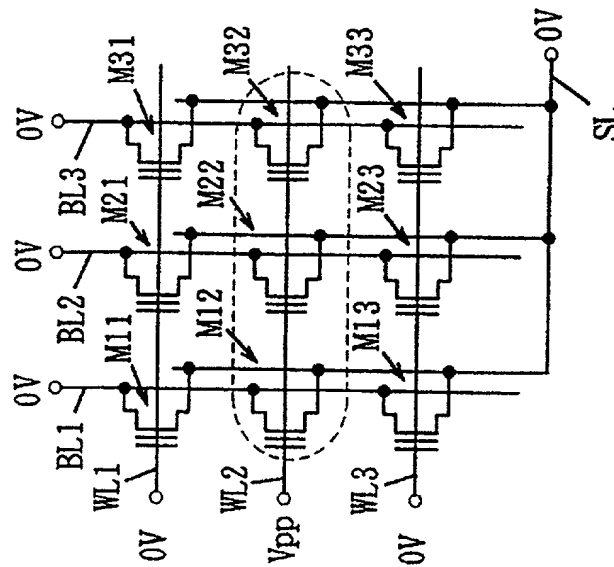


FIG. 17B

PROGRAM OPERATION

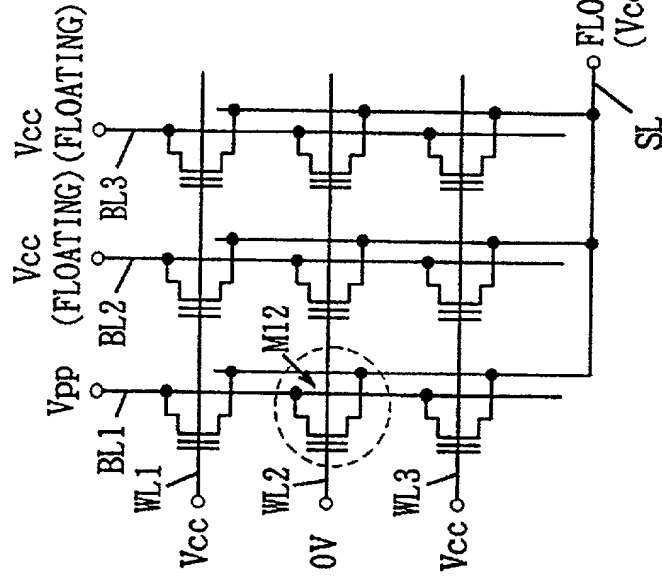


FIG. 17C

READ OPERATION

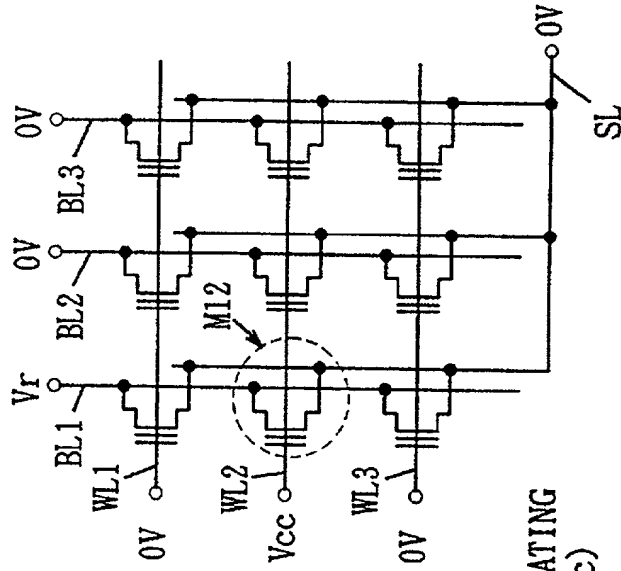




FIG. 18

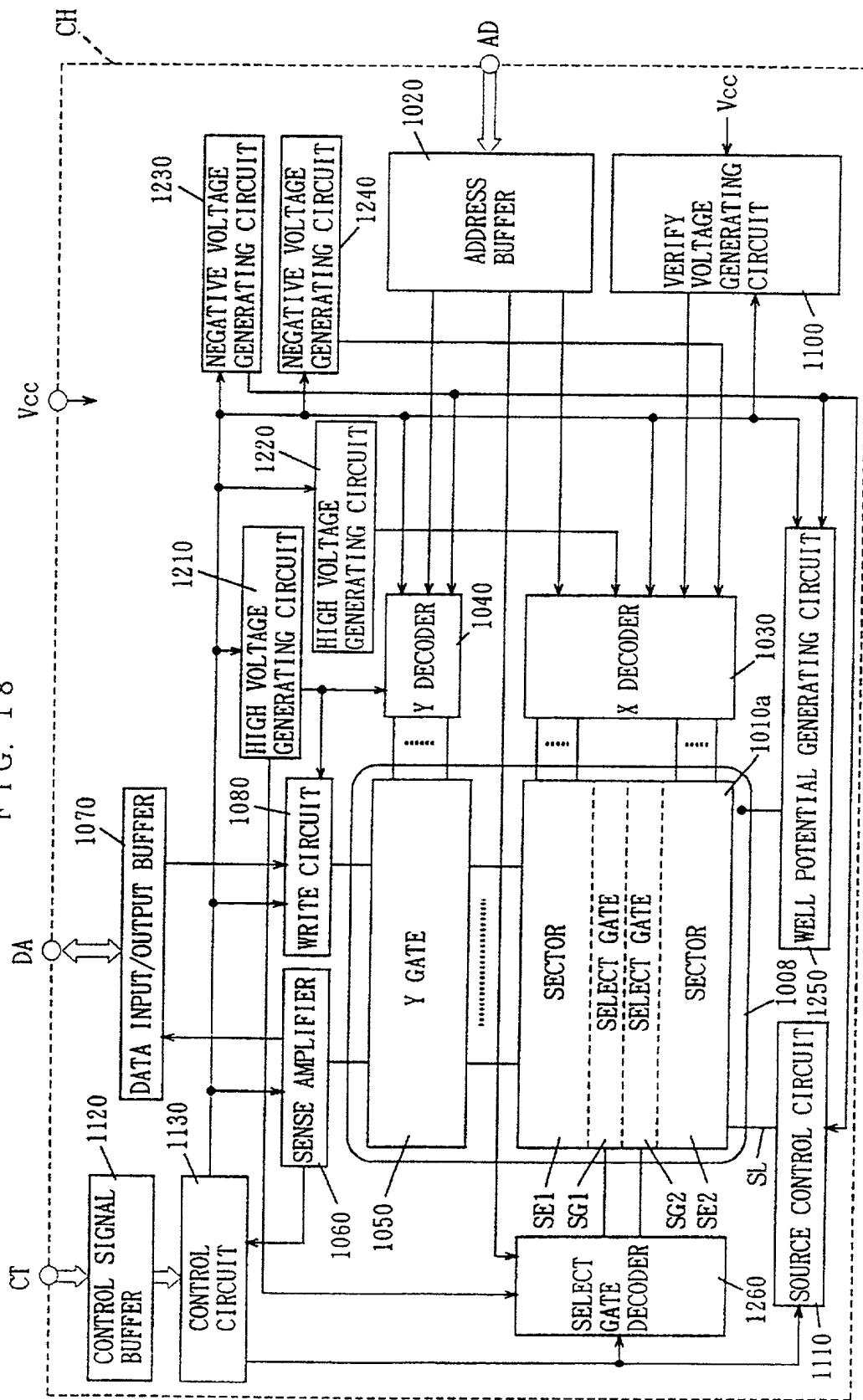


FIG. 19

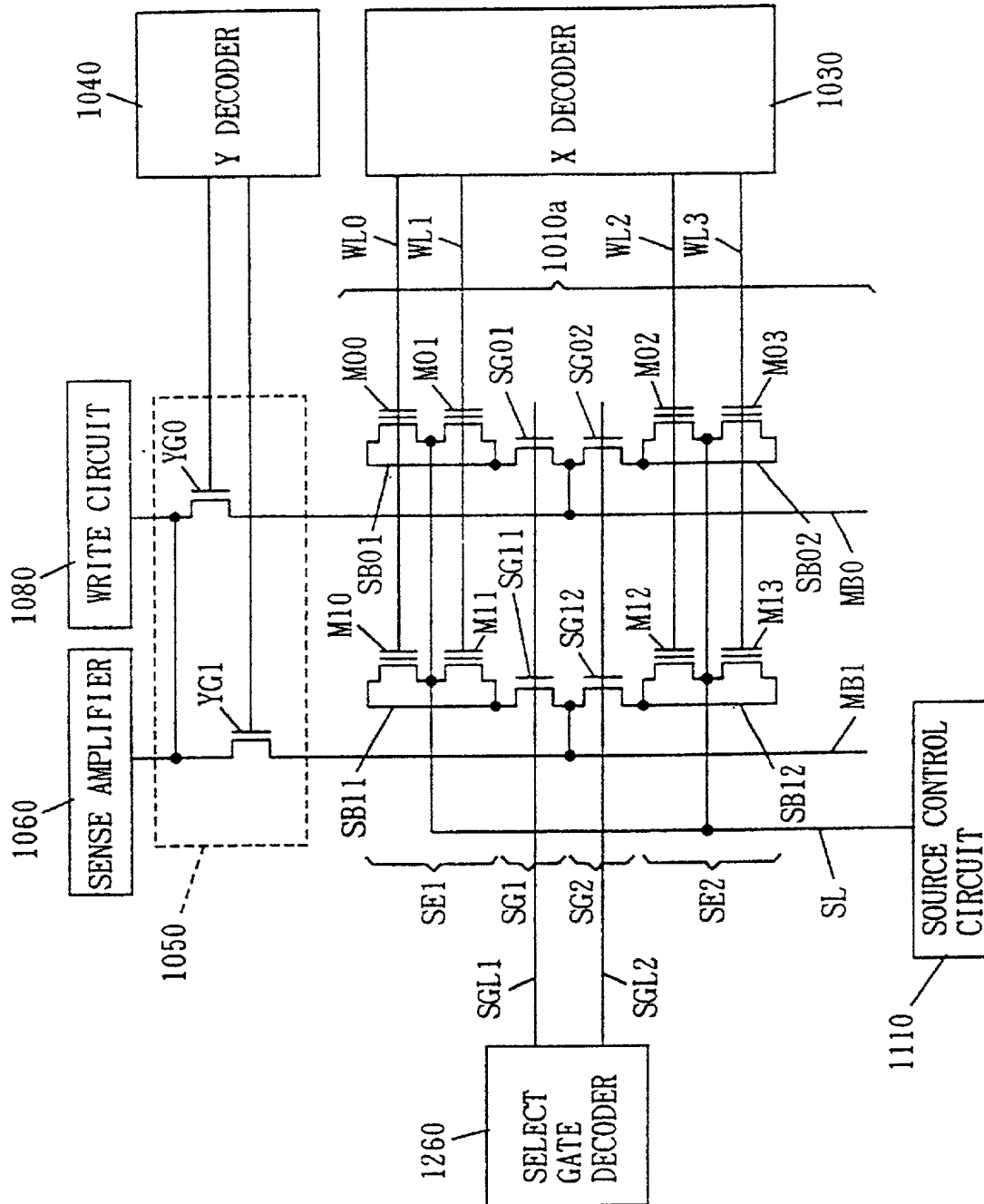


FIG. 20A

(PROGRAM)

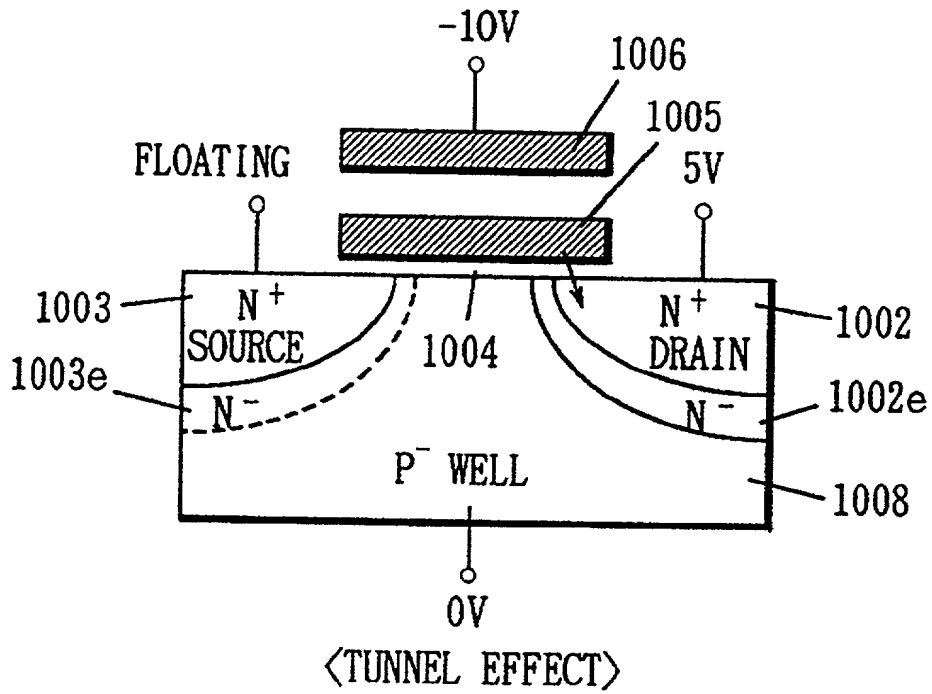
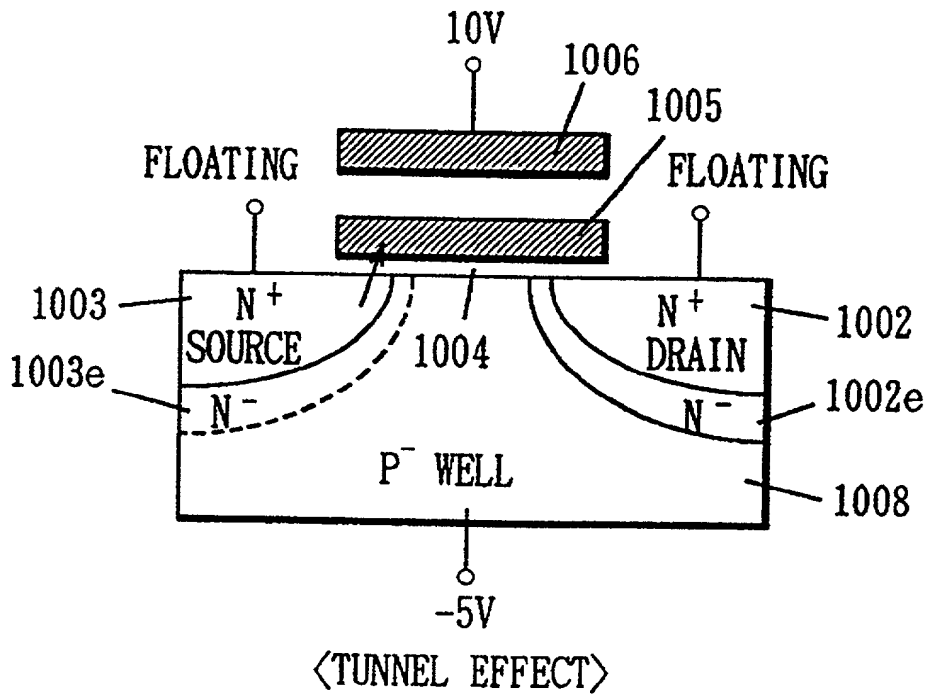


FIG. 20B

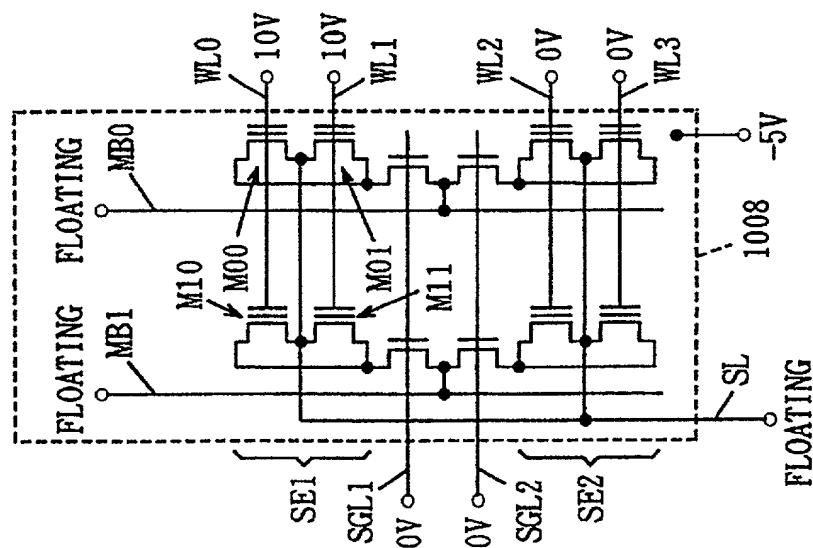
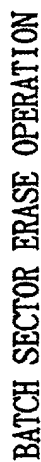
(ERASE)



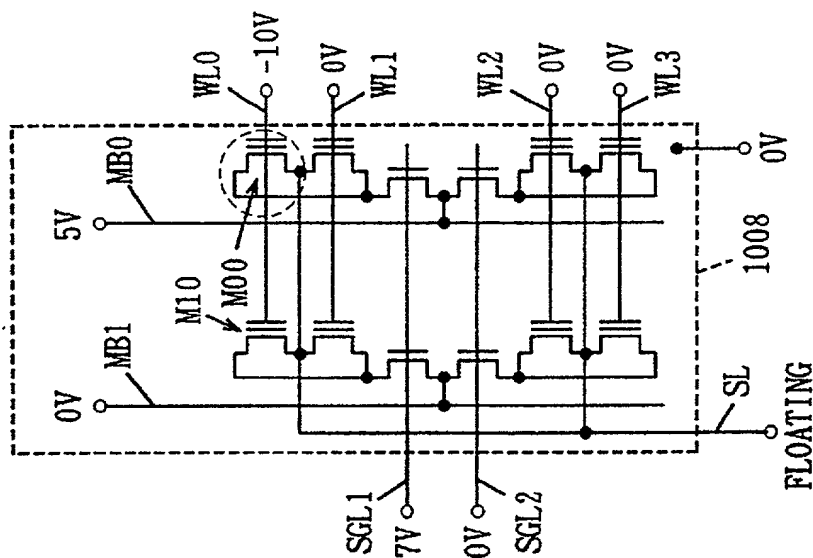
09643165-043704

**FIG. 21B**

FIG. 21C



# PROGRAM OPERATION



## READ OPERATION

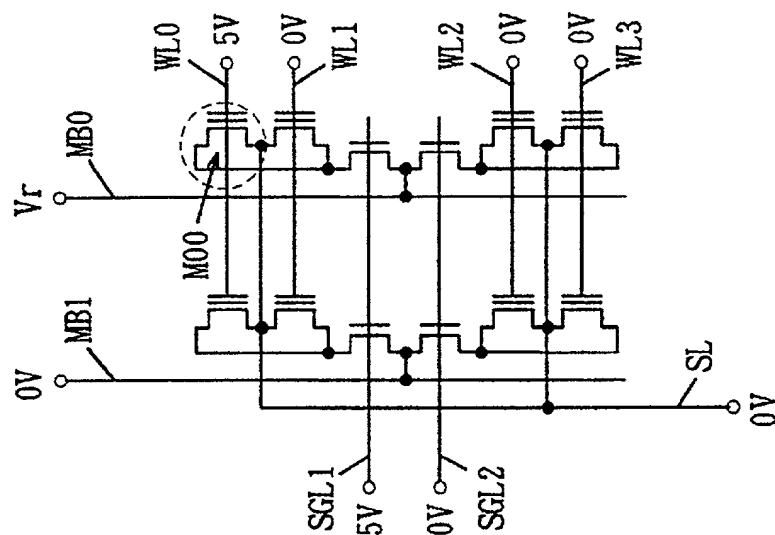


FIG. 22

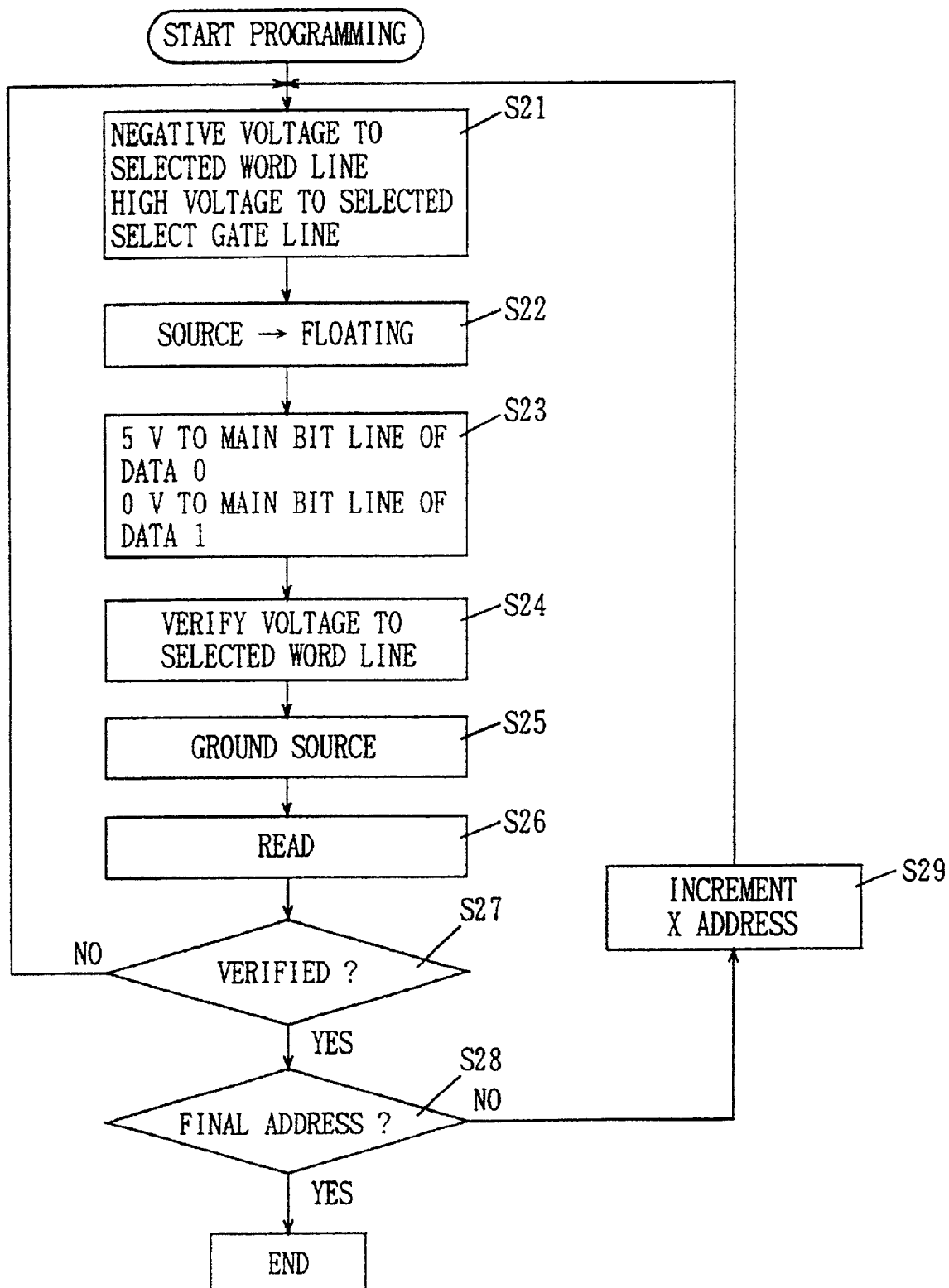


FIG. 23

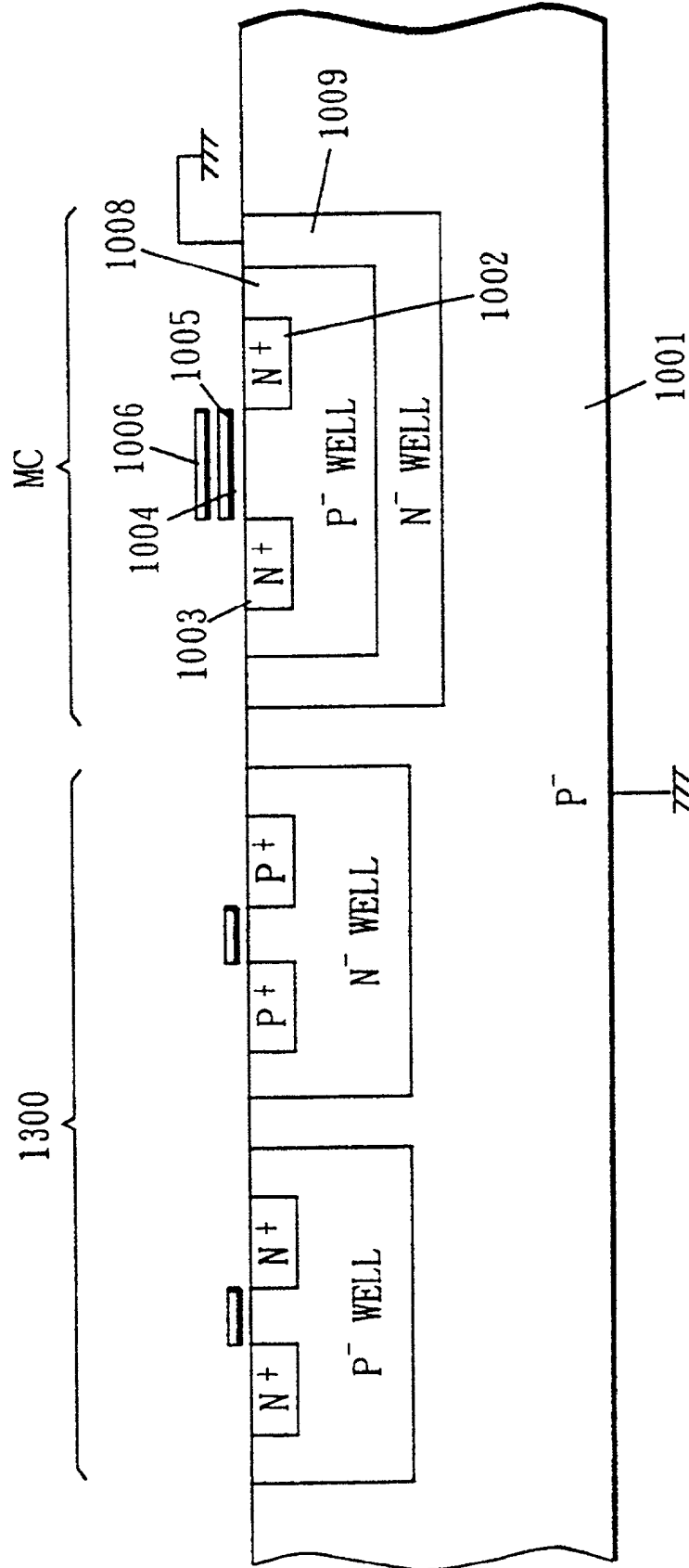


FIG. 24

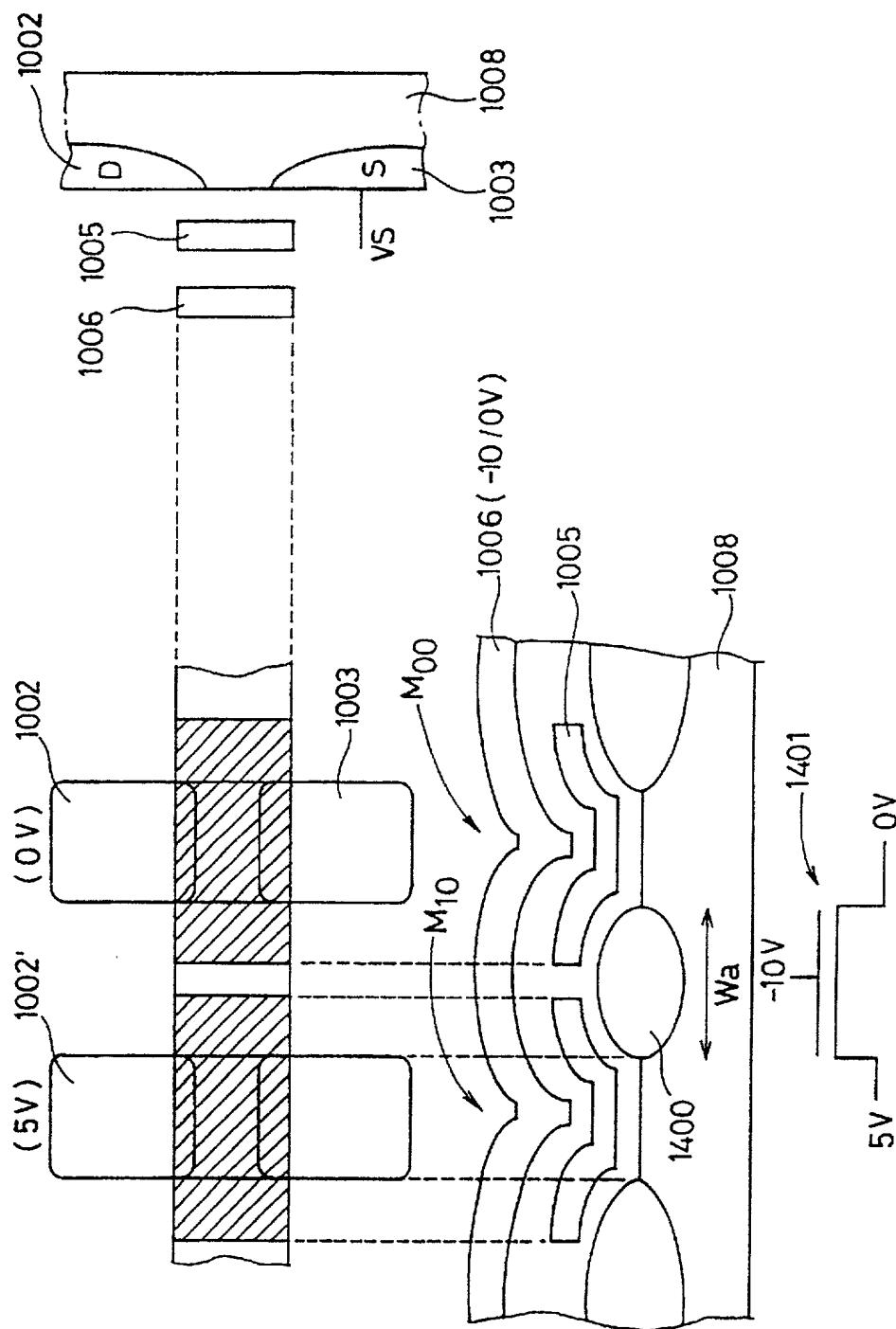


FIG. 25

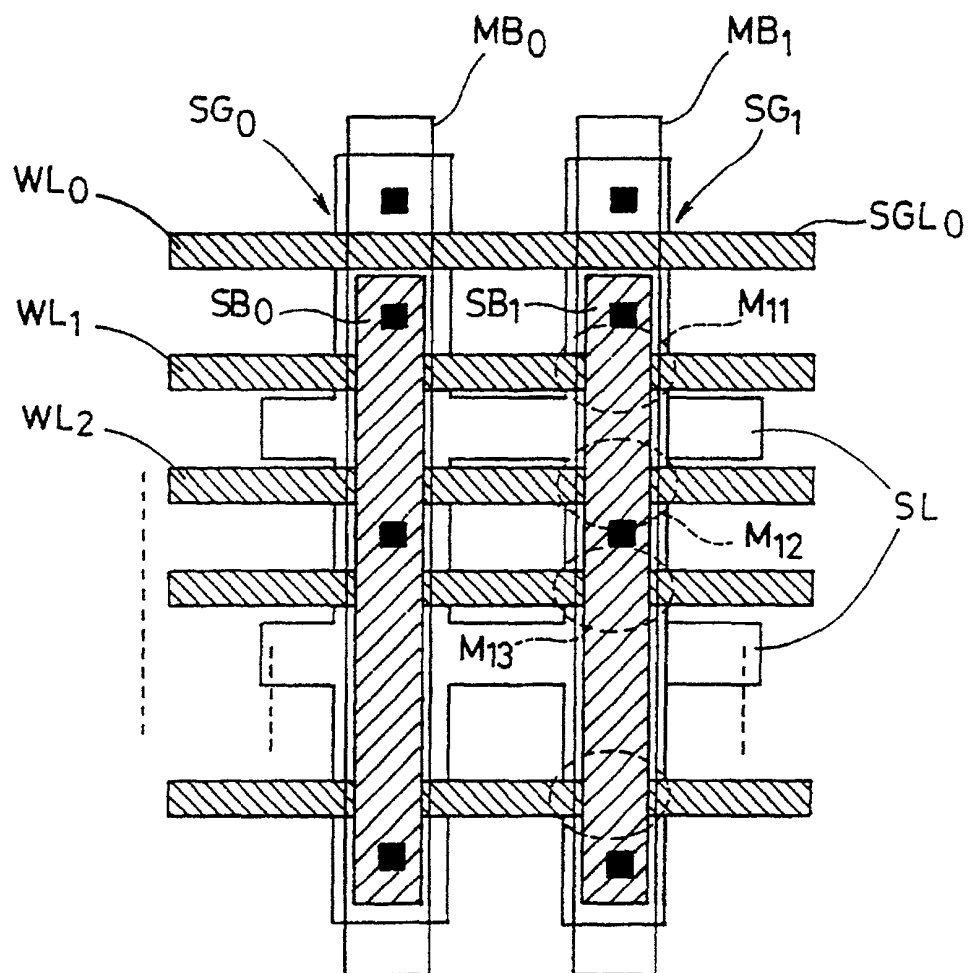




FIG. 26A

PROGRAM

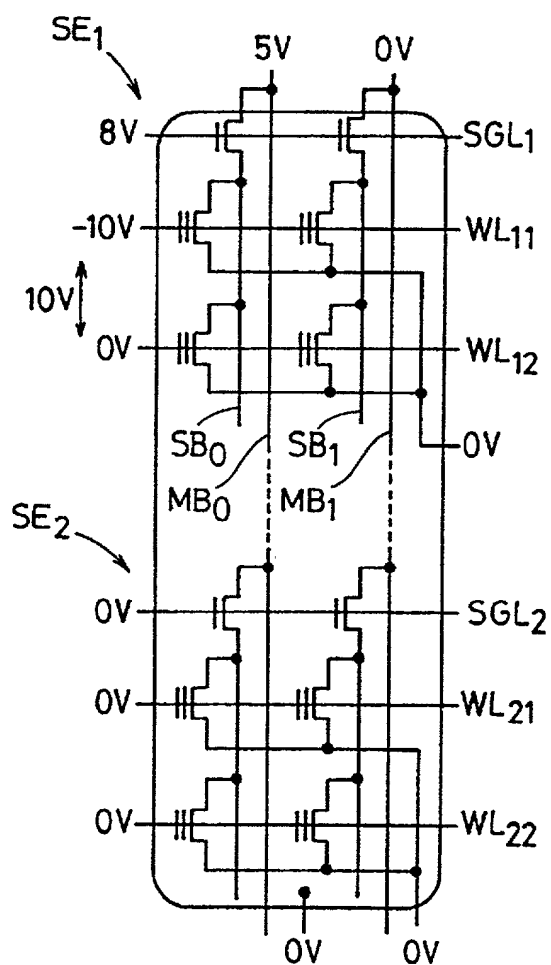


FIG. 26B

ERASE

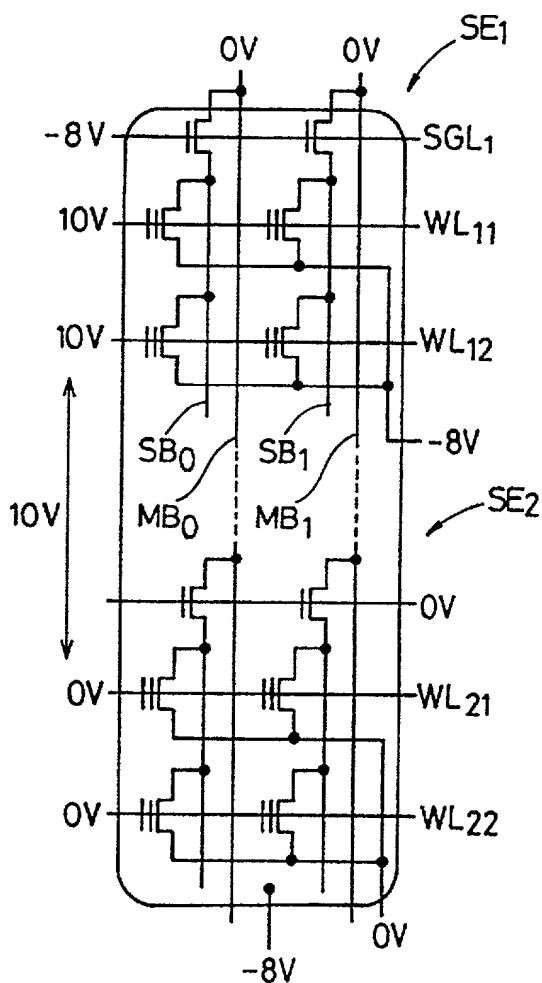


FIG. 27A

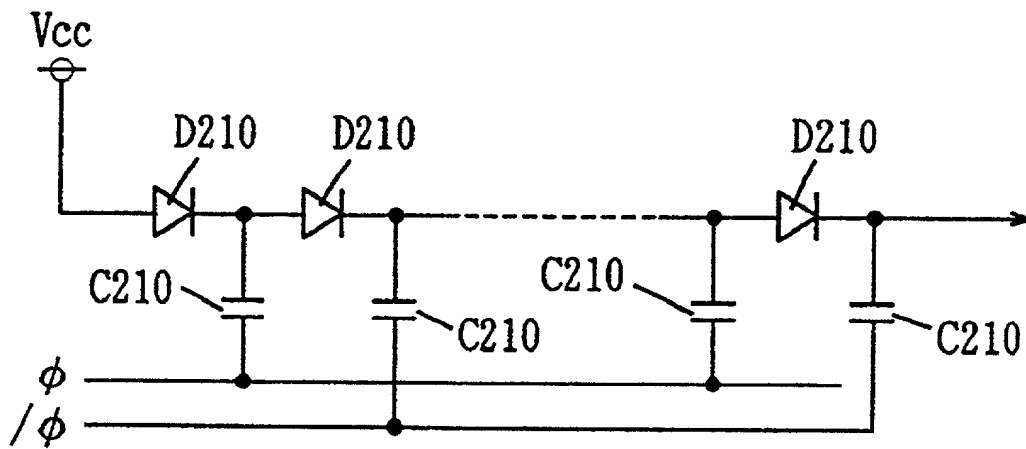


FIG. 27B

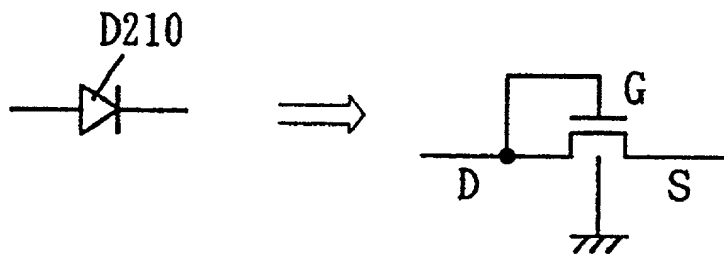


FIG. 28

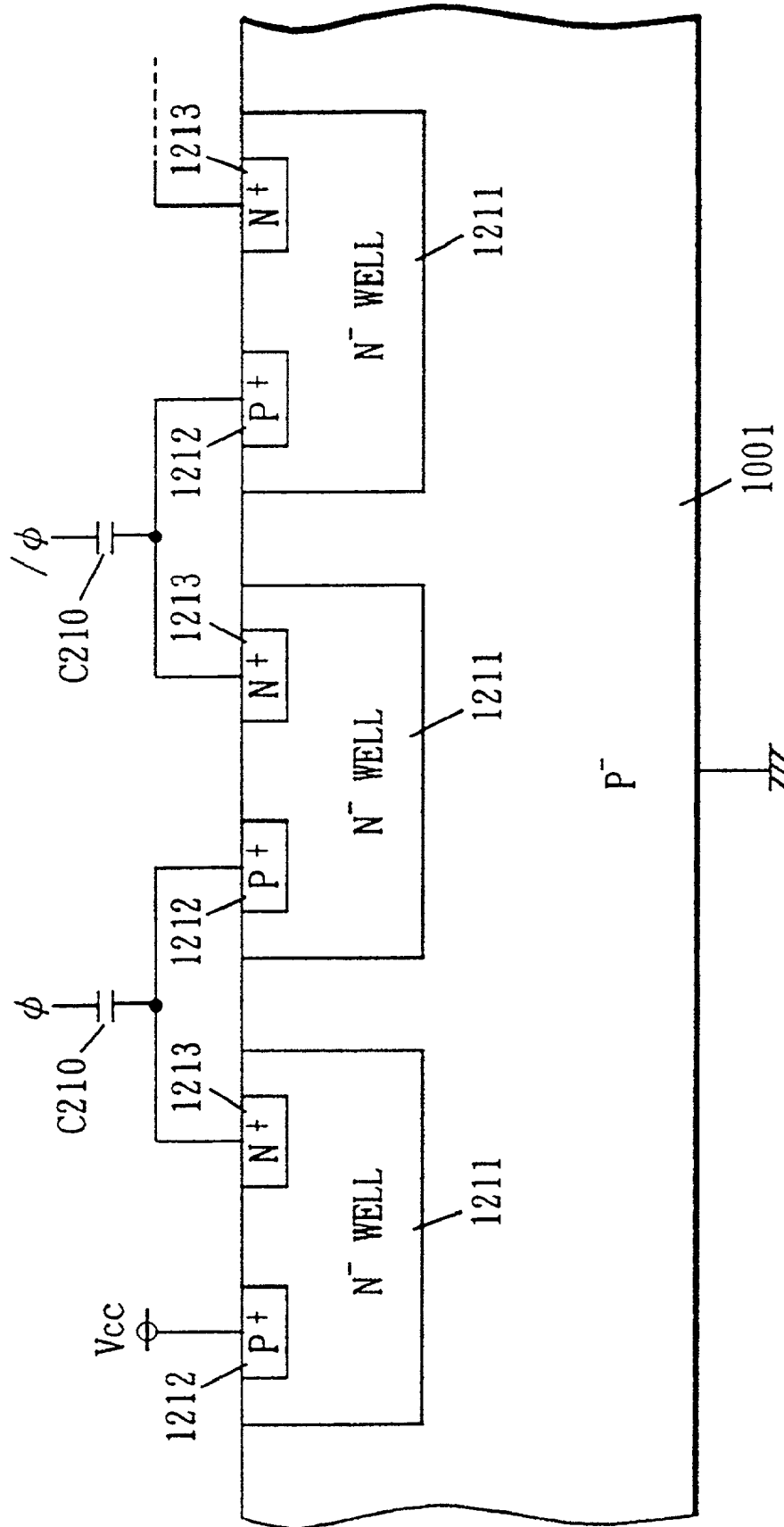


FIG. 29

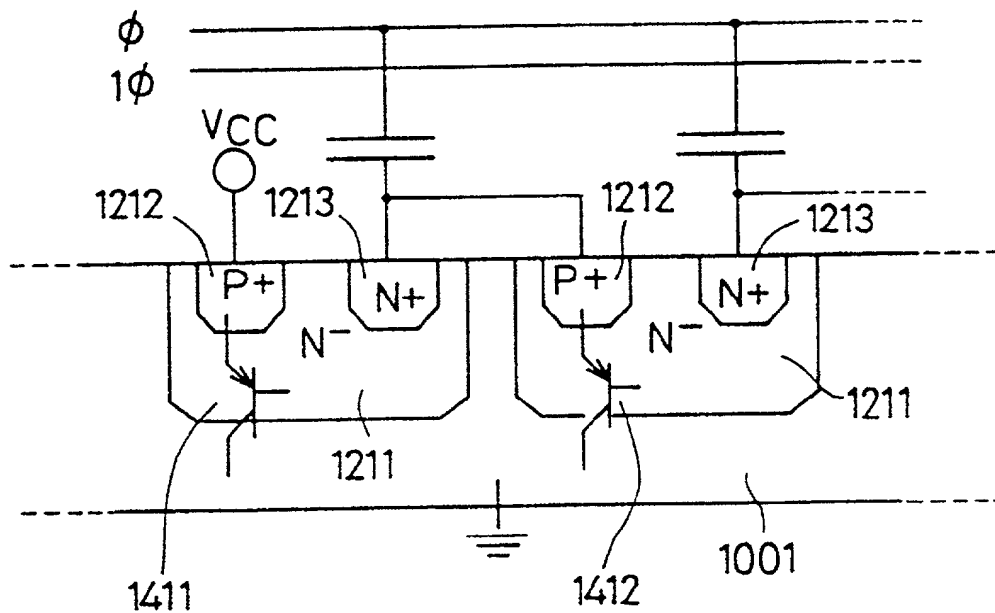


FIG. 30

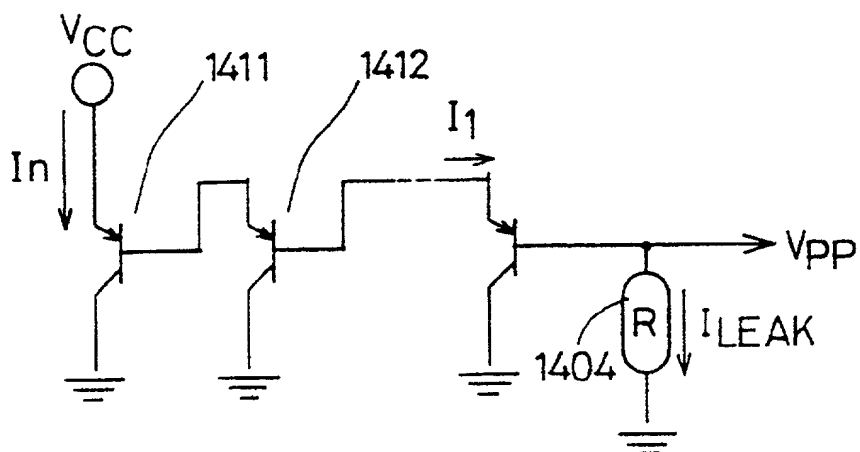


FIG. 31

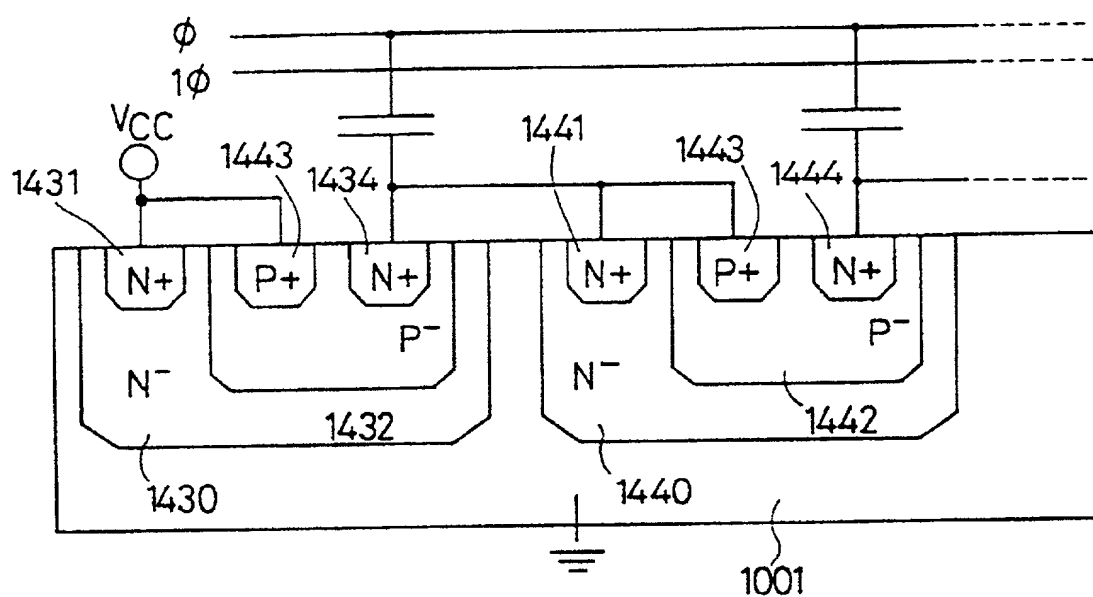
[illegible]

FIG. 32A

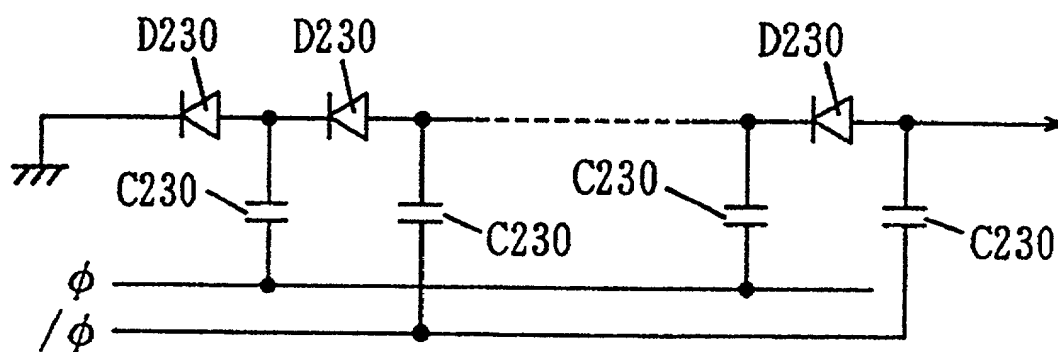


FIG. 32B

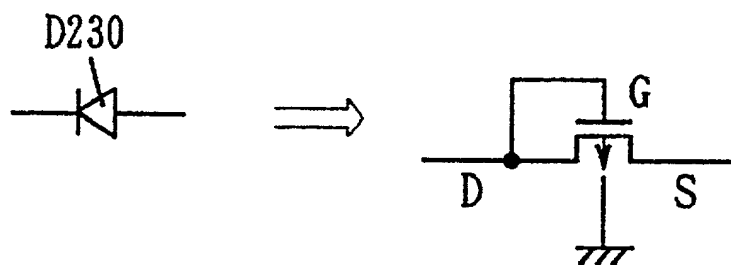


FIG. 33

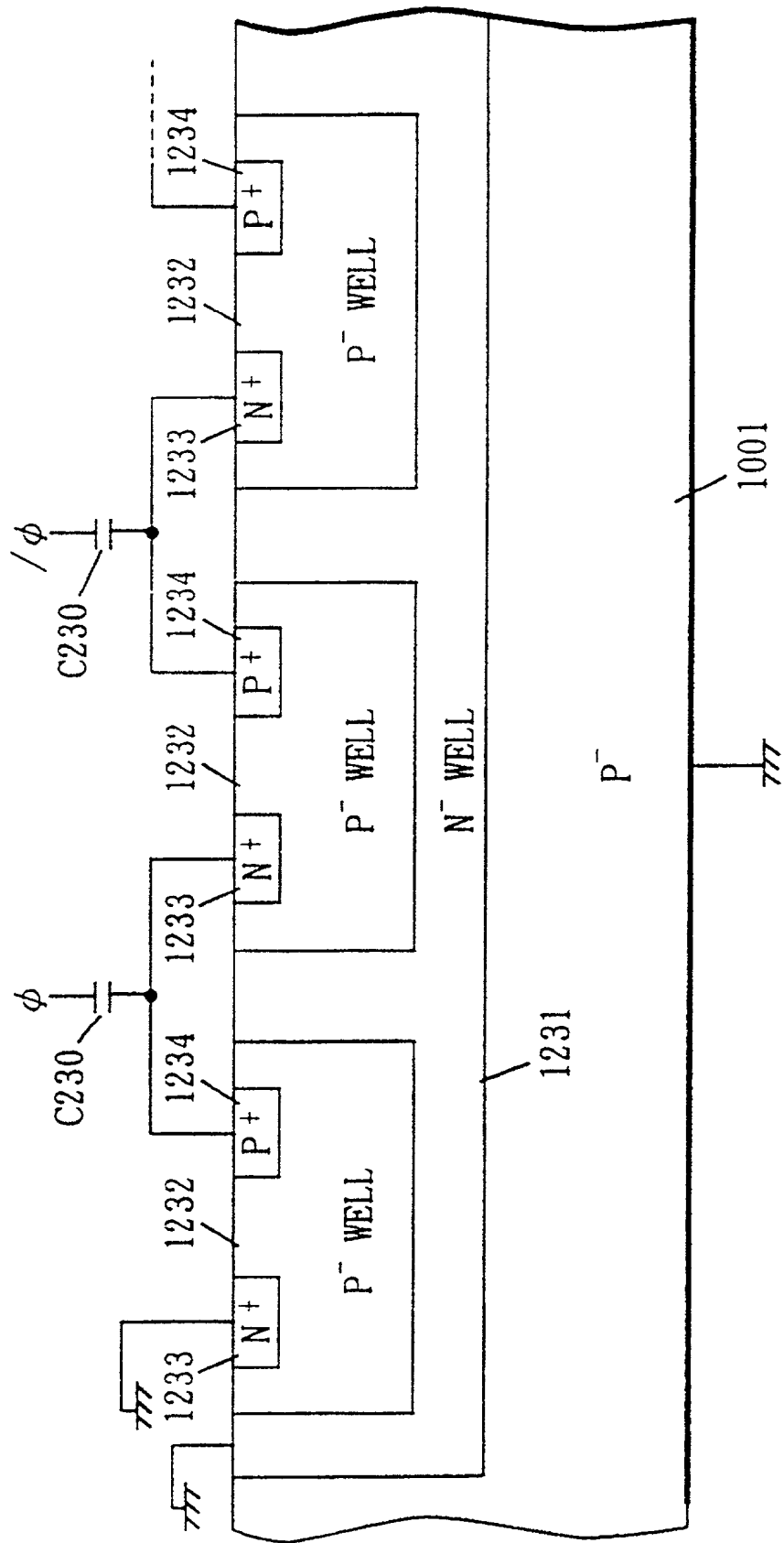
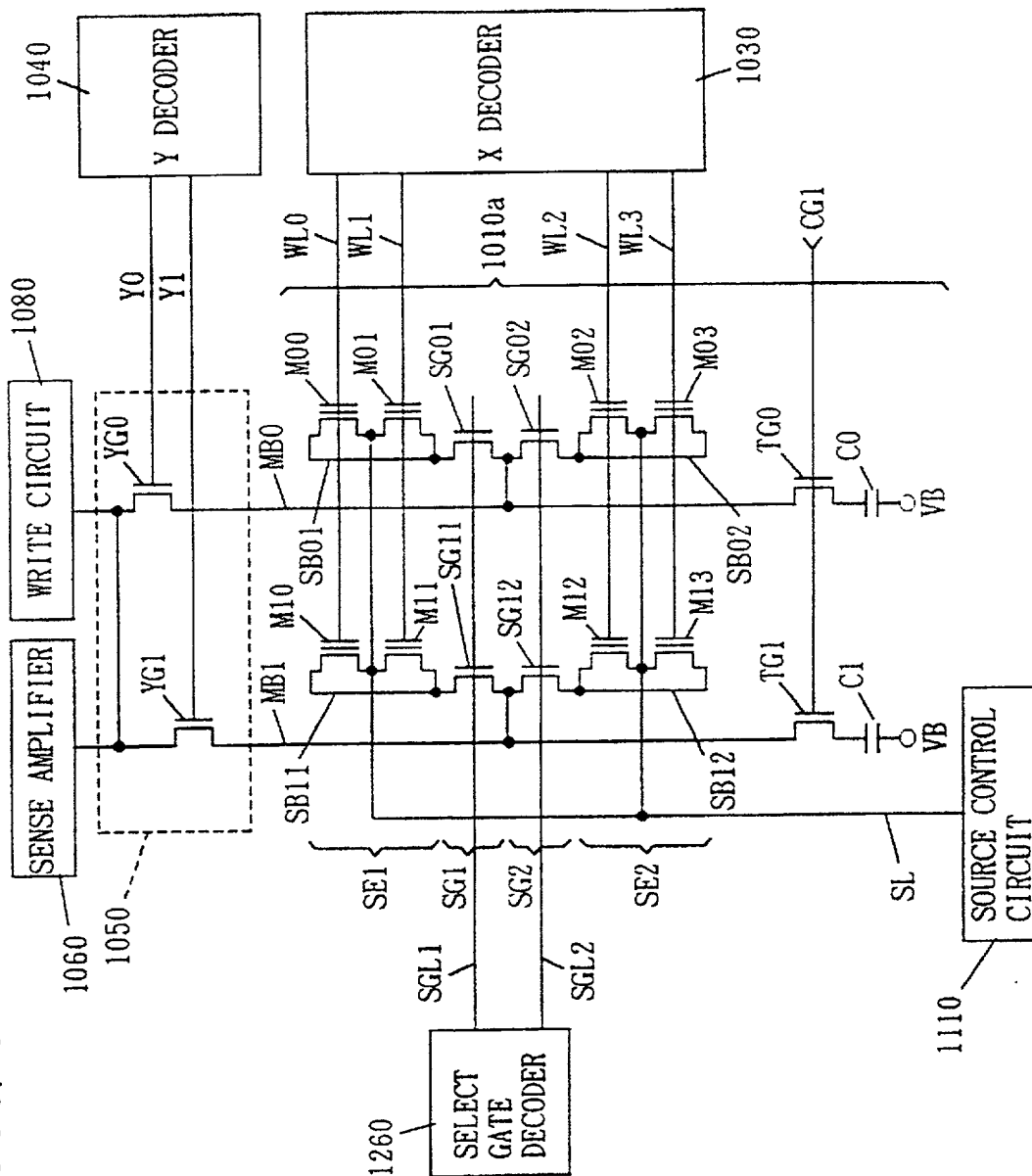


FIG. 34





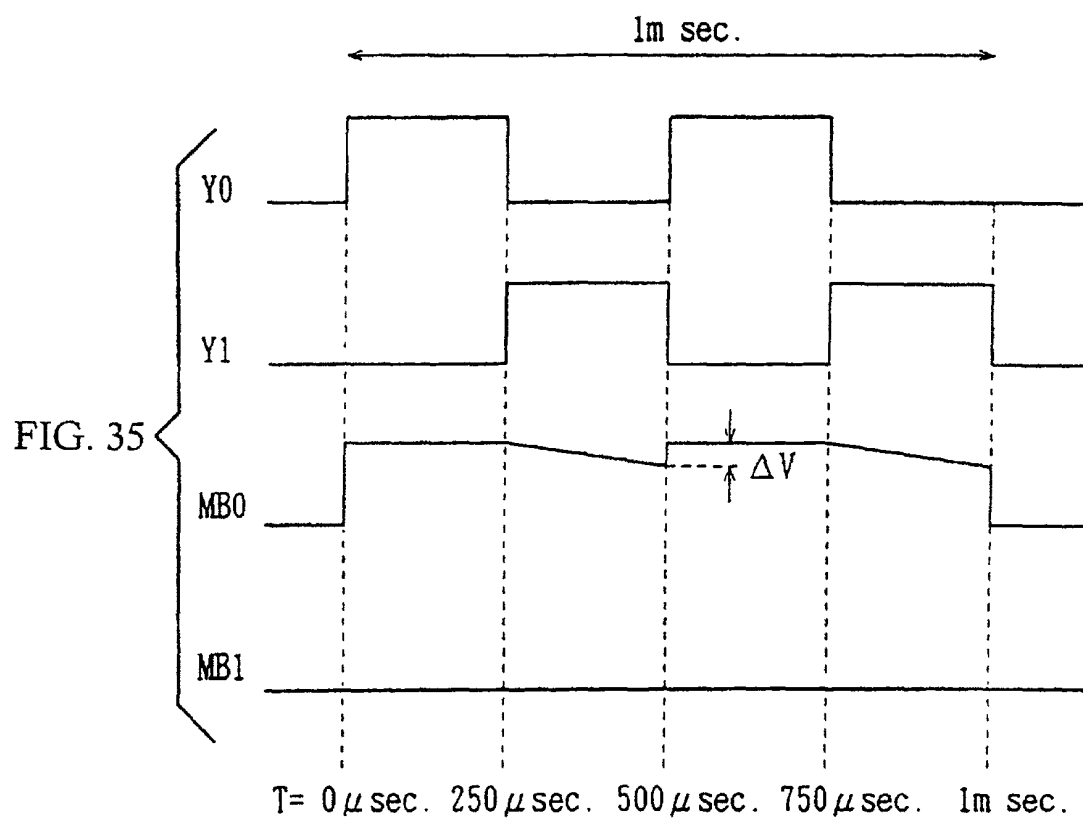


FIG. 36

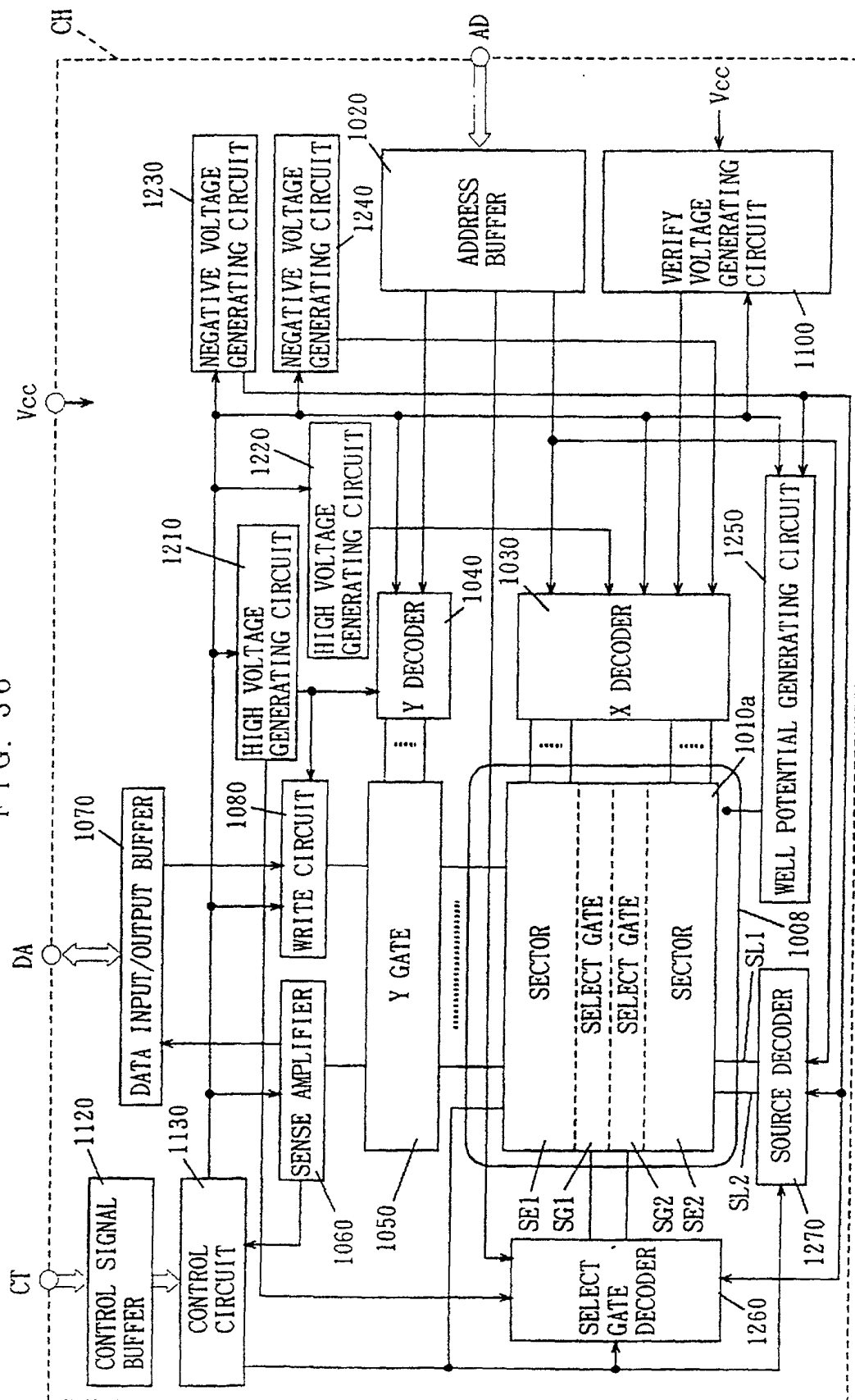


FIG. 37

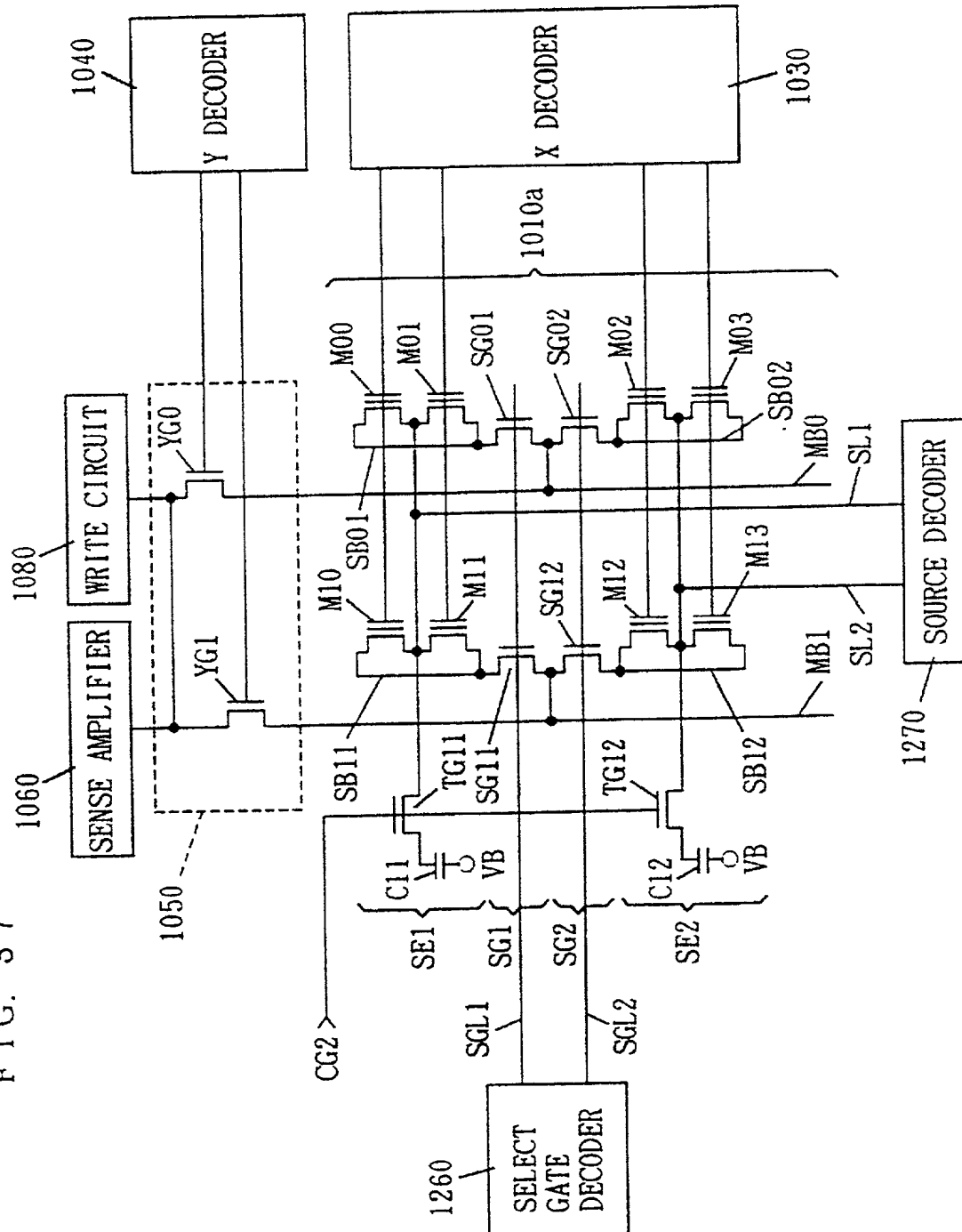


FIG. 38

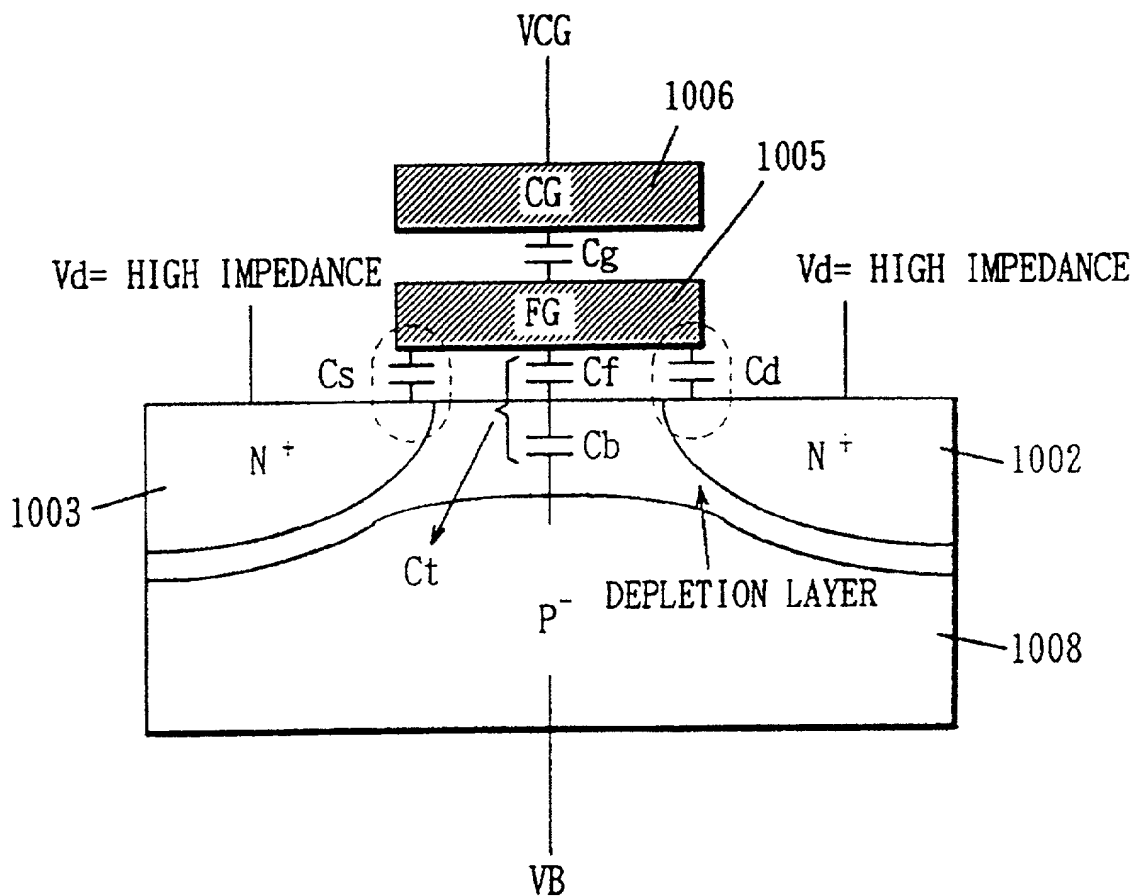


FIG. 39A

[ERASING WITH NO GATE BIRD'S BEAK]

(MEMORY CELL OF SELECTED SECTOR)

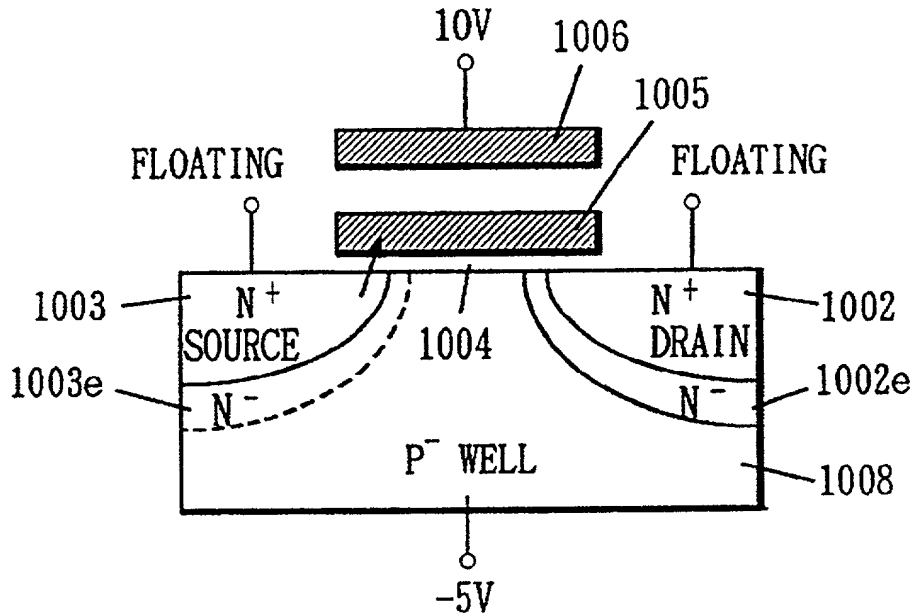


FIG. 39B

(MEMORY CELL OF NONSELECTED SECTOR)

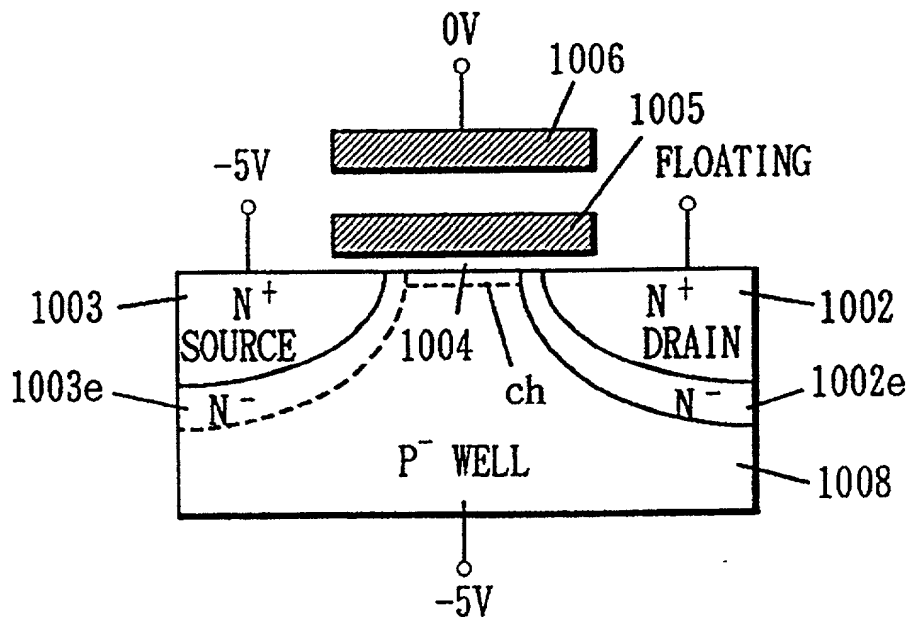


FIG. 39A - 042701

FIG. 40

[WITH NO GATE BIRD'S BEAK]

(BATCH SECTOR ERASE)

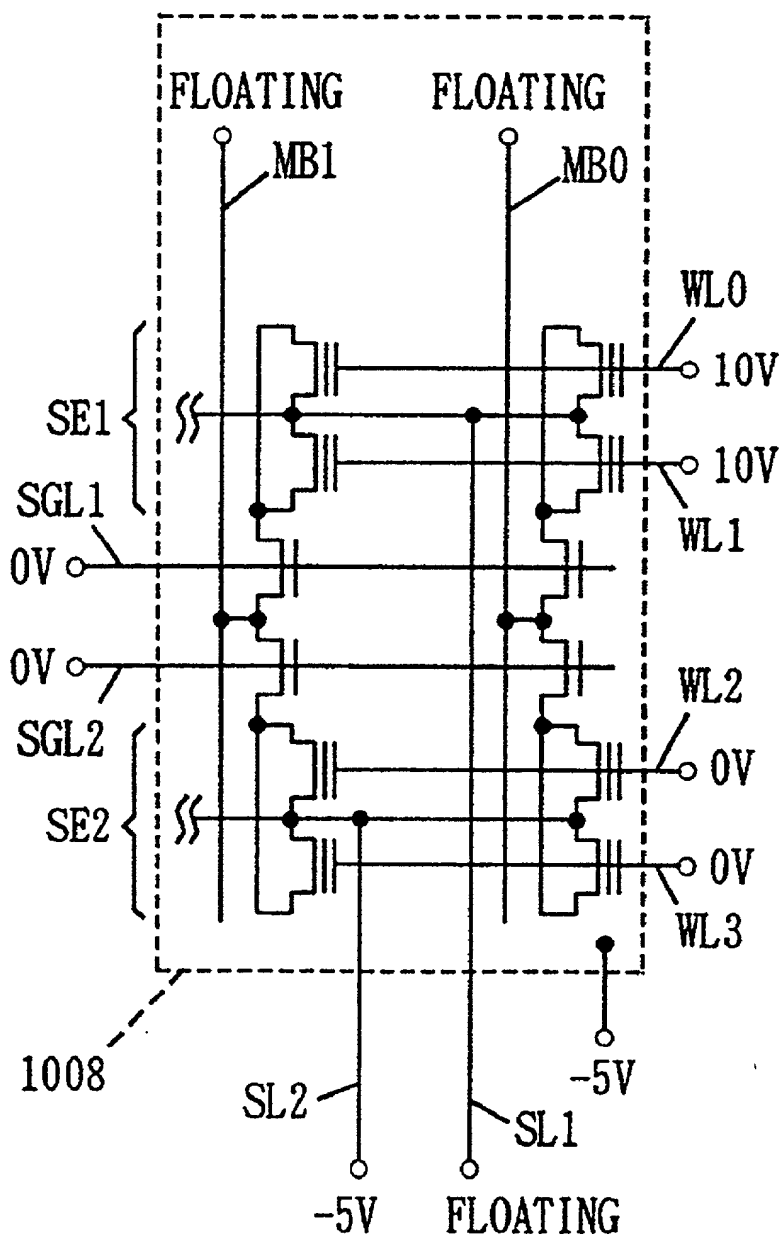
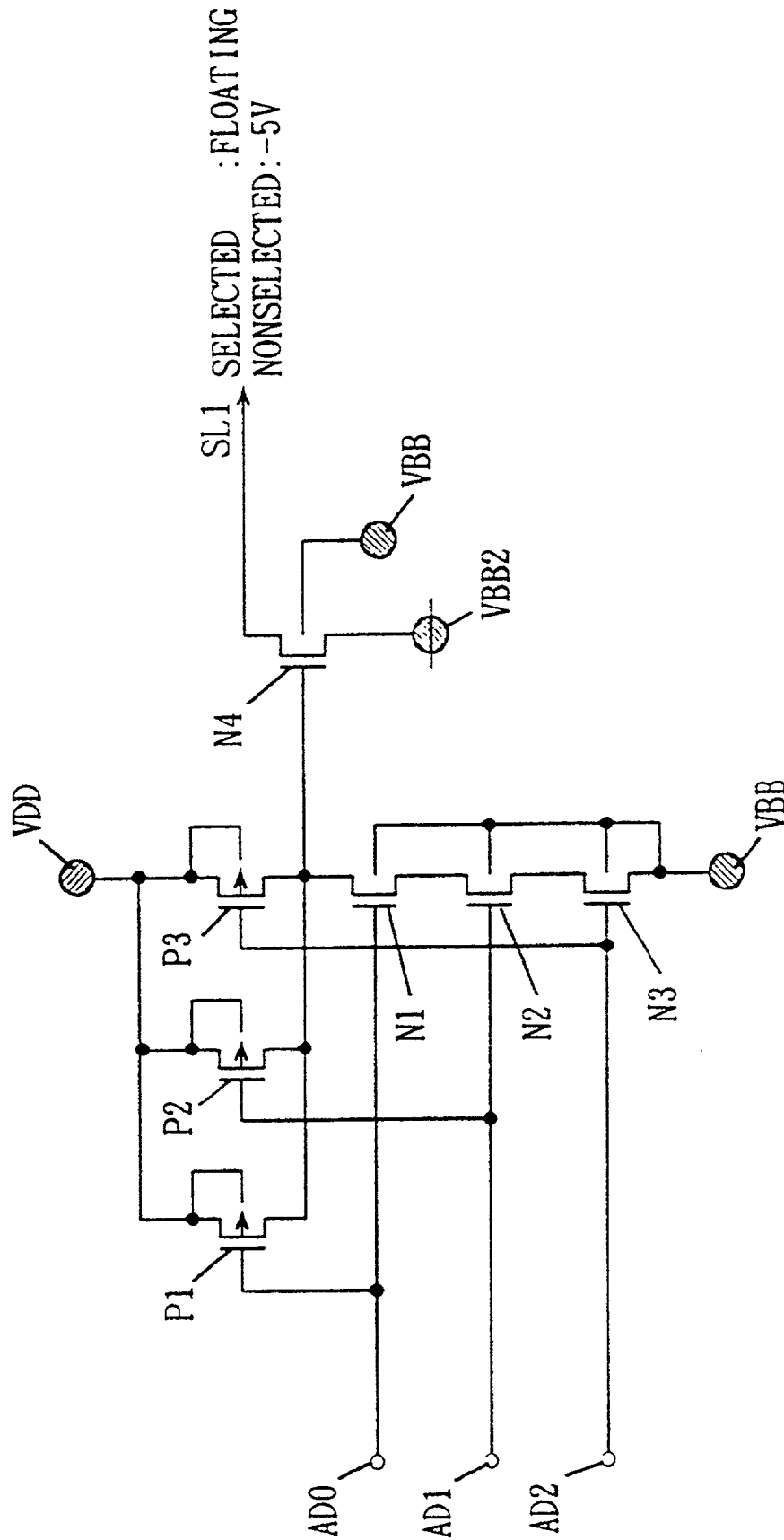


FIG. 40 - 09445-0430

FIG. 41



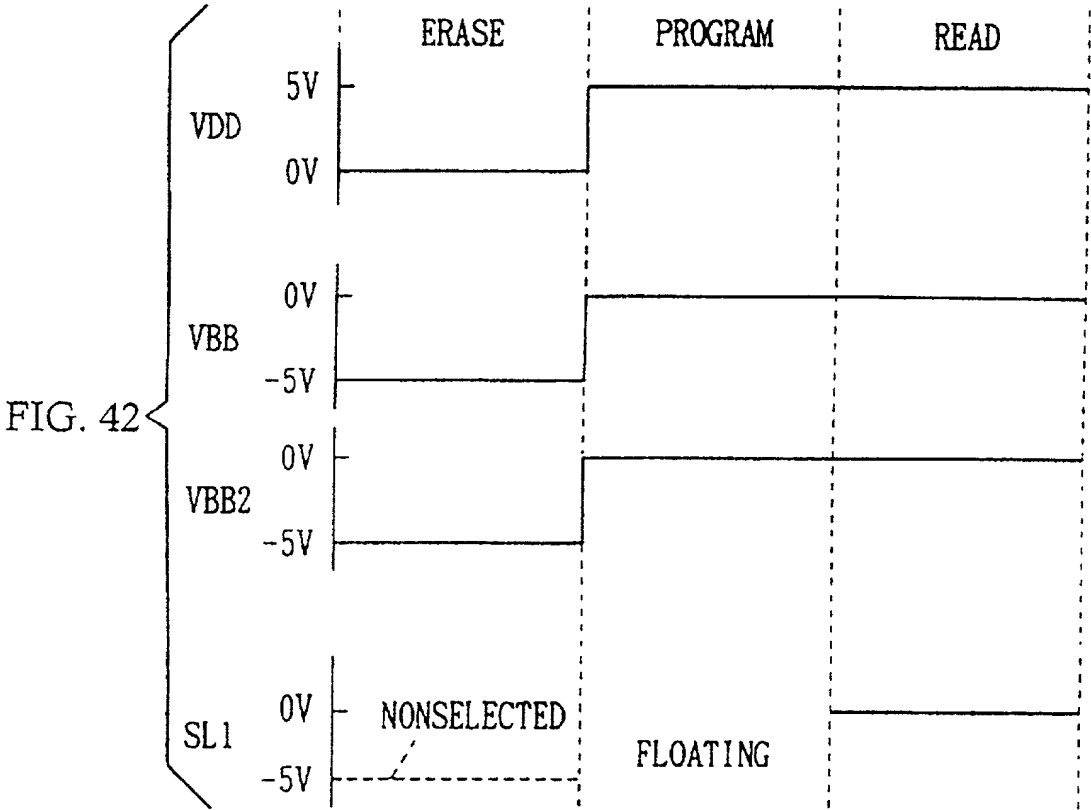




FIG. 43

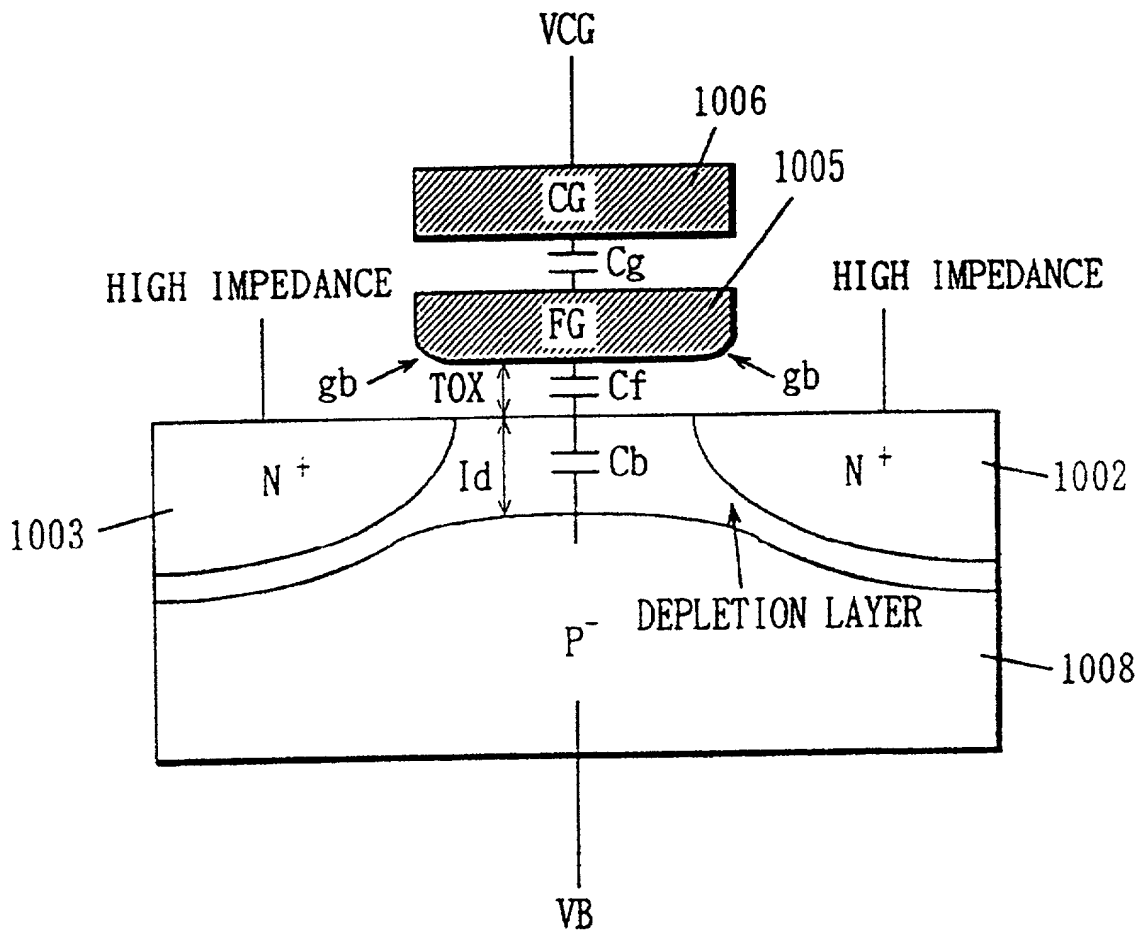


FIG. 44A

[ERASING WITH GATE BIRD'S BEAK]

(MEMORY CELL OF SELECTED SECTOR)

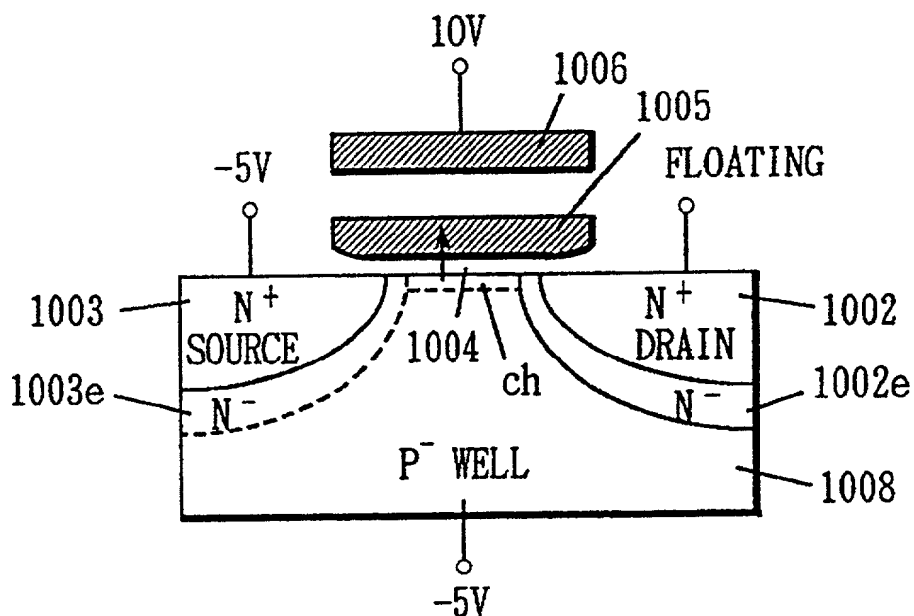


FIG. 44B

(MEMORY CELL OF NONSELECTED SECTOR)

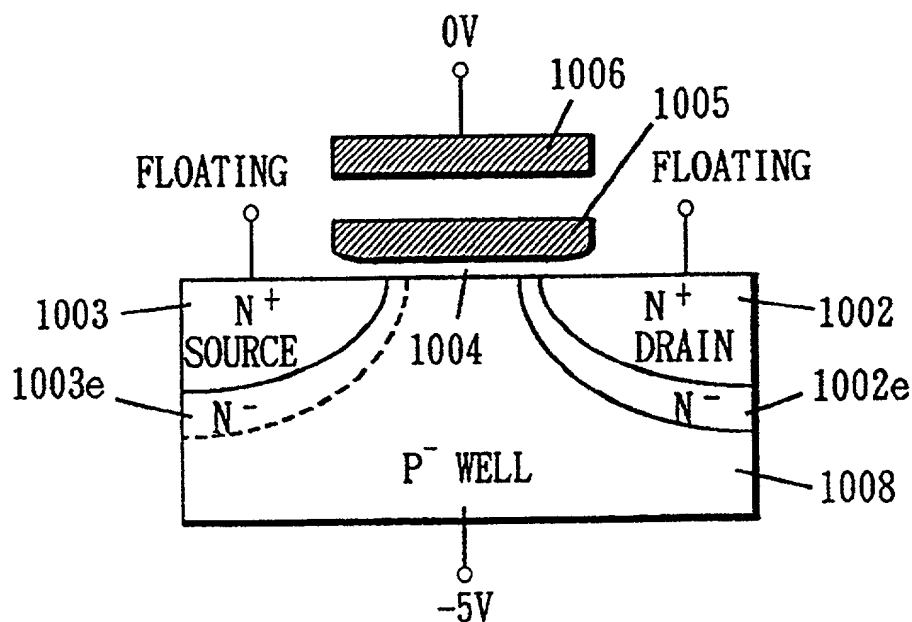


FIG. 45

[WITH GATE BIRD'S BEAK]

(BATCH SECTOR ERASE)

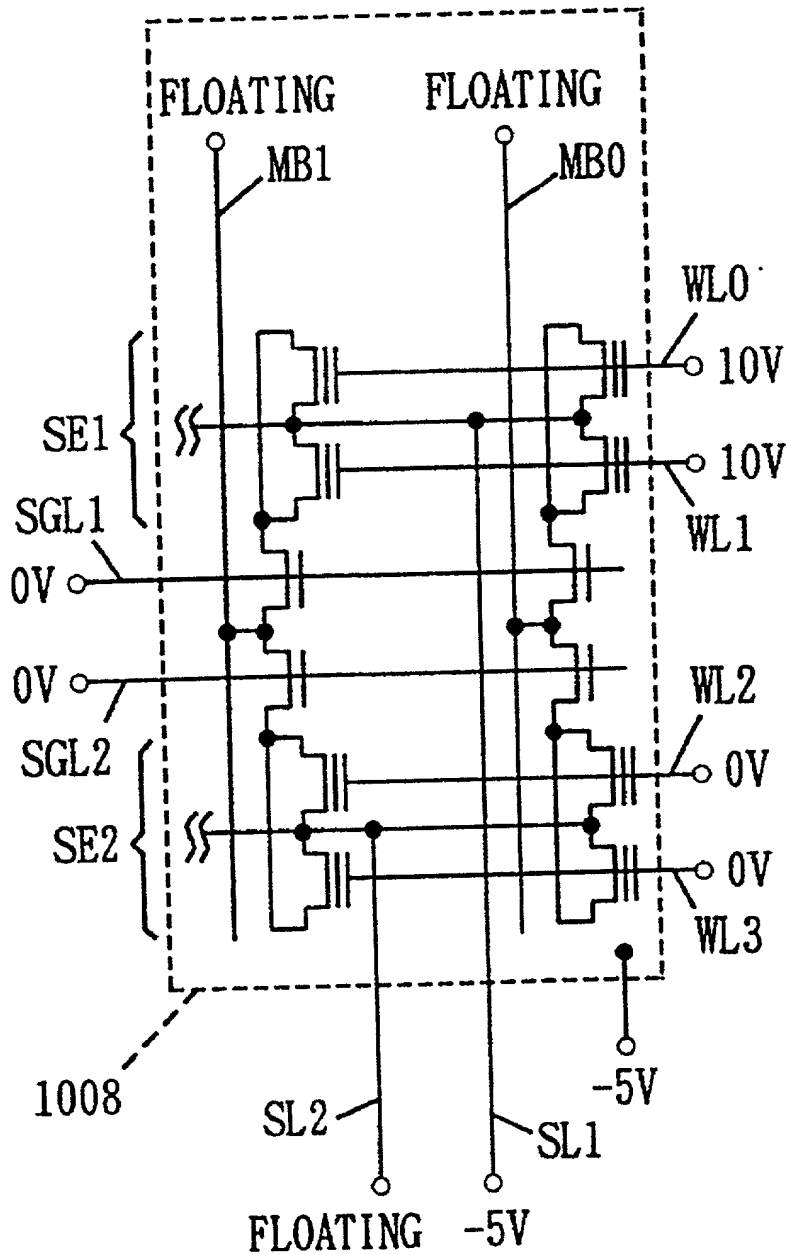
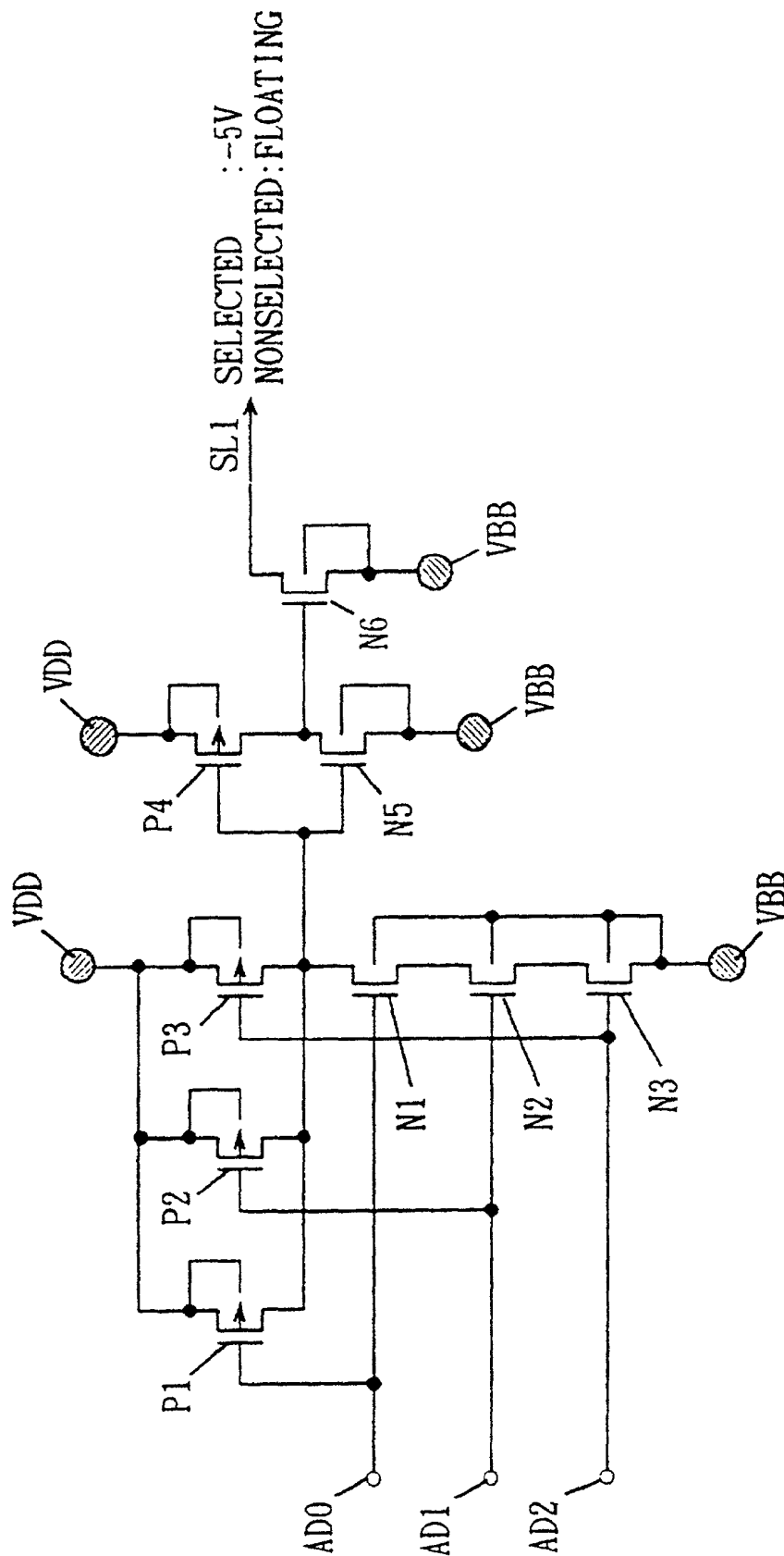


FIG. 45 - SECTOR ERASE

FIG. 46



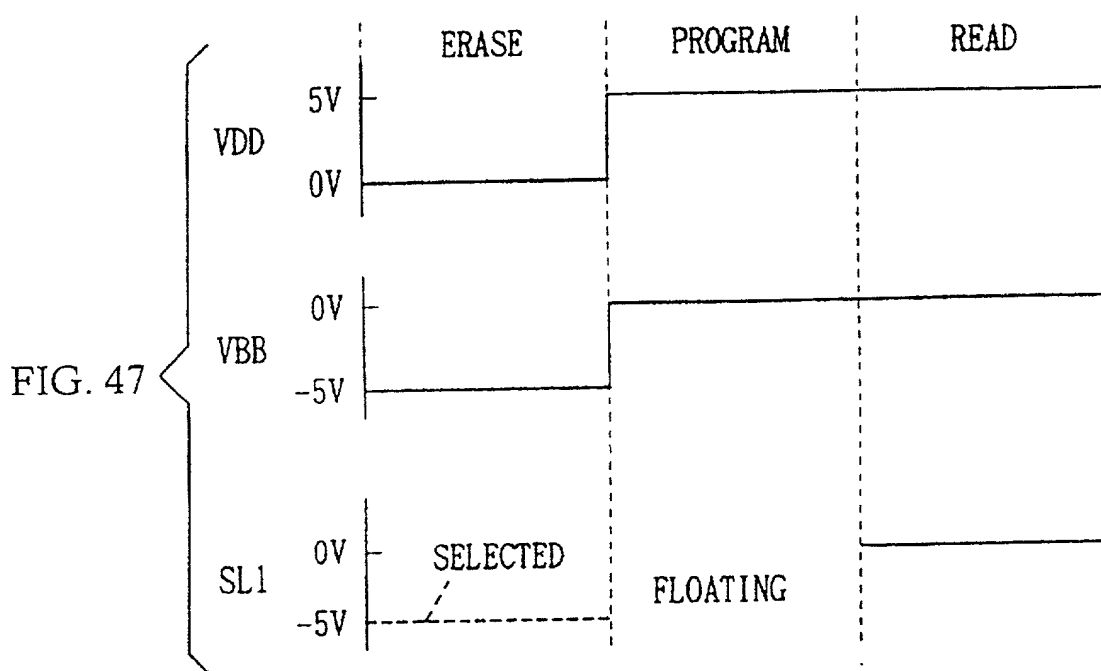


FIG. 48A

[ERASING WITH LOW WELL POTENTIAL]

(MEMORY CELL OF SELECTED SECTOR)

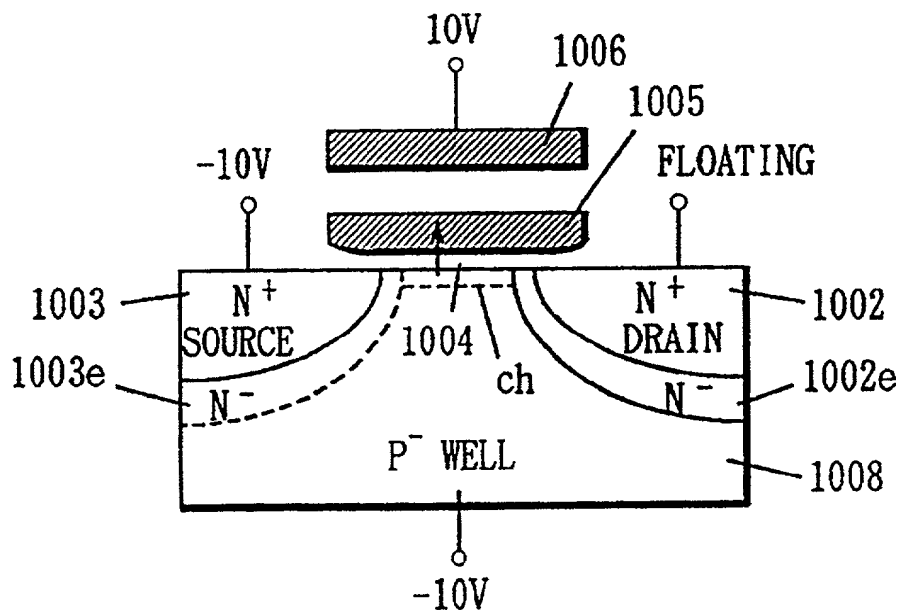


FIG. 48B

(MEMORY CELL OF NONSELECTED SECTOR)

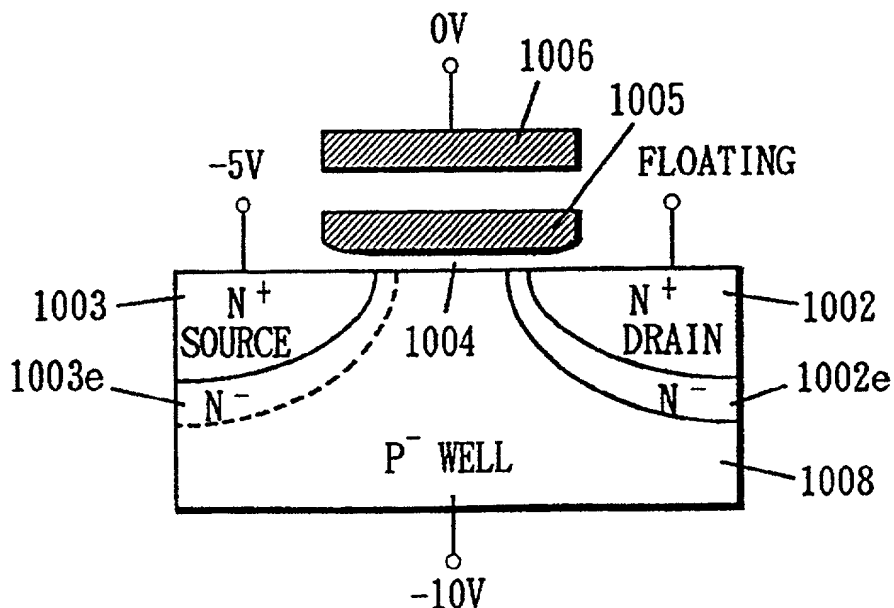
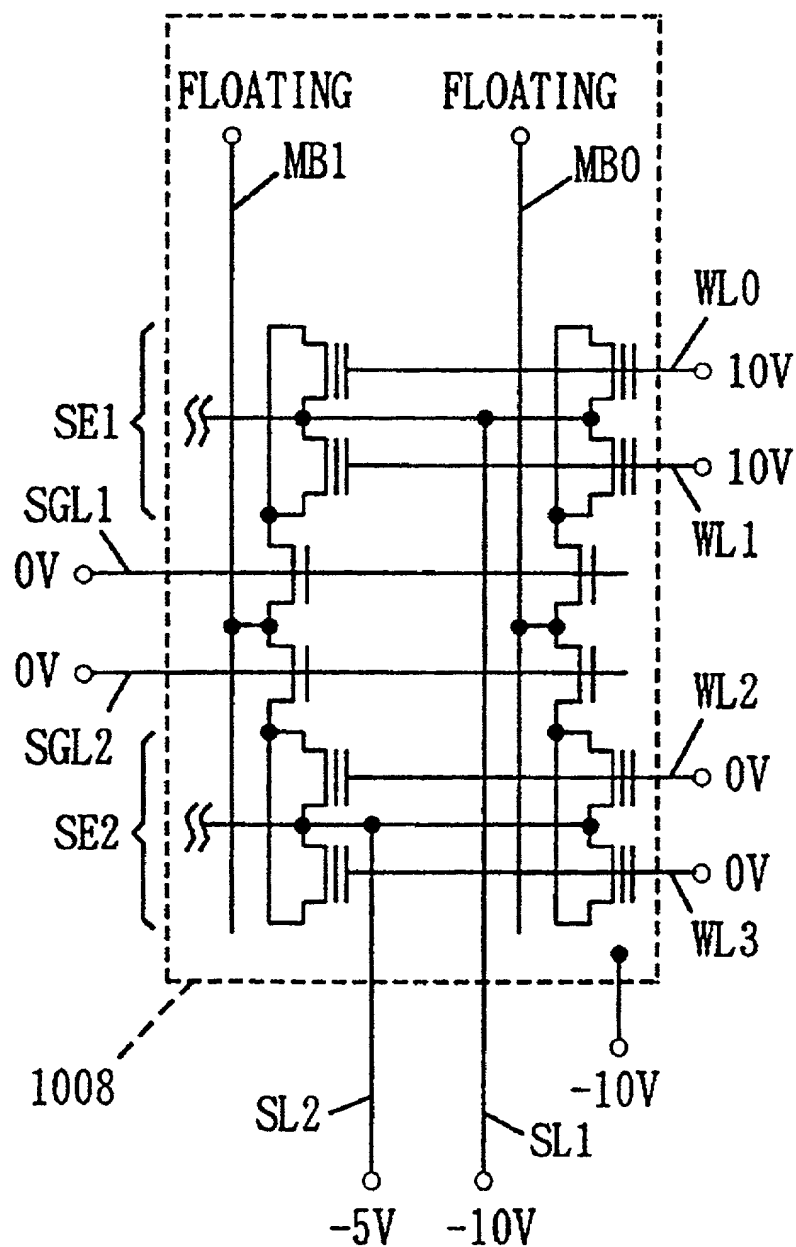


FIG. 49

[WITH LOW WELL POTENTIAL]

(BATCH SECTOR ERASE)



Variable	Mean	SD	Min	Max
Age	34.5	10.2	21	55
Gender	0.5	0.5	0	1
Marital Status	0.6	0.5	0	1
Education	12.5	1.5	9	16
Income	3500	1500	1000	8000
Health Status	0.7	0.4	0	1
Exercise Frequency	2.5	1.5	0	5
Stress Level	4.5	1.5	1	7
Sleep Quality	3.5	1.5	1	6
Diet Quality	4.0	1.5	1	6
Work-Life Balance	3.0	1.5	1	5
Family Support	4.5	1.5	1	6
Community Involvement	2.0	1.5	0	4
Life Satisfaction	5.5	1.5	1	7
Overall Well-being	4.0	1.5	1	6

FIG. 50

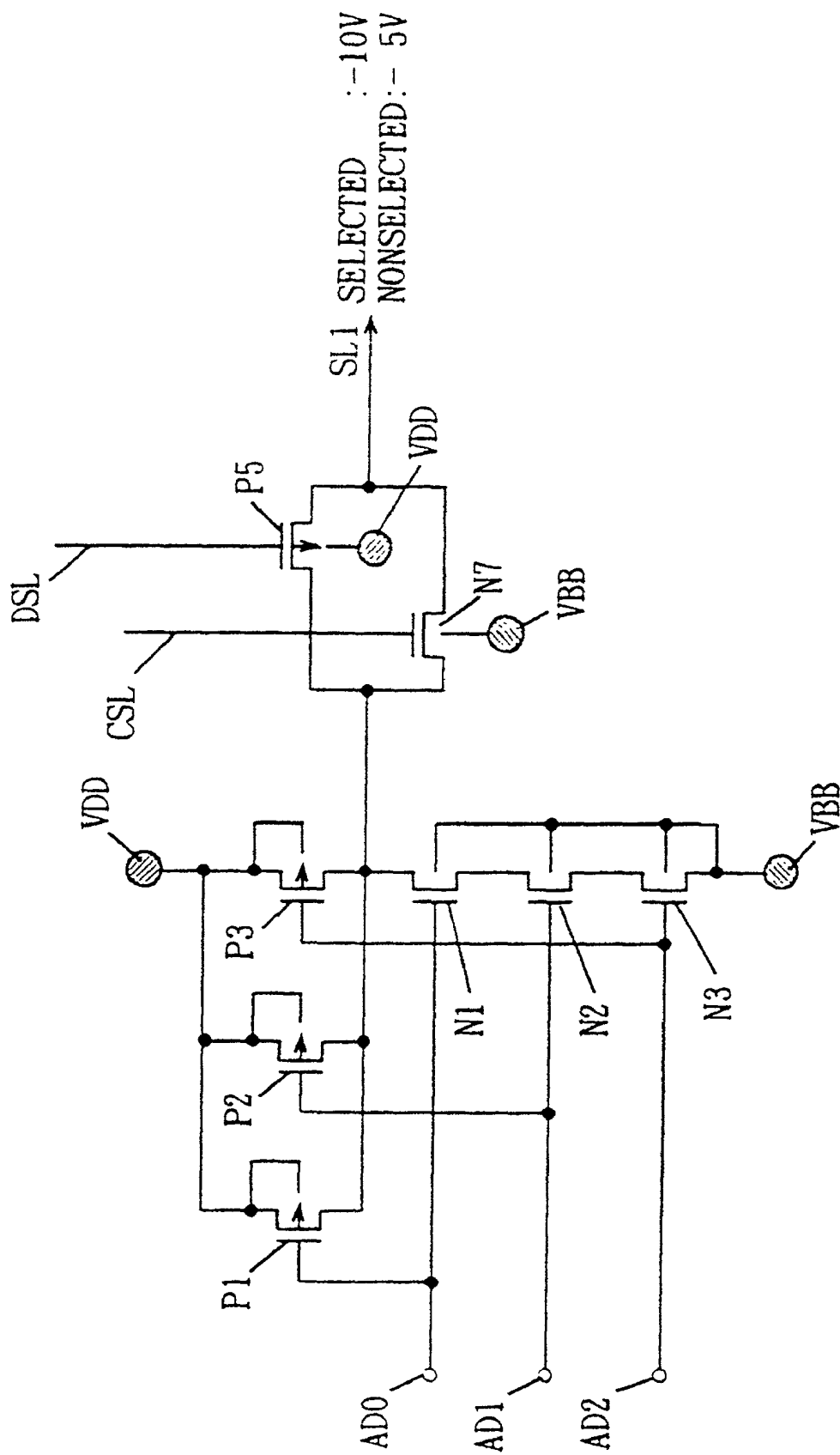




FIG. 51

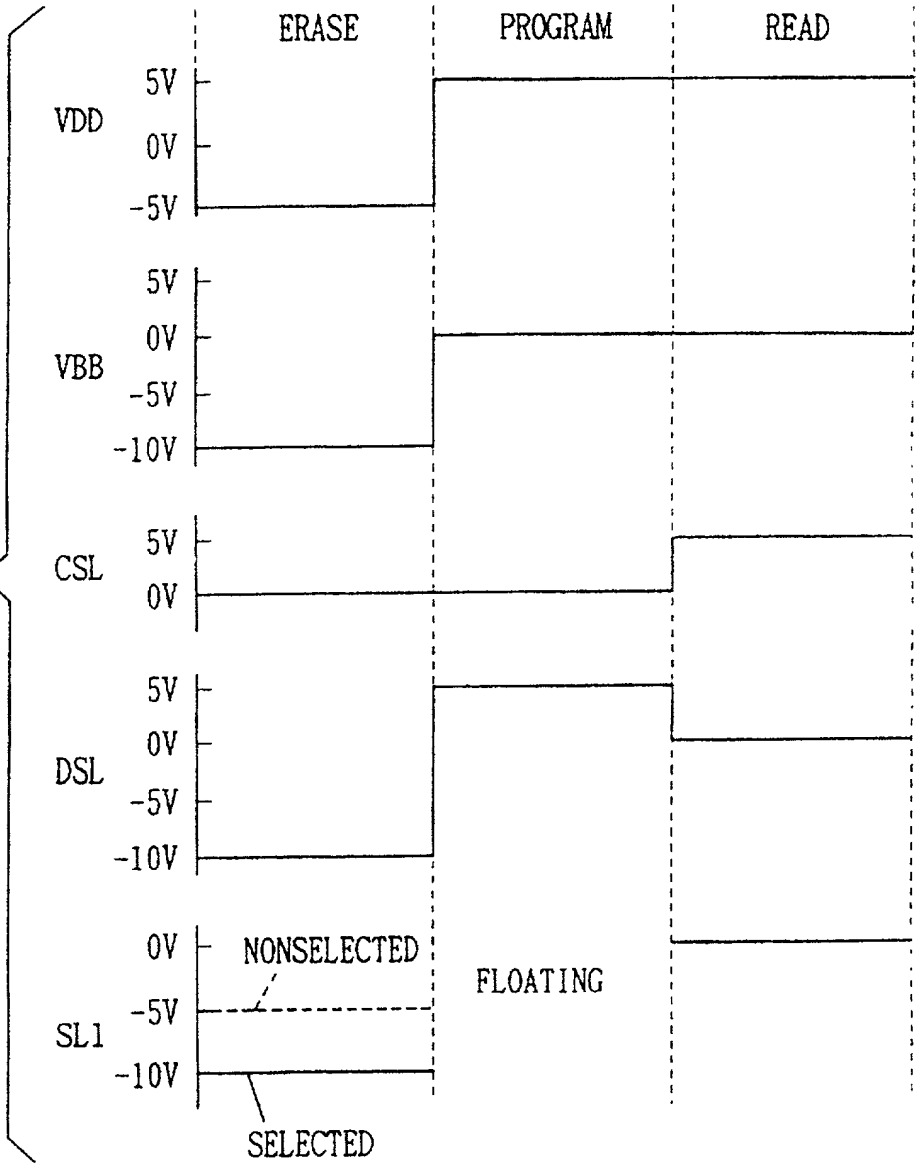


FIG. 52

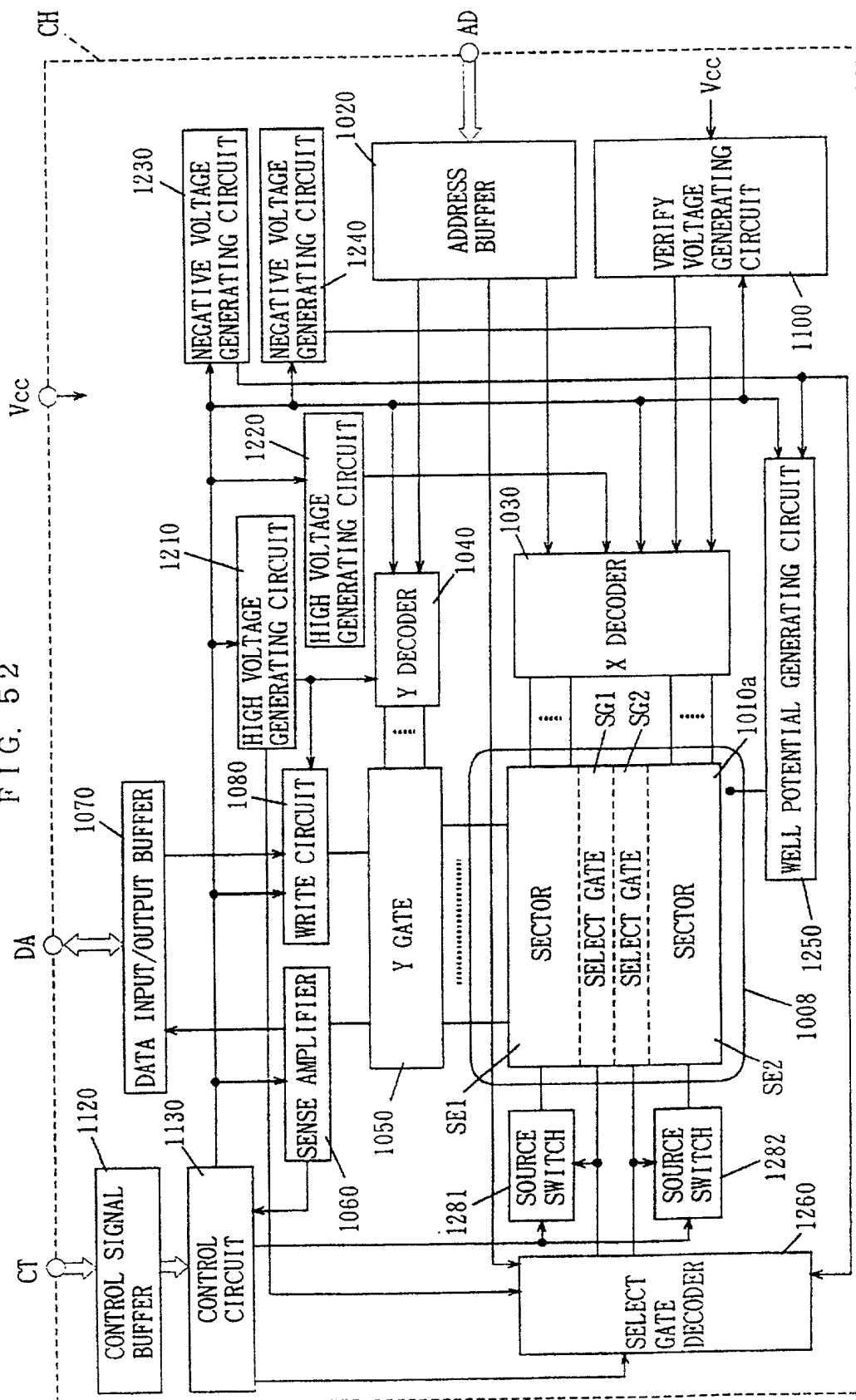


FIG. 53

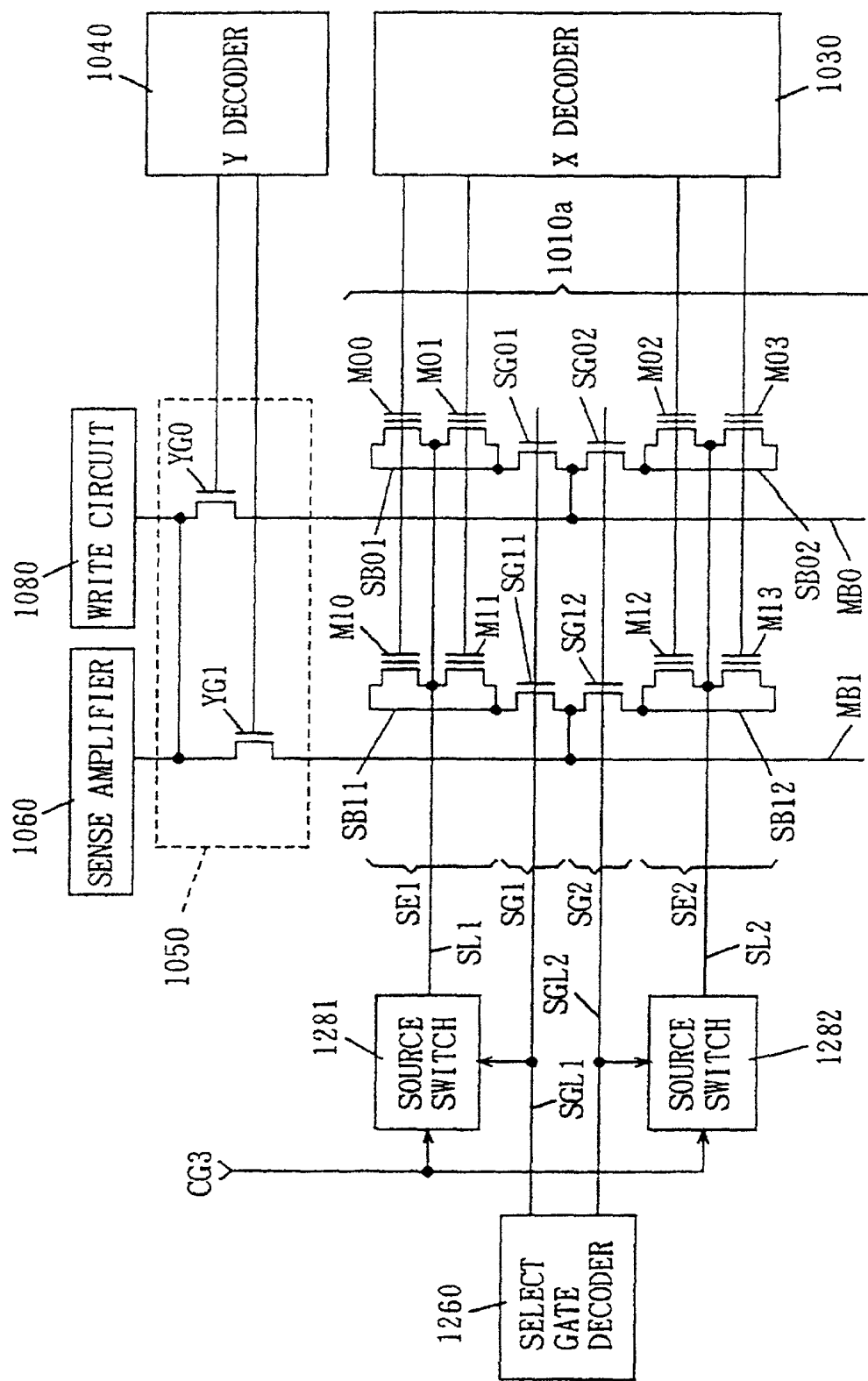


FIG. 54

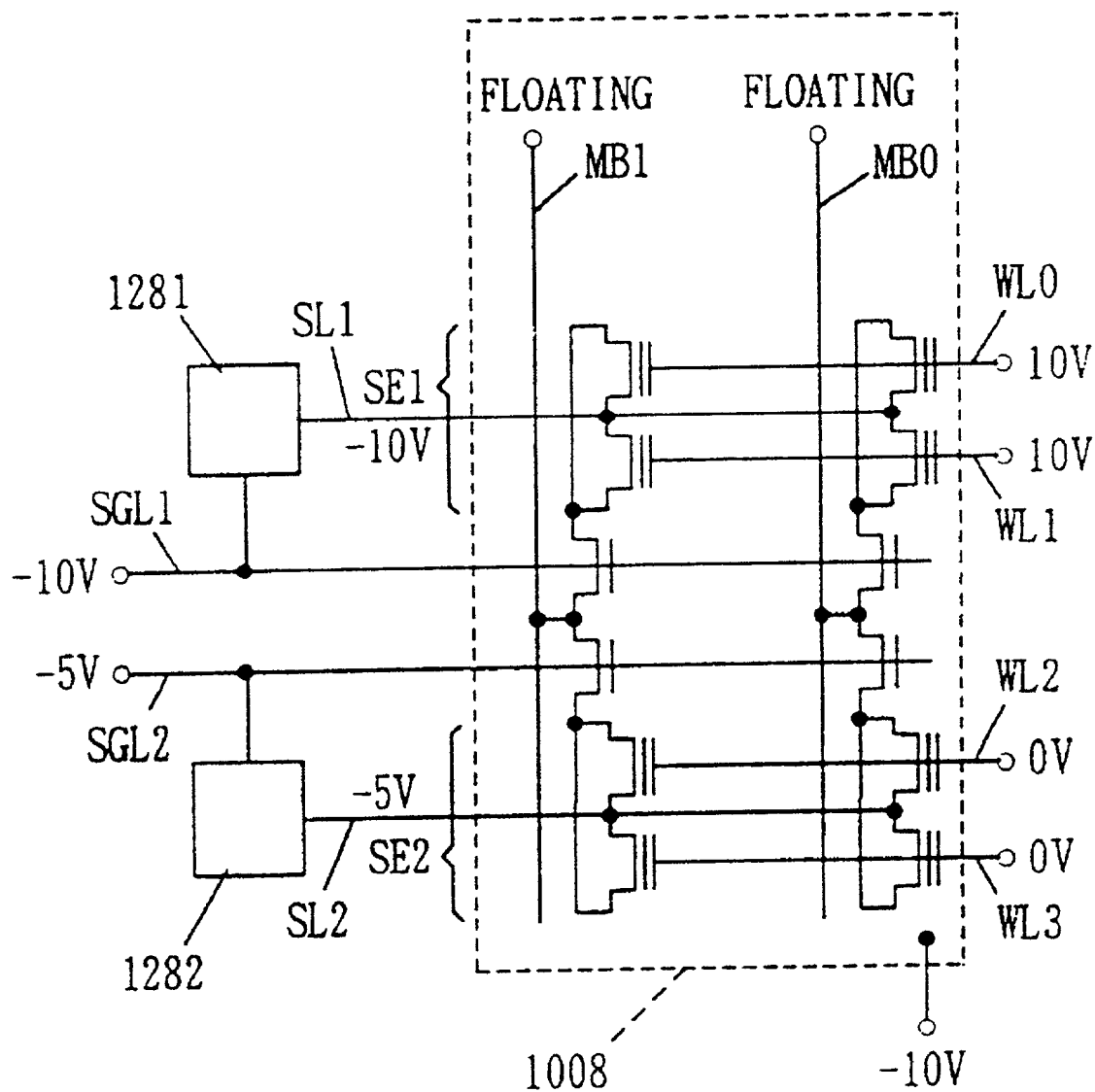


FIG. 55

FIG. 55

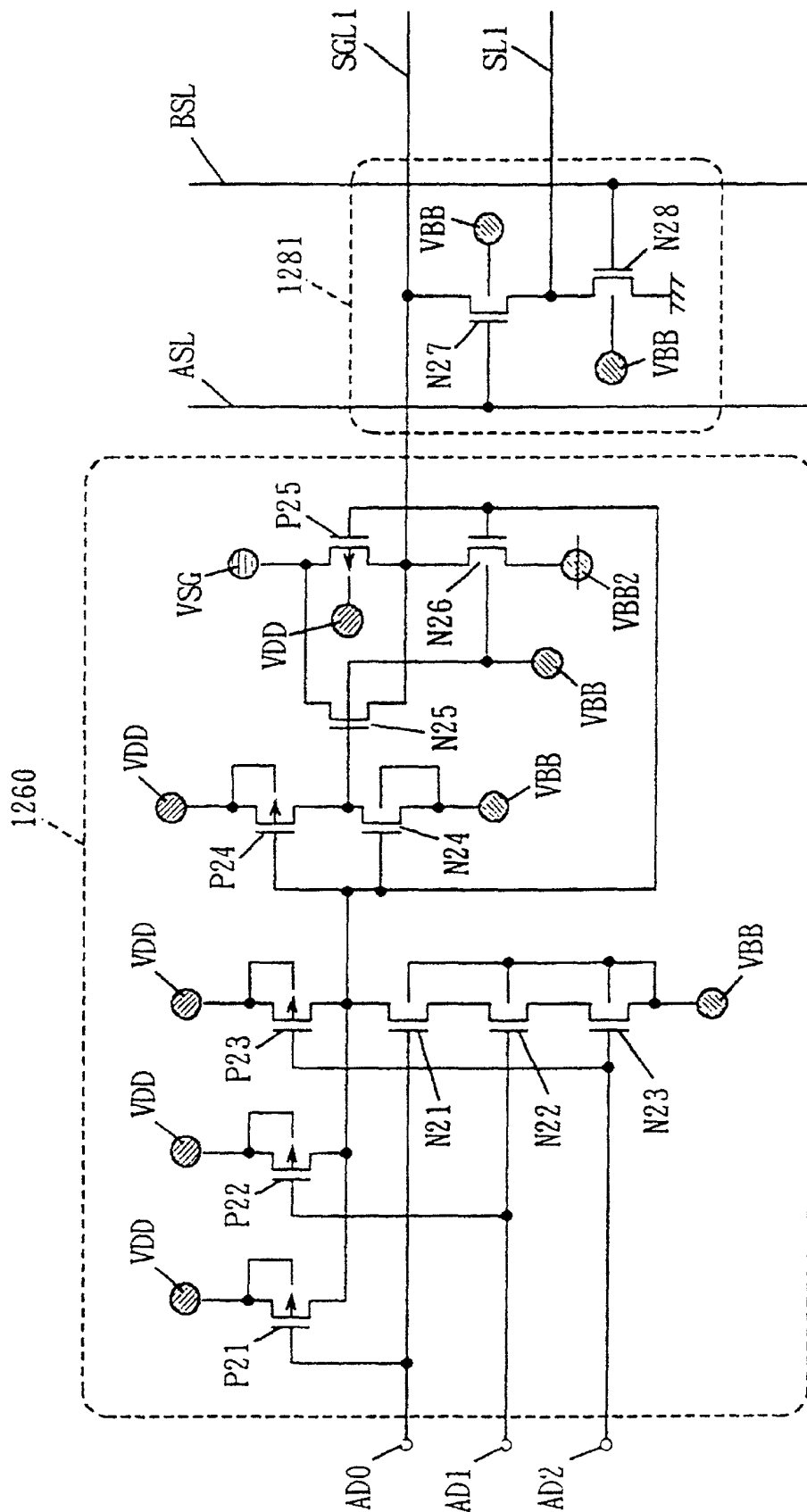


FIG. 56

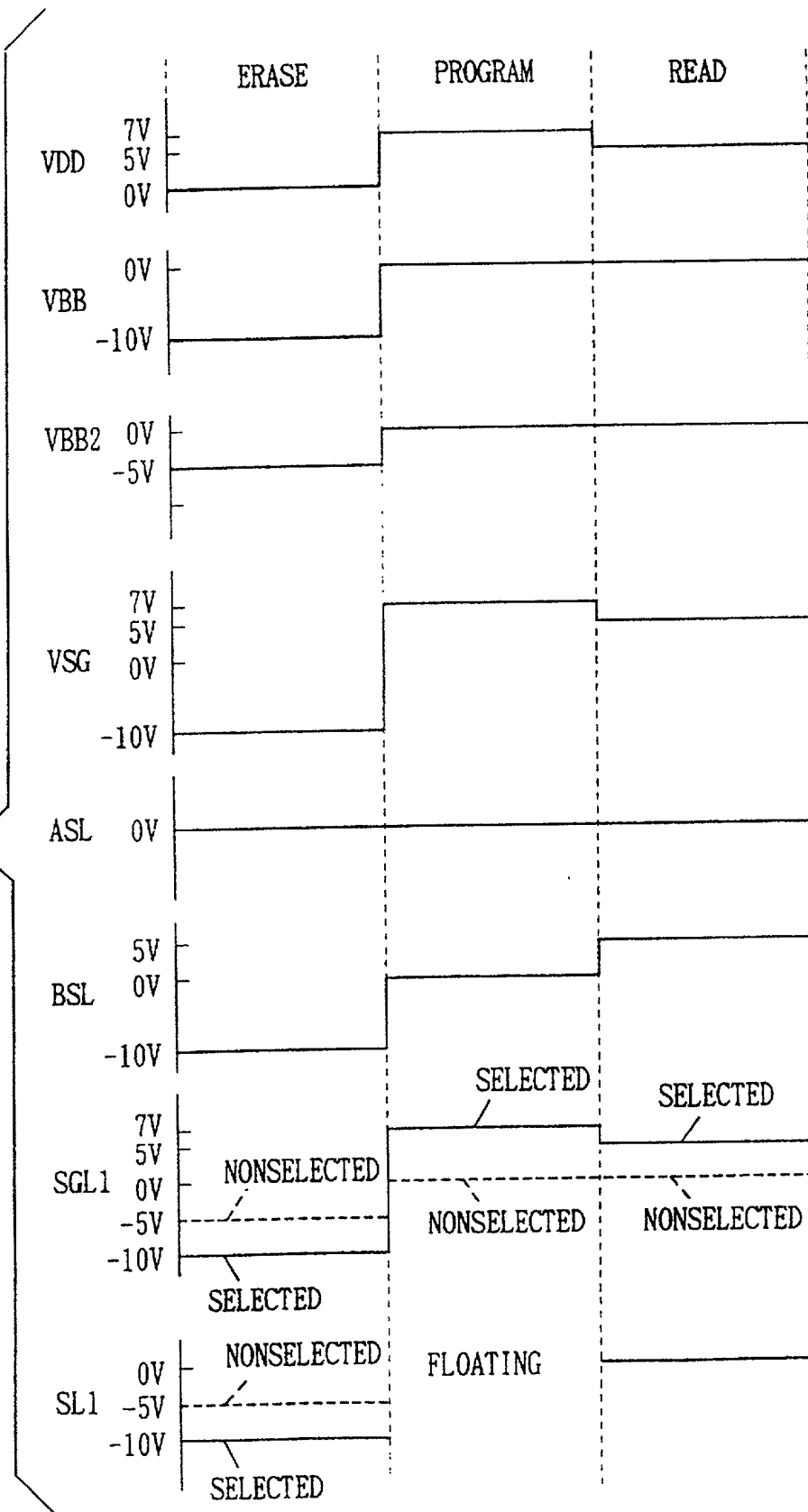


FIG. 57

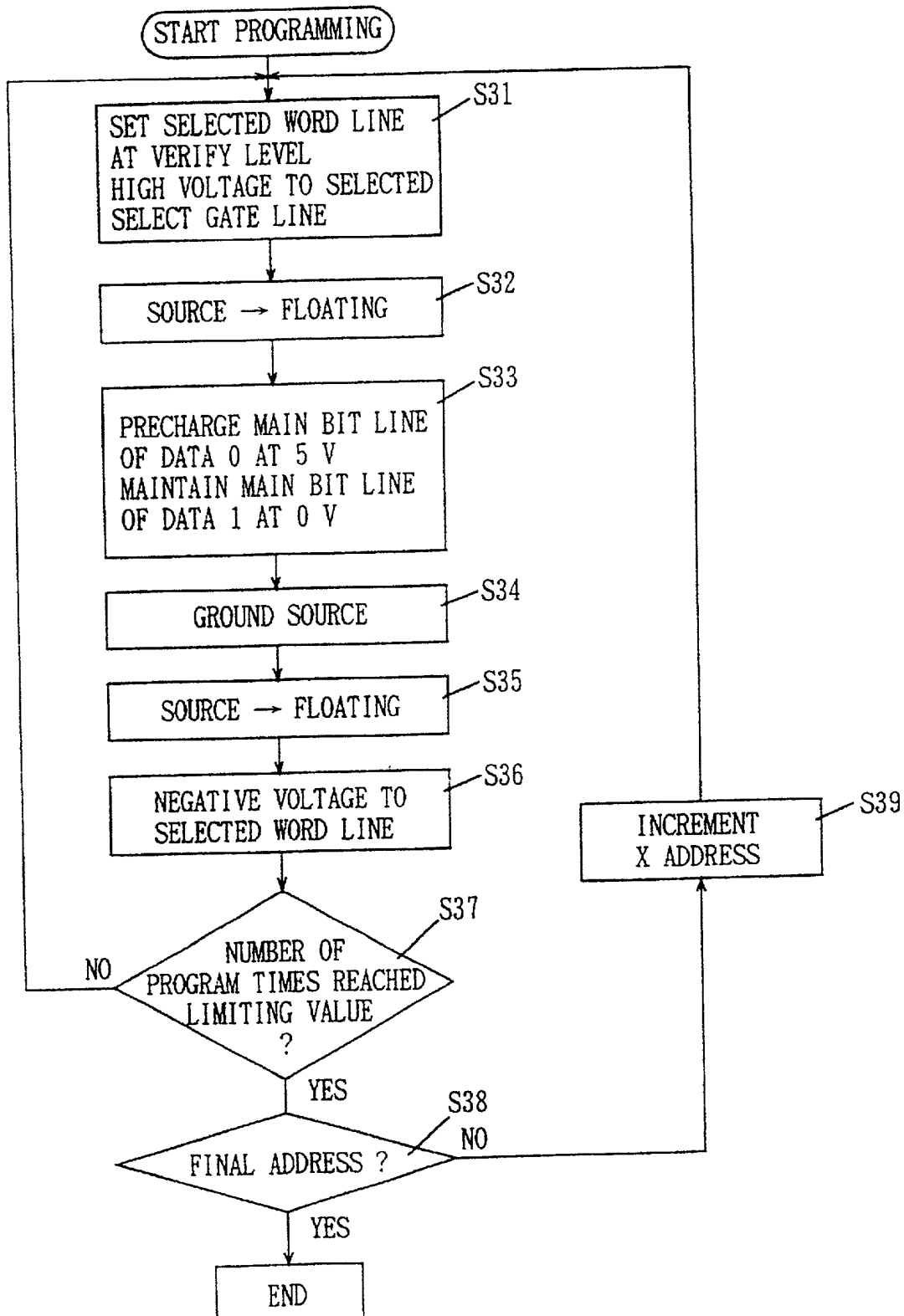


FIG. 58

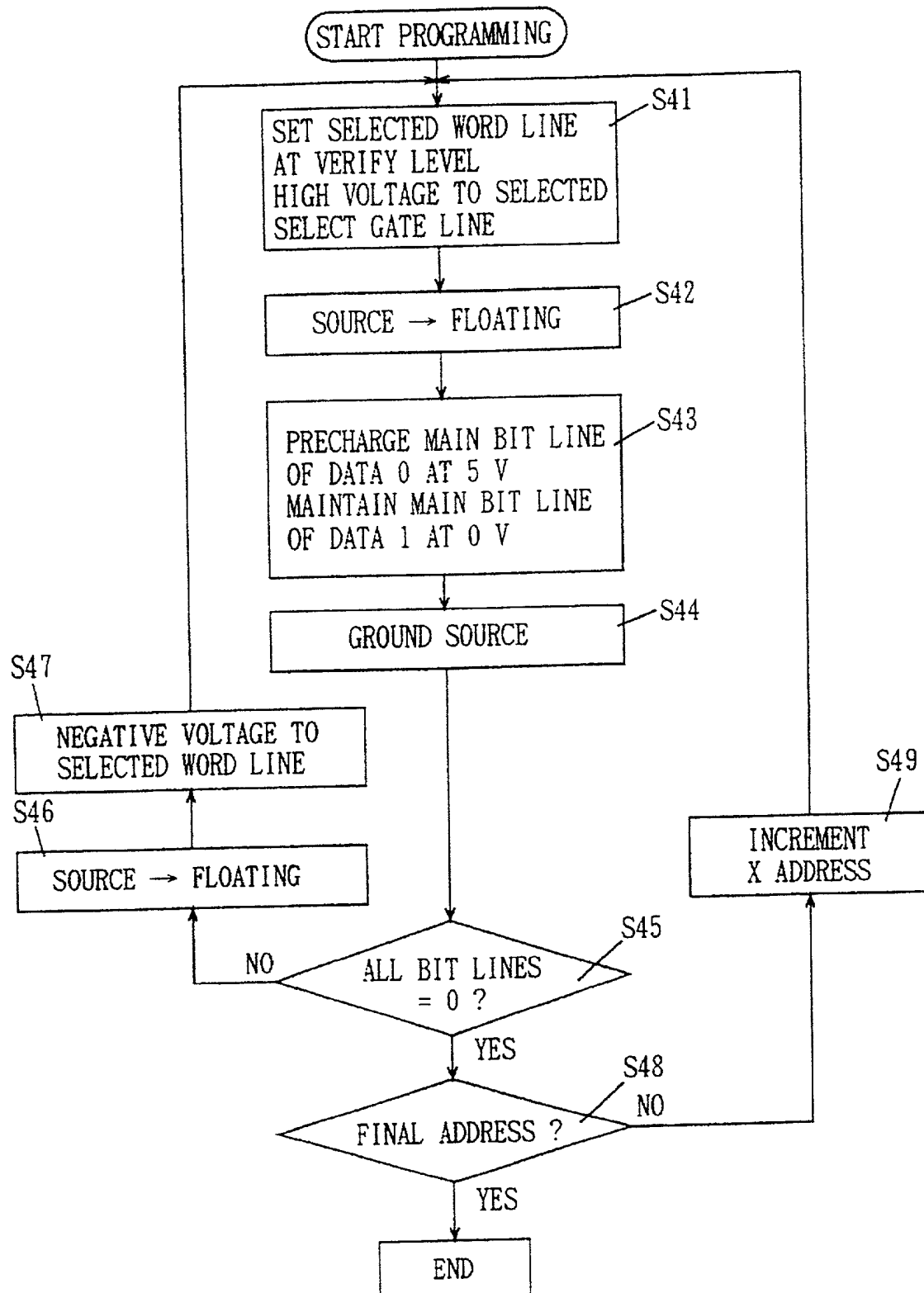




FIG. 59

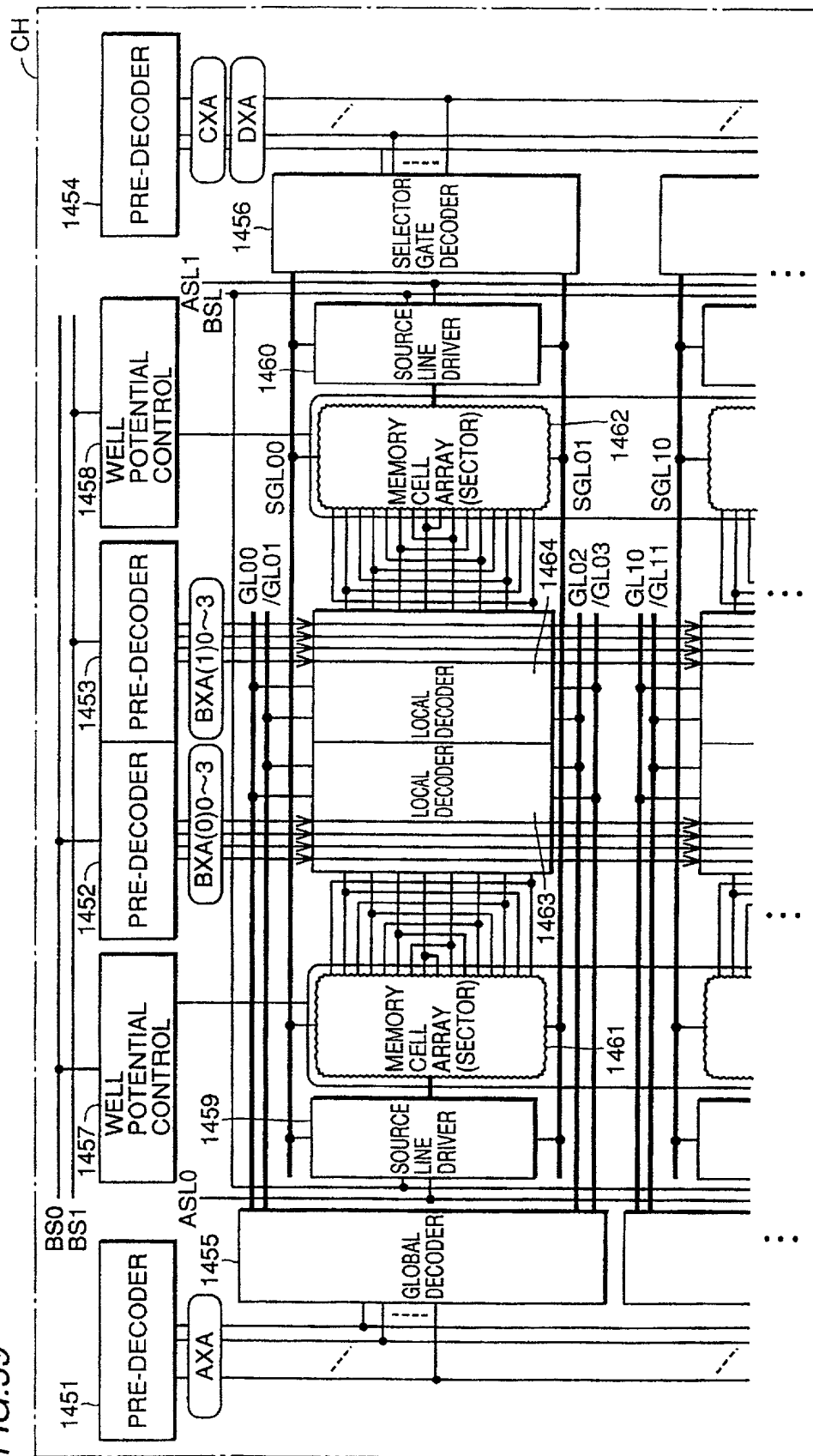


FIG. 60

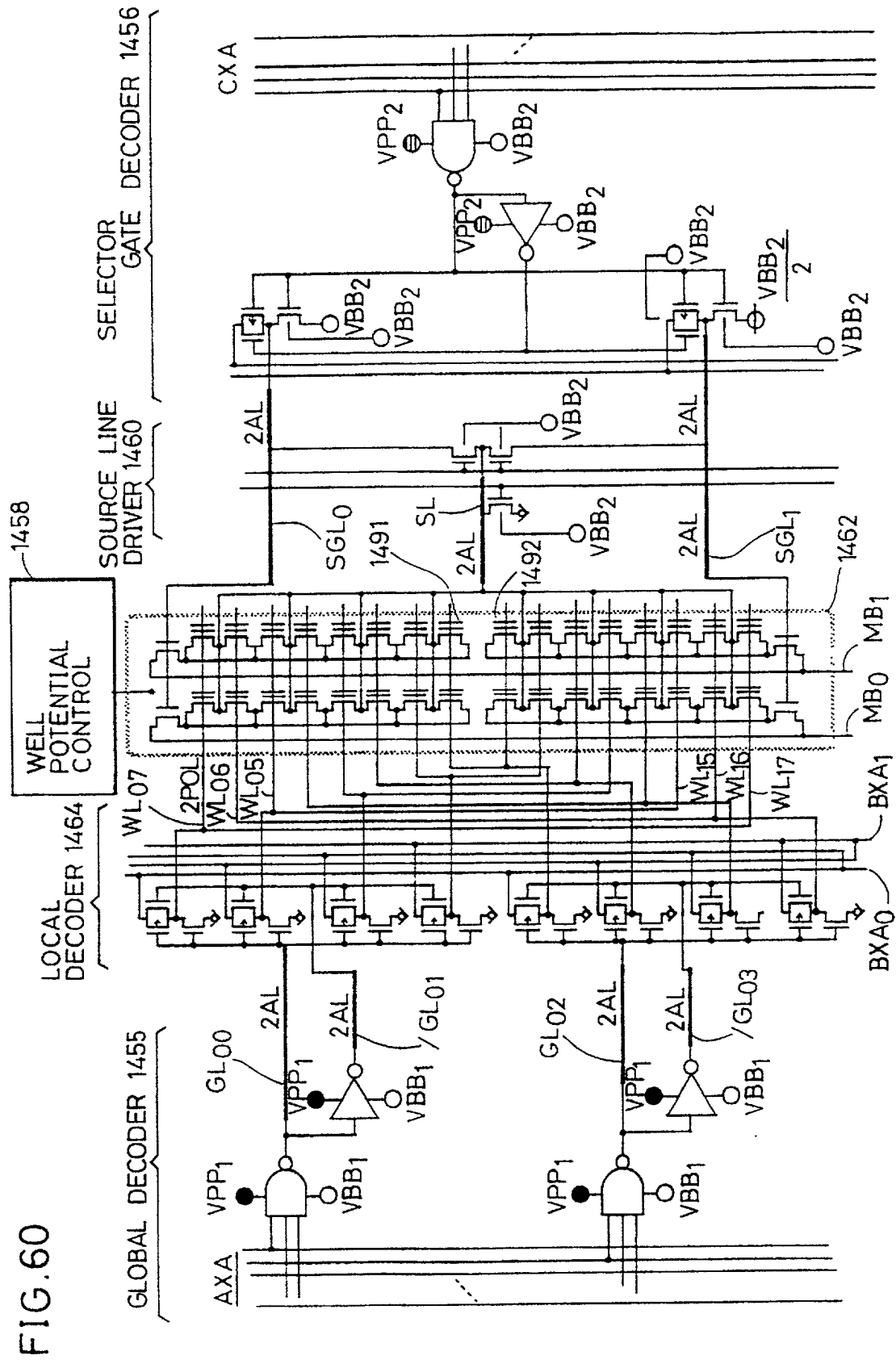


FIG. 61

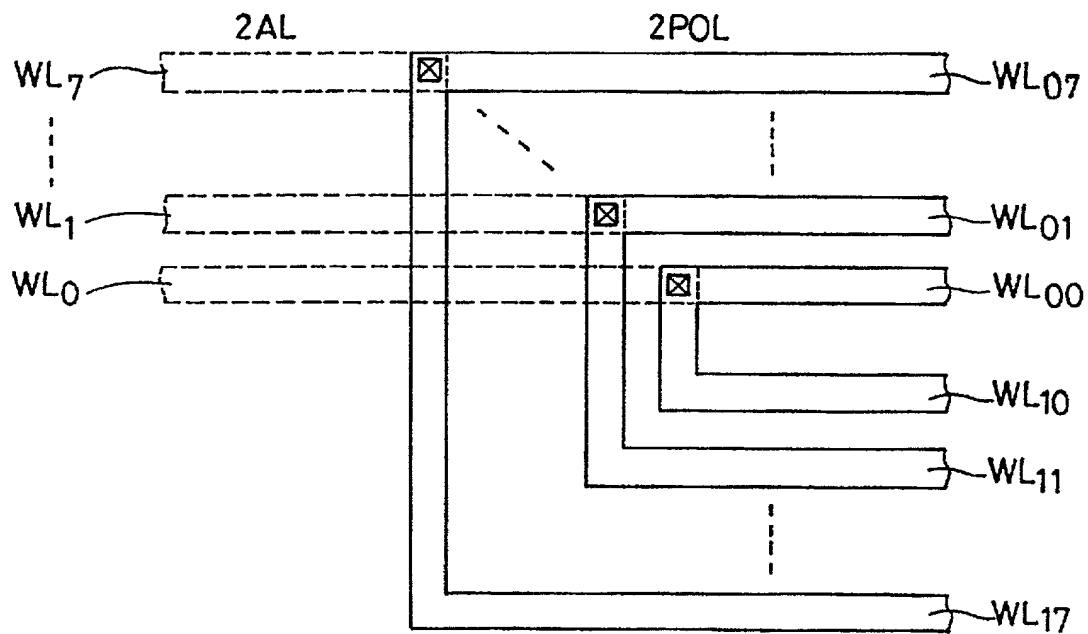


FIG. 62

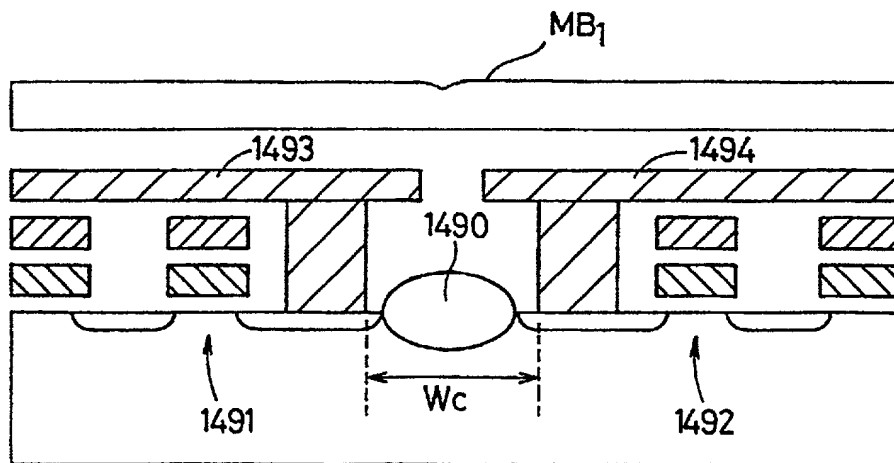


FIG. 61

FIG. 63

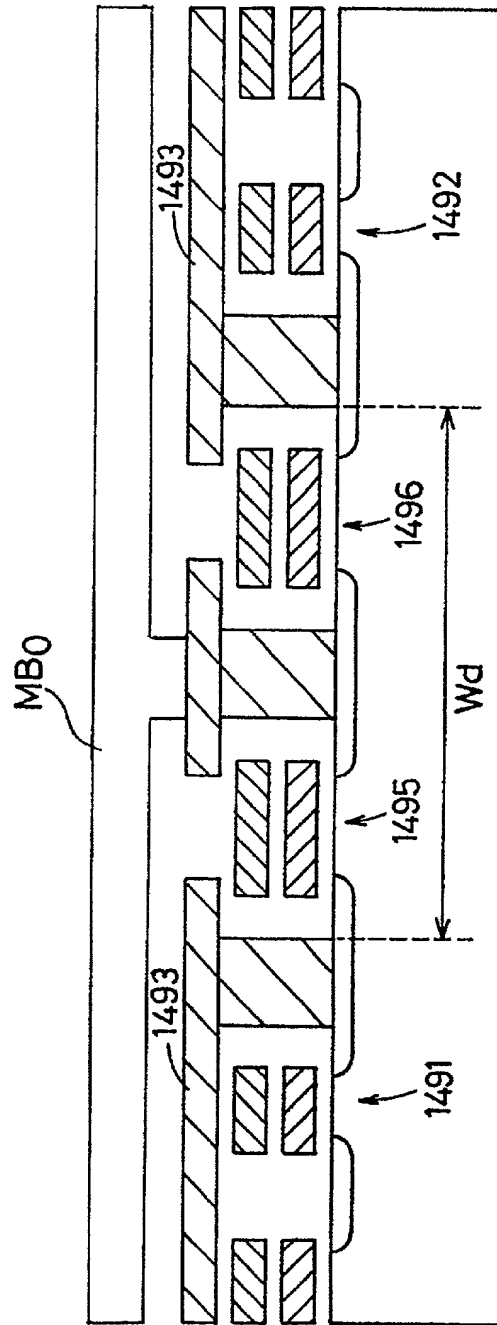


FIG. 64

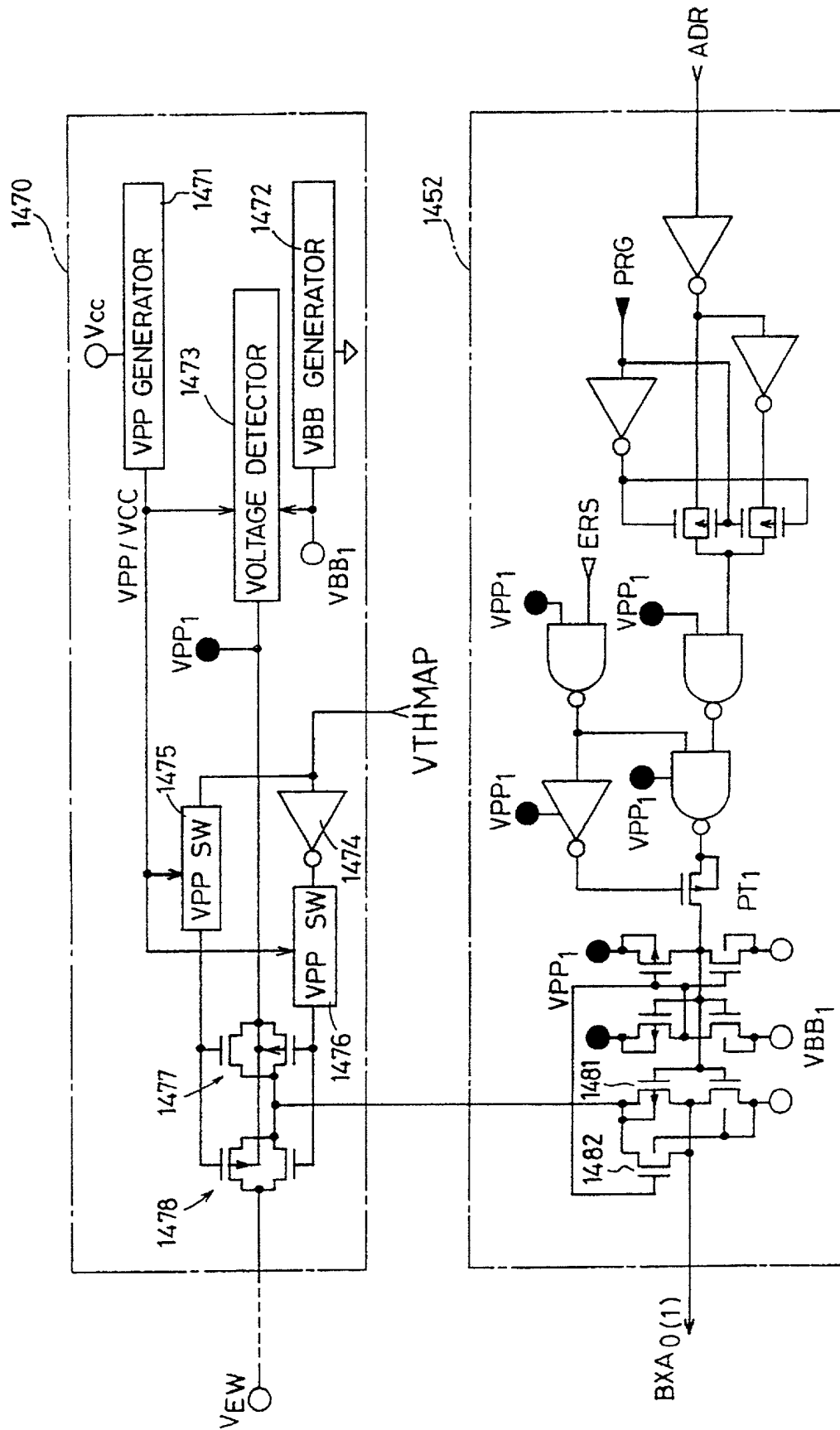
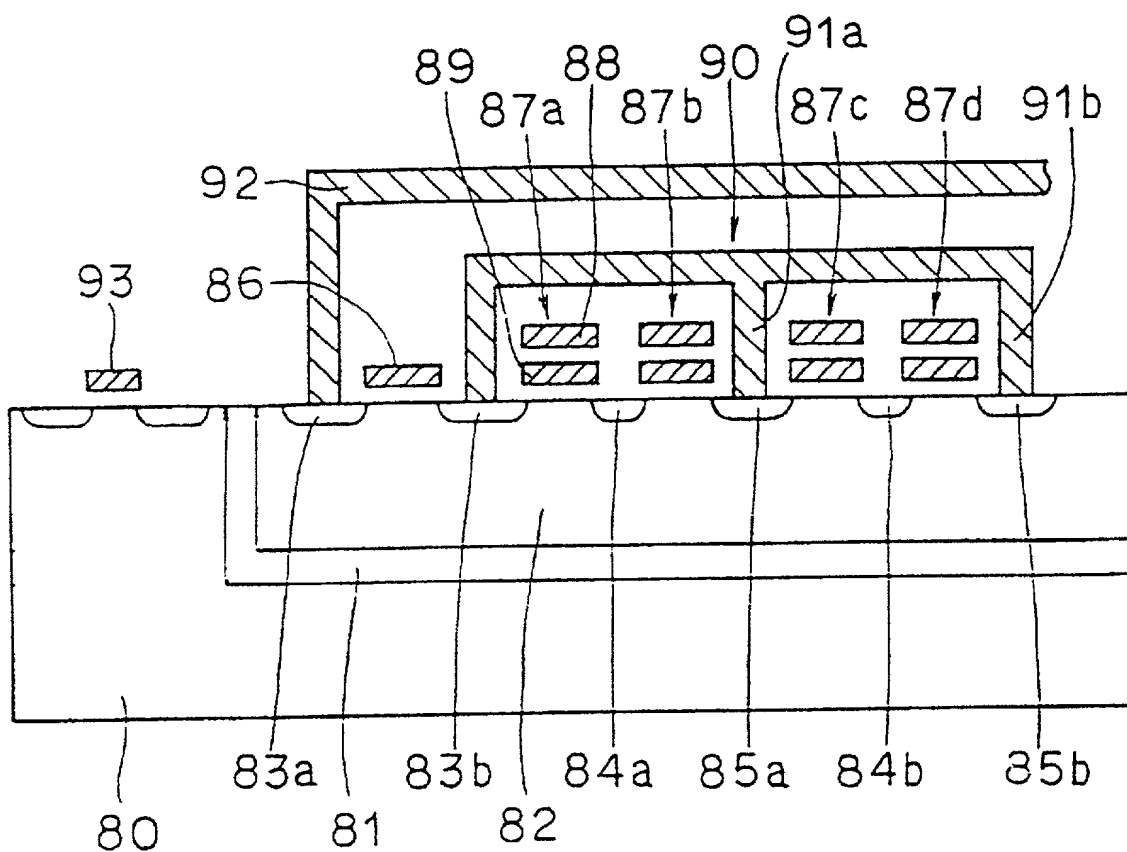


FIG. 65



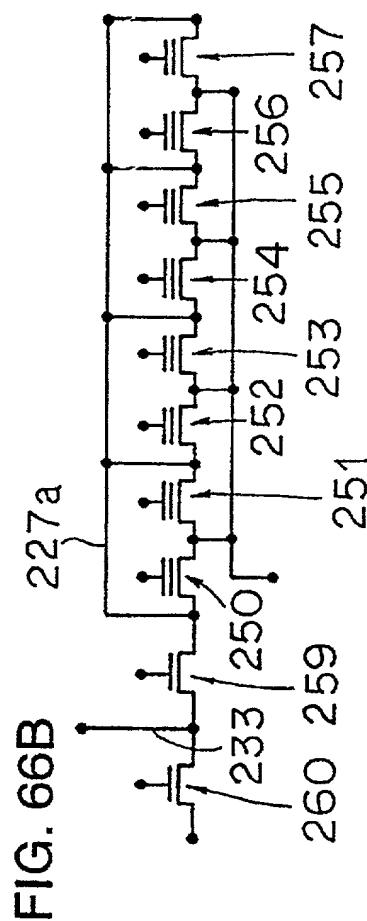
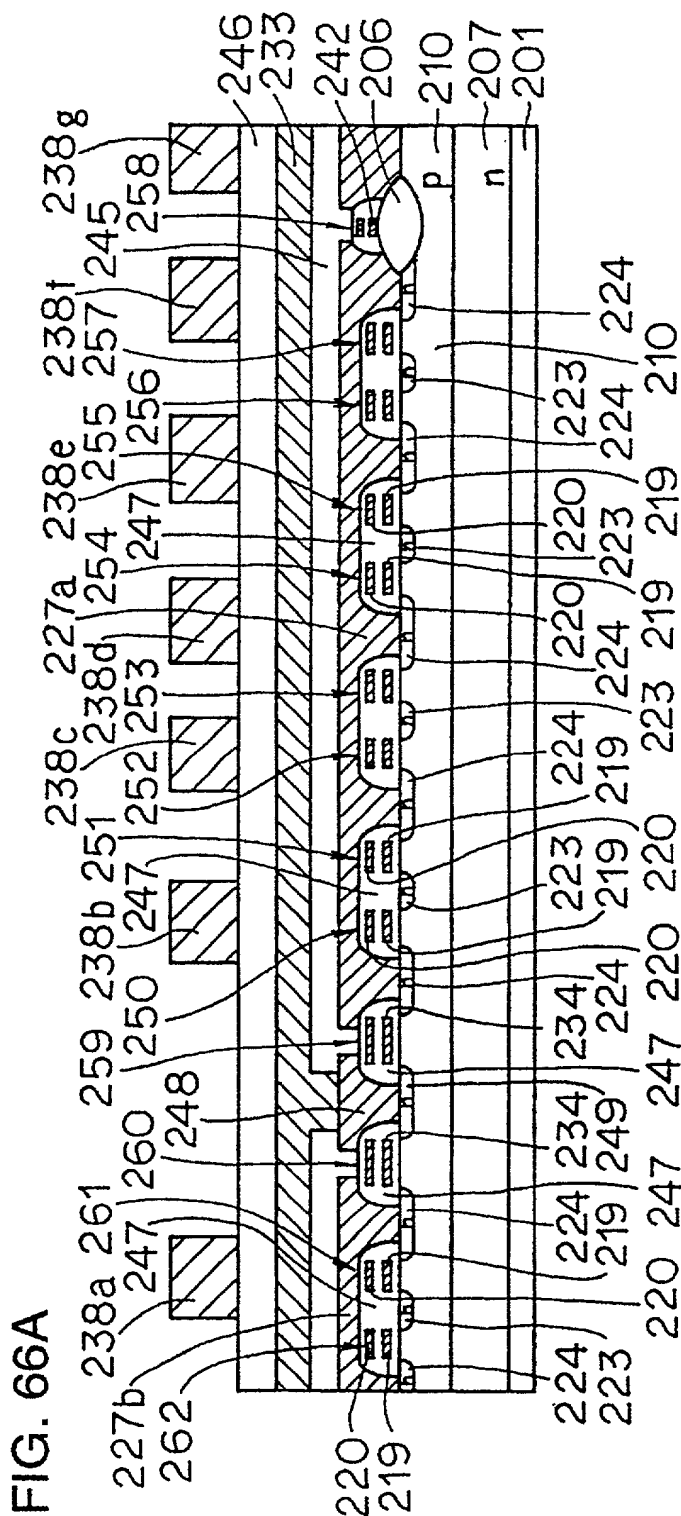


FIG. 67

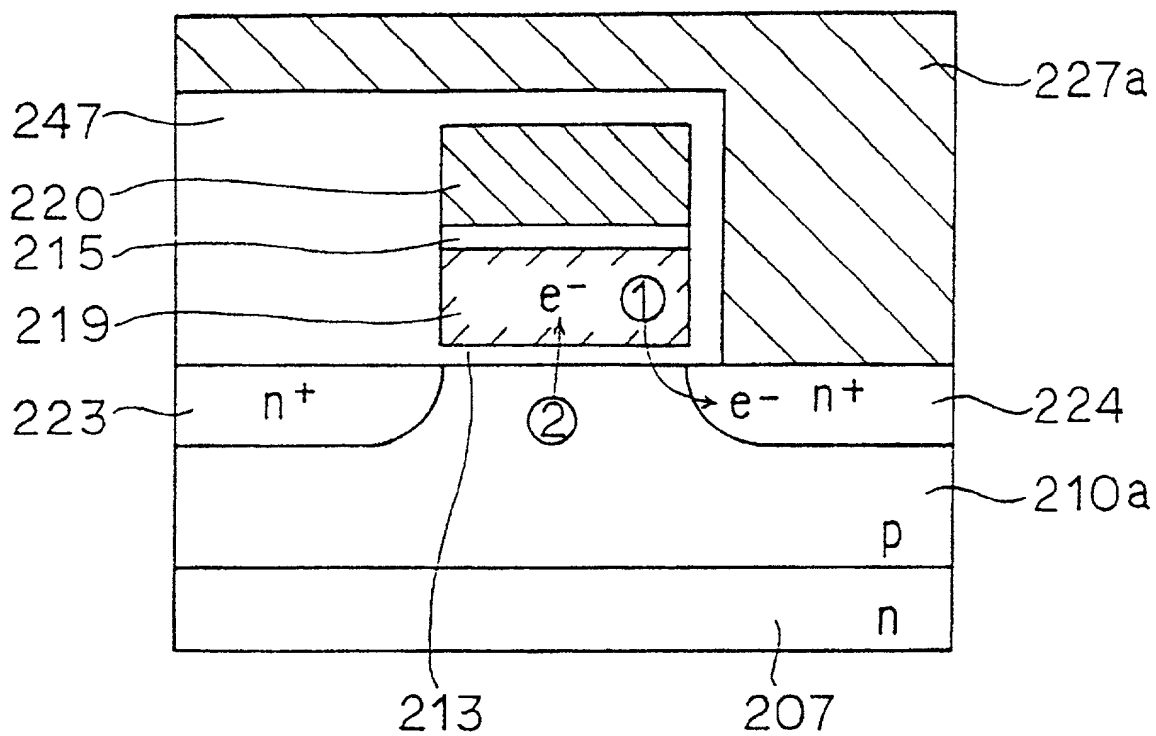




FIG. 68

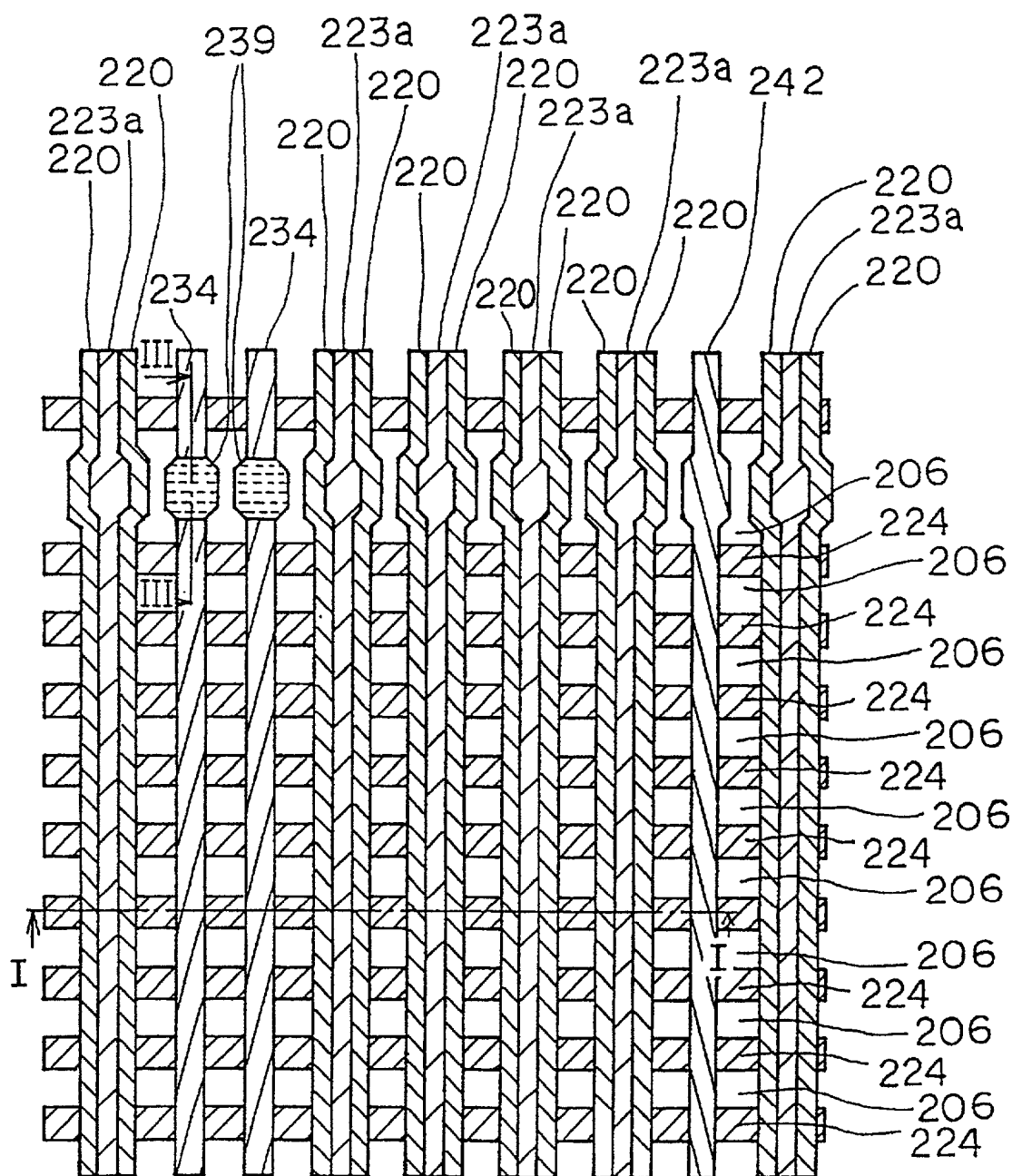


FIG. 69

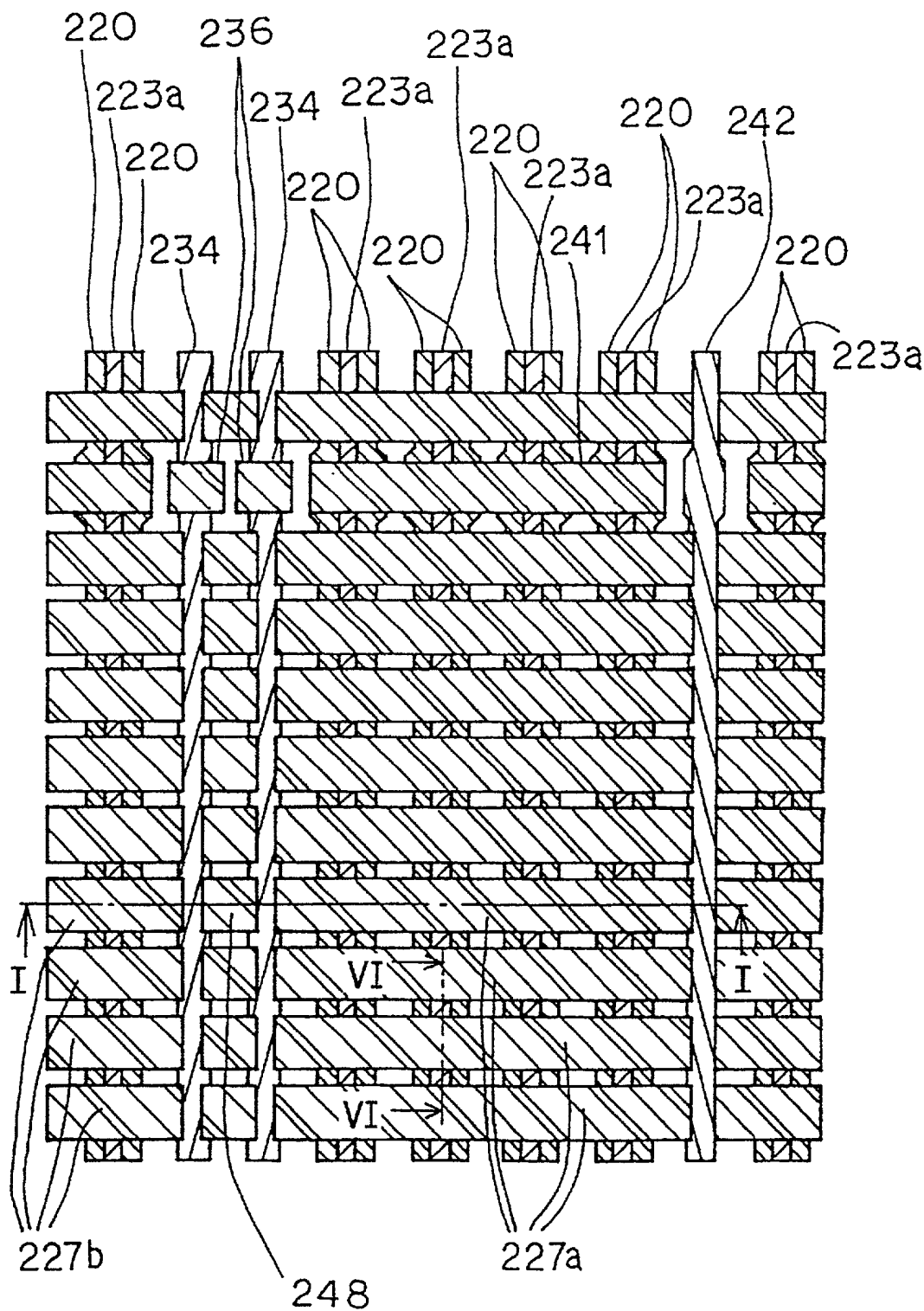


FIG. 70

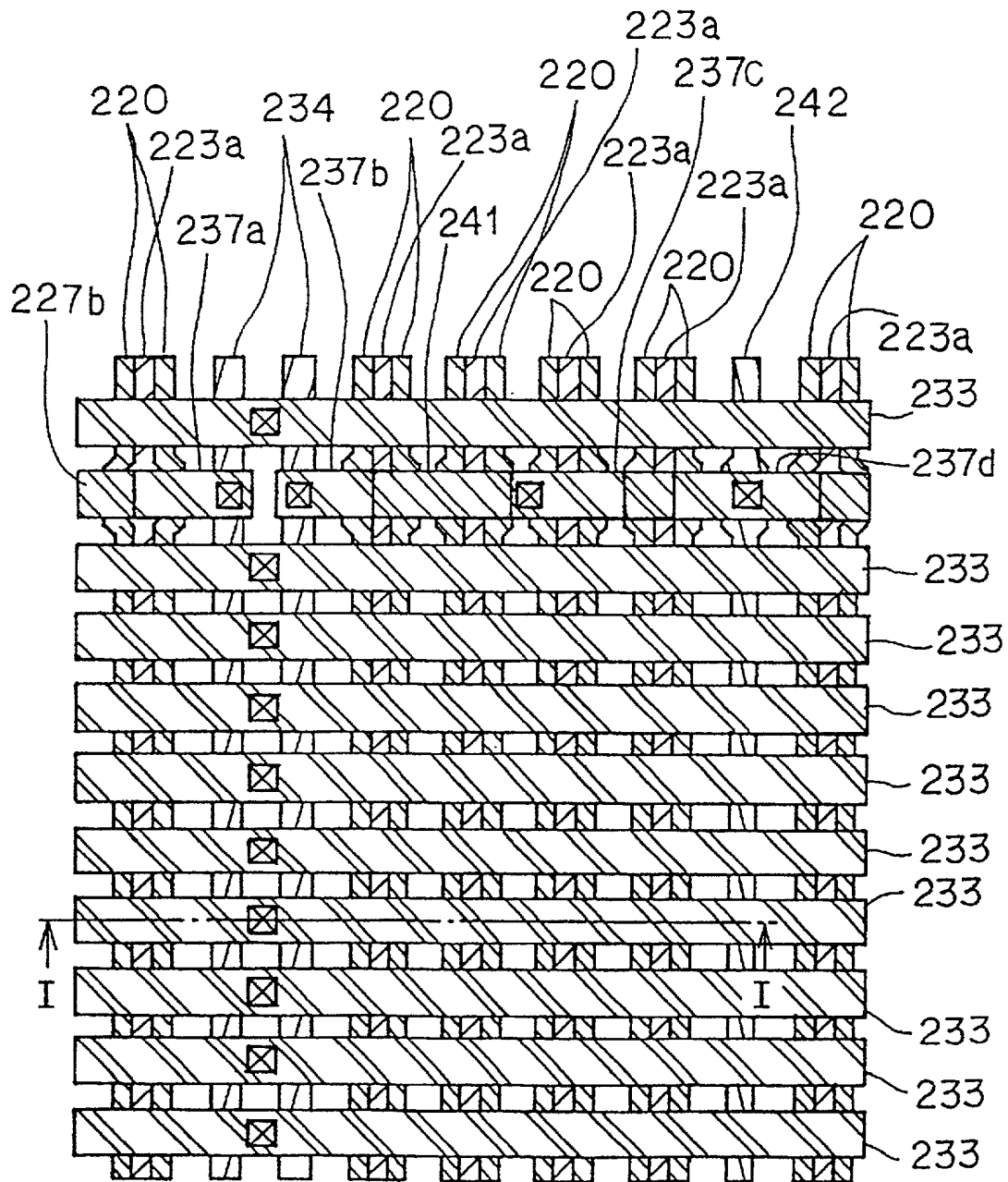
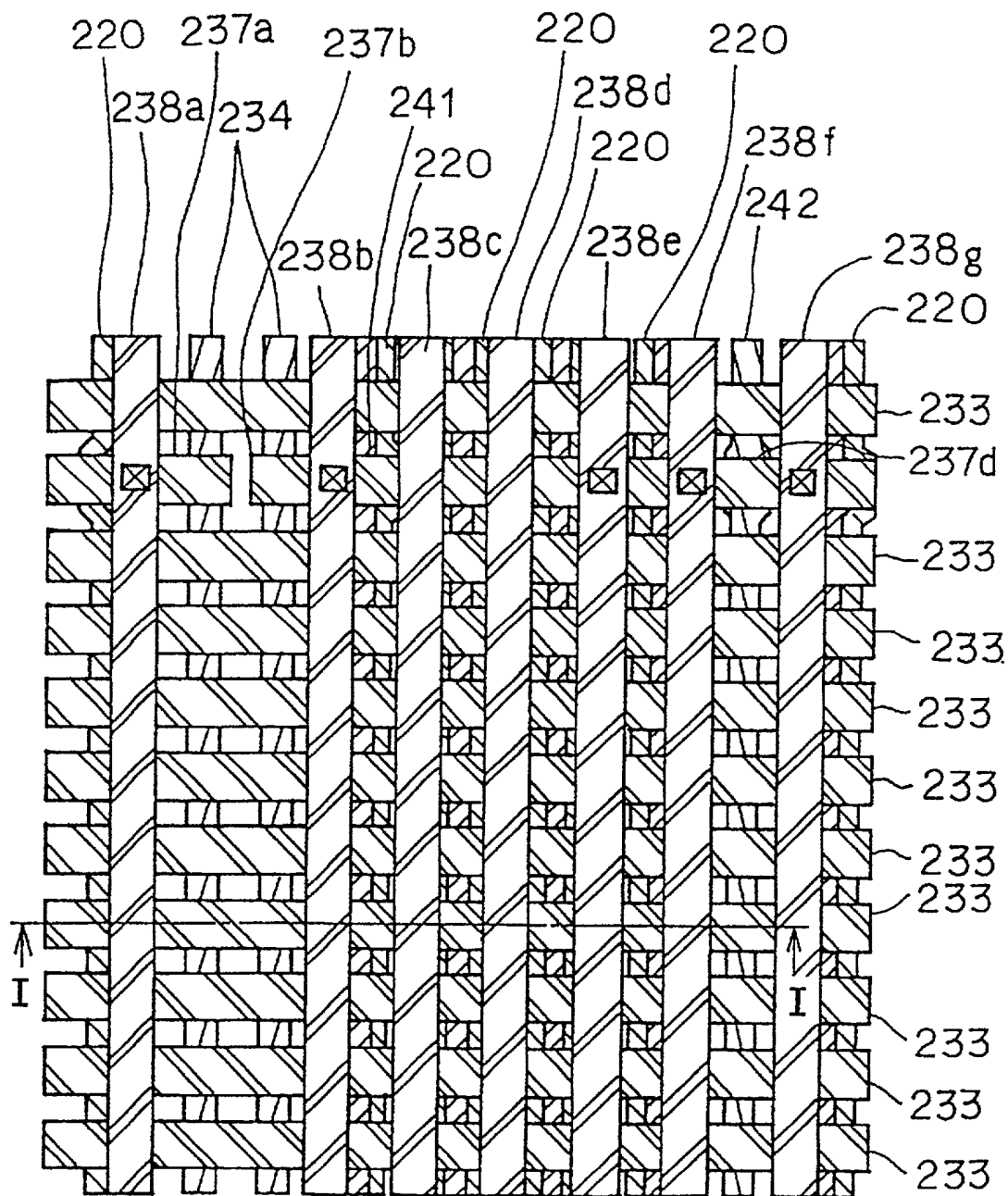


FIG. 71





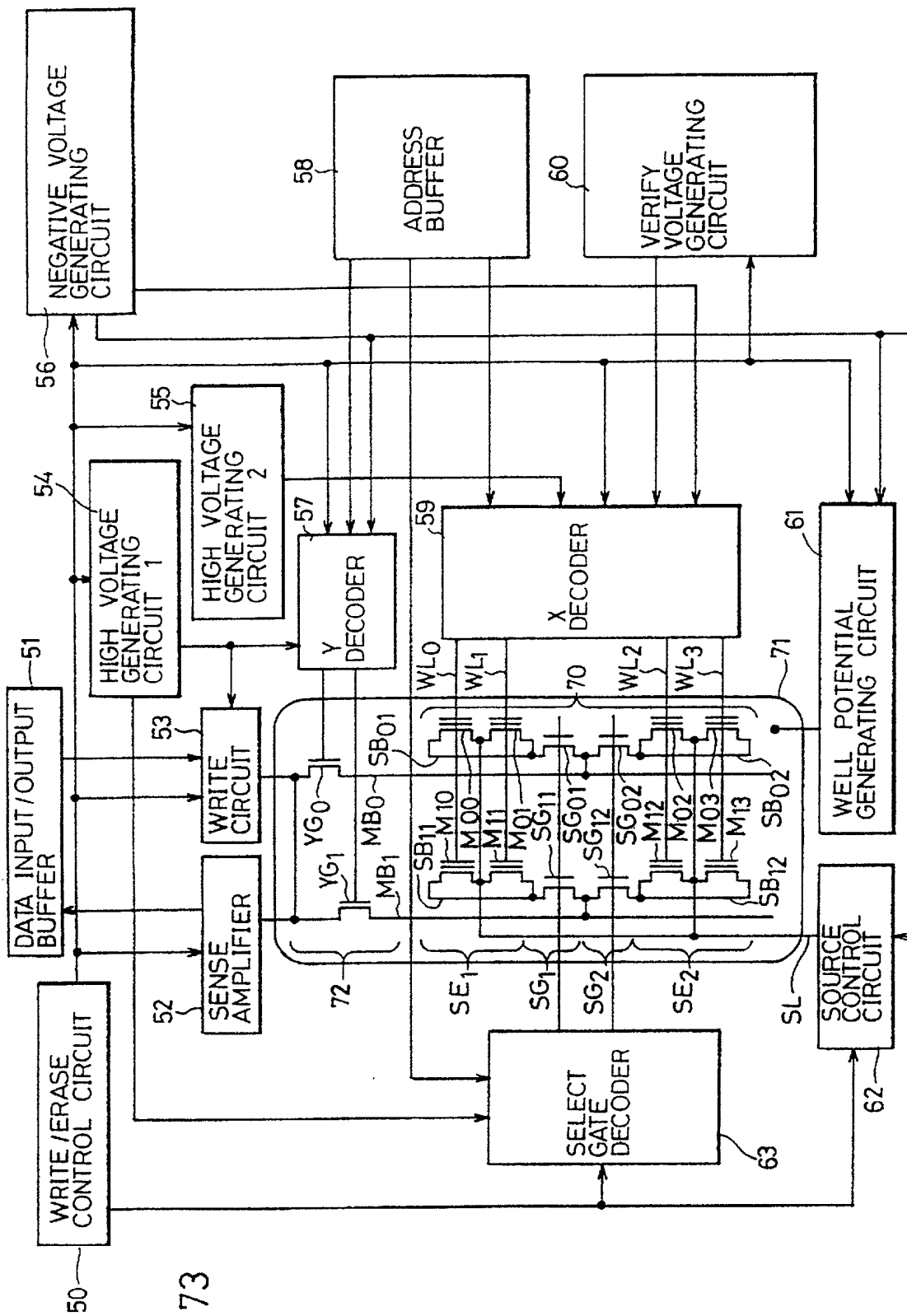
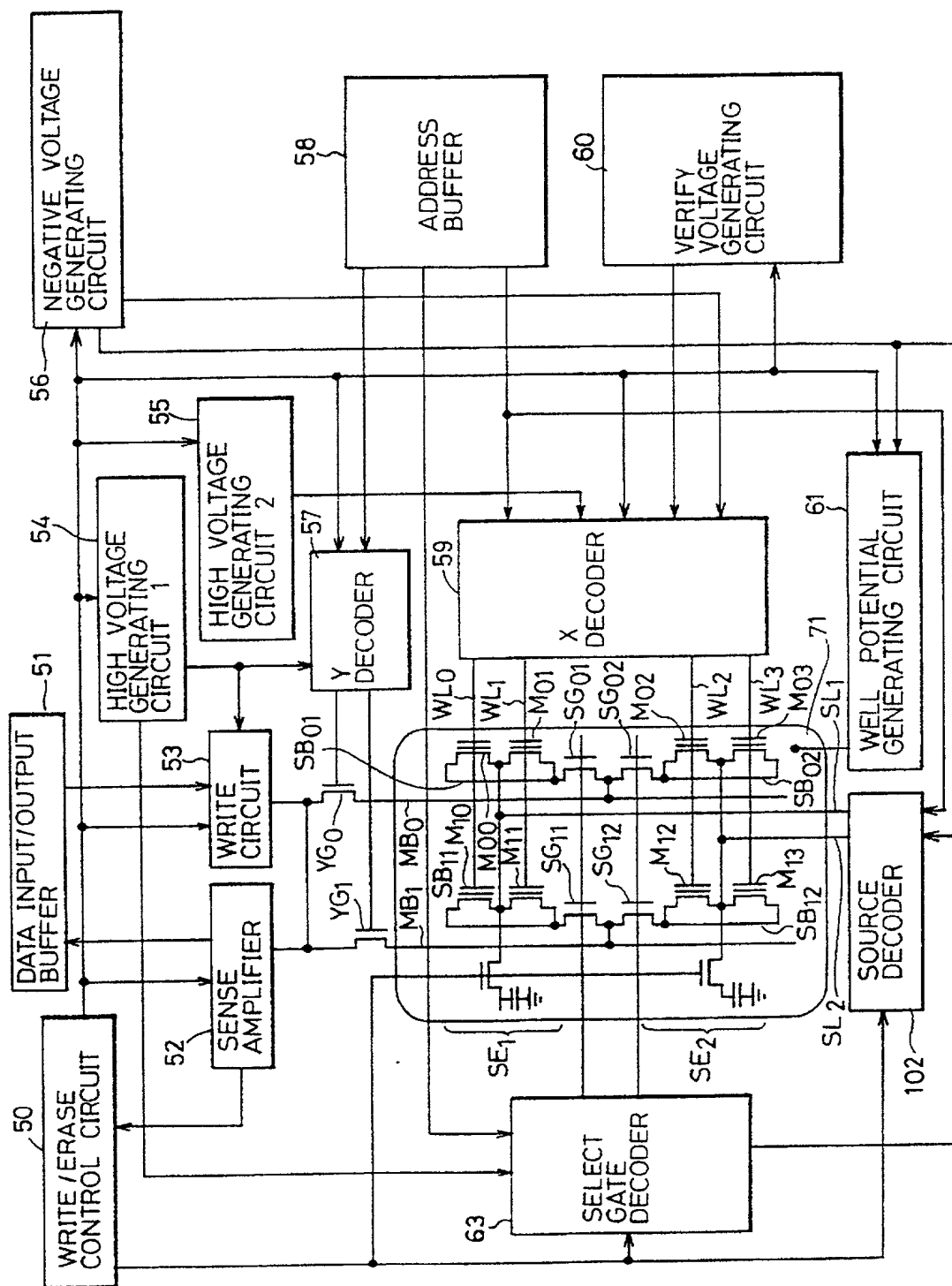


FIG. 73

FIG. 74



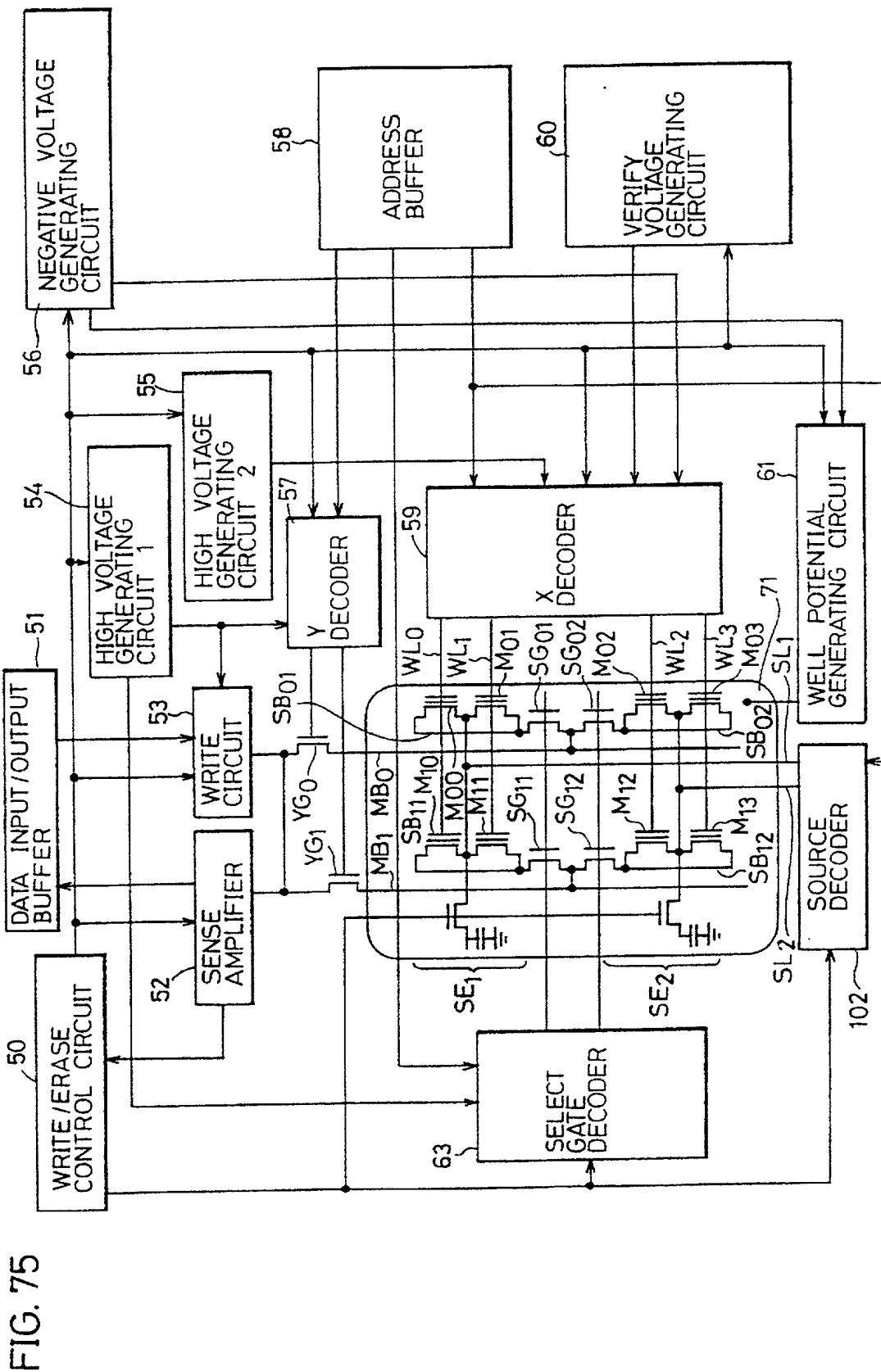




FIG. 76

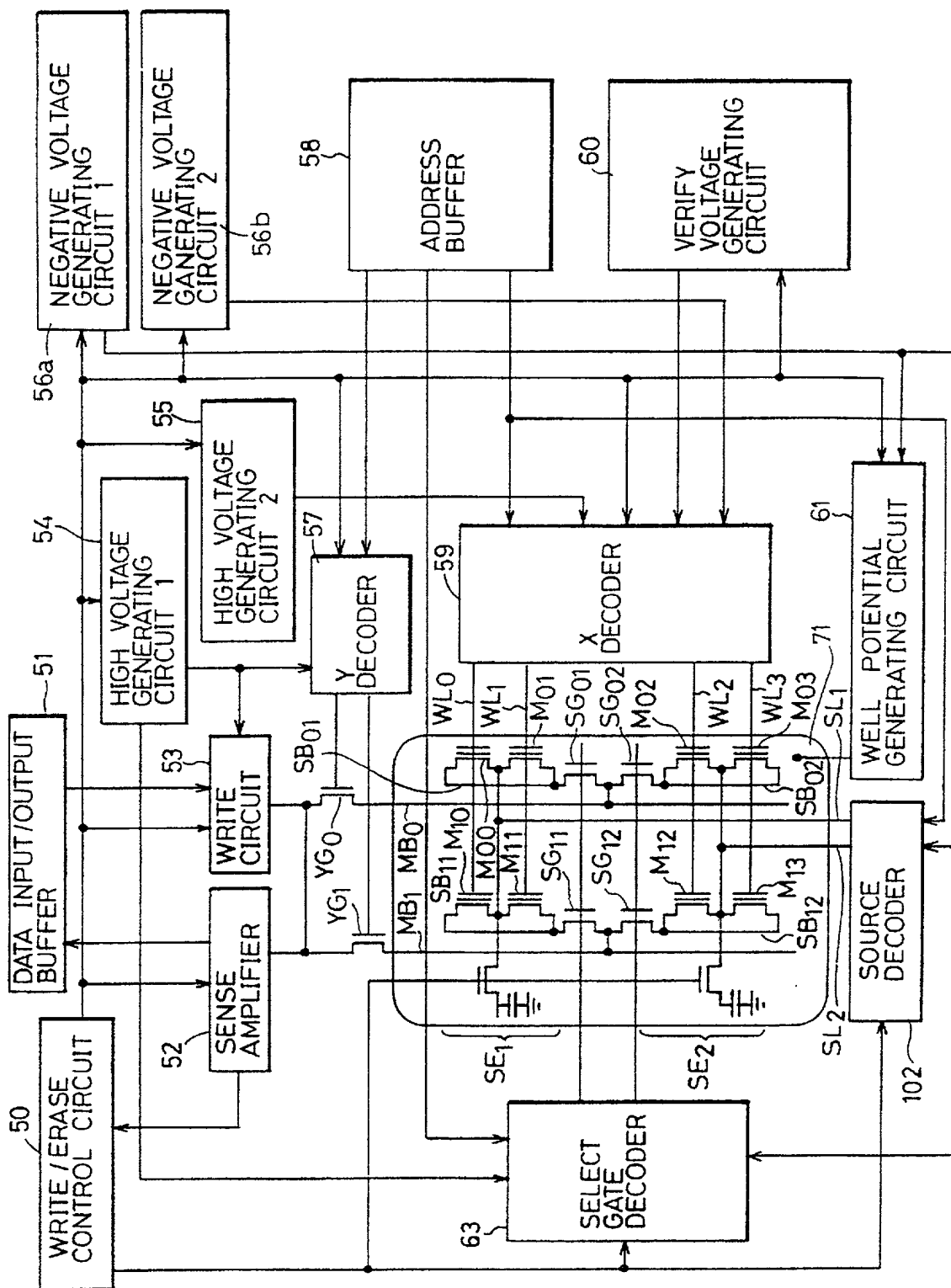


FIG. 77

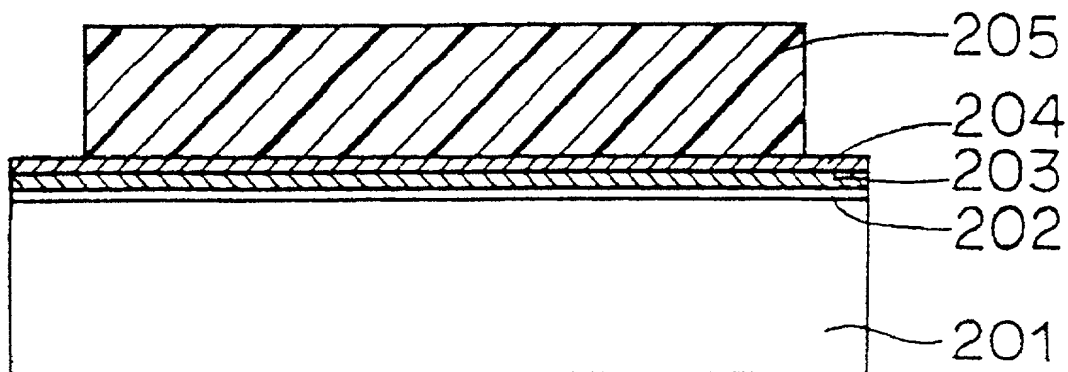


FIG. 78

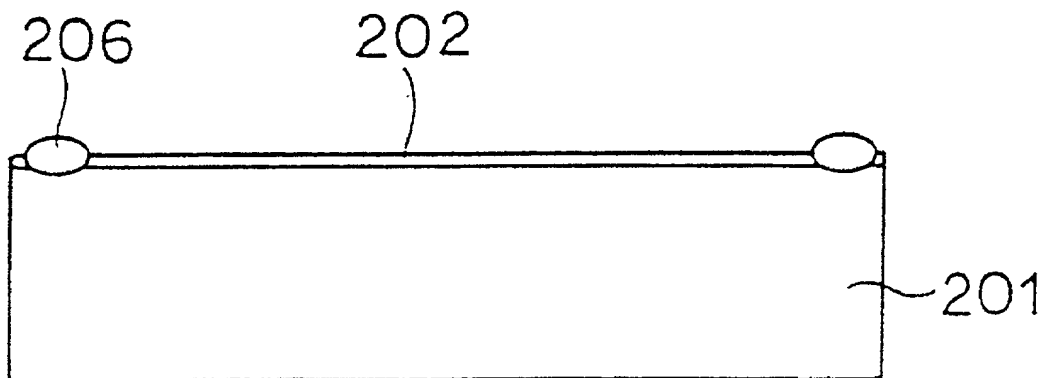


FIG. 79

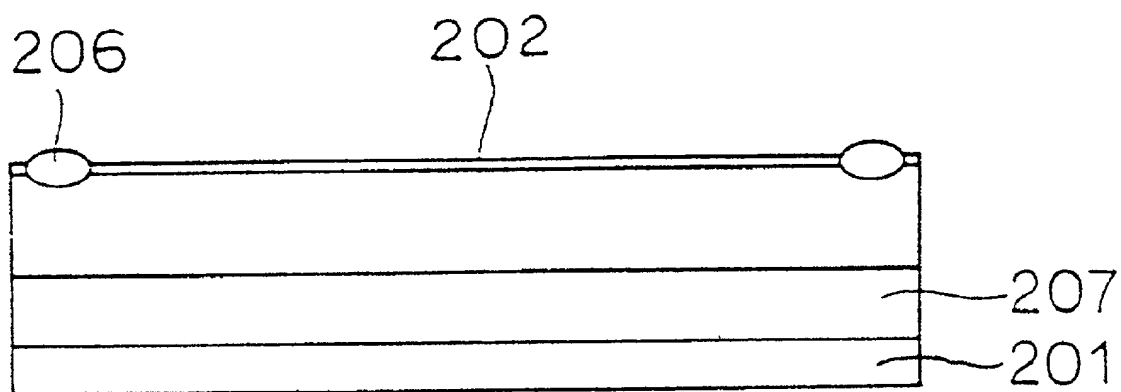


FIG. 80

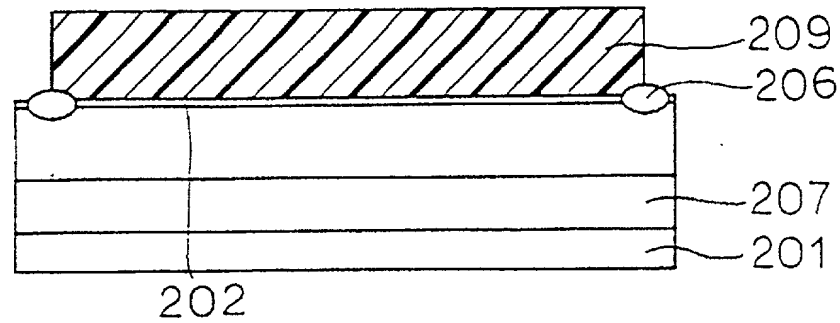


FIG. 81

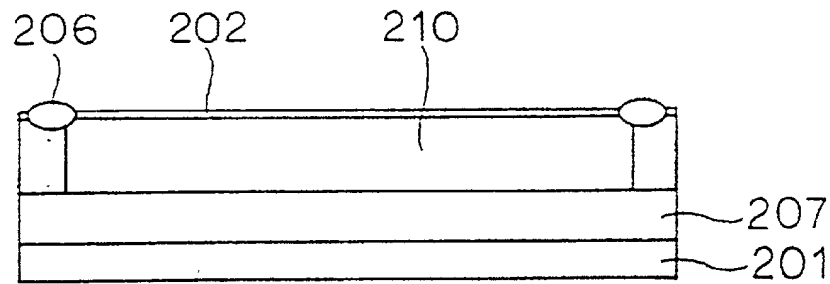


FIG. 82

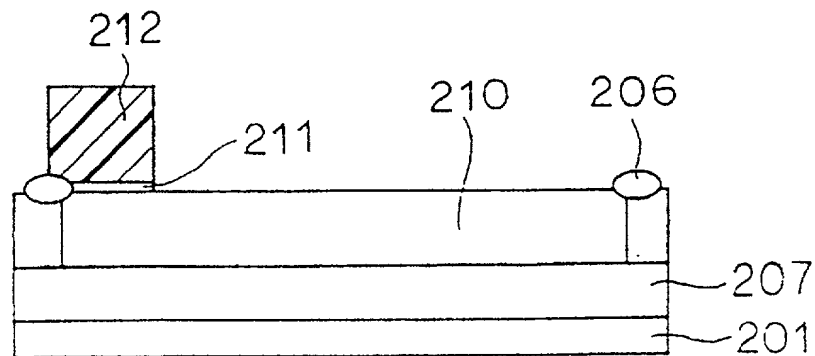


FIG. 83

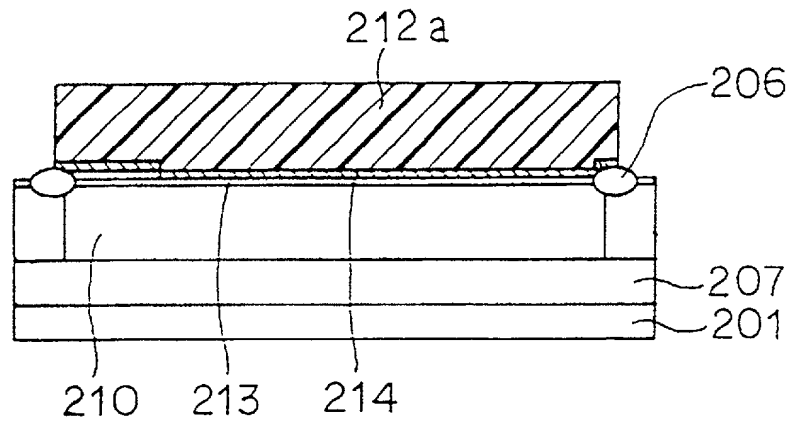


FIG. 84

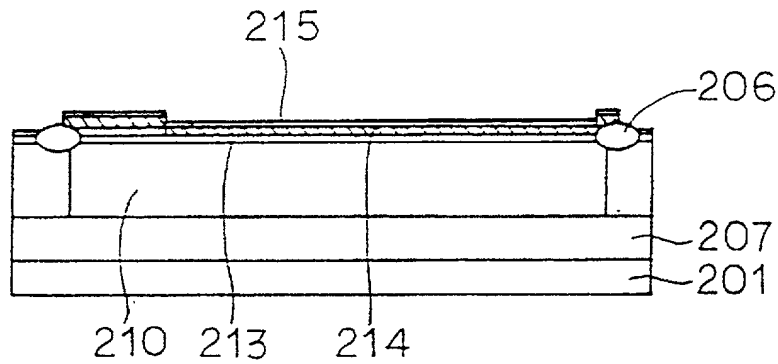


FIG. 85

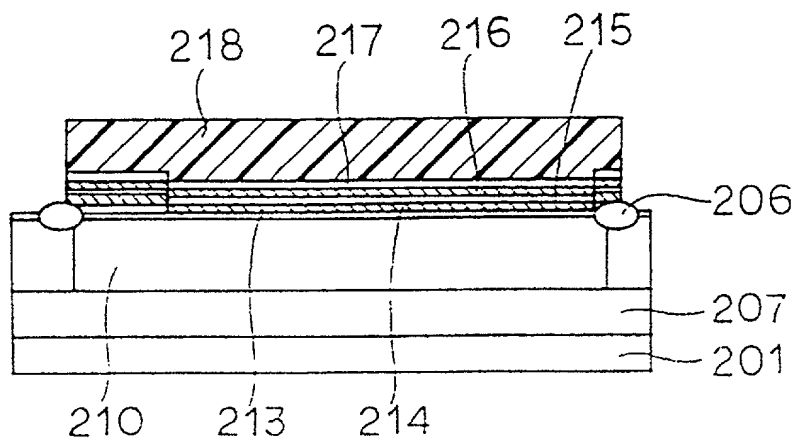


FIG. 86

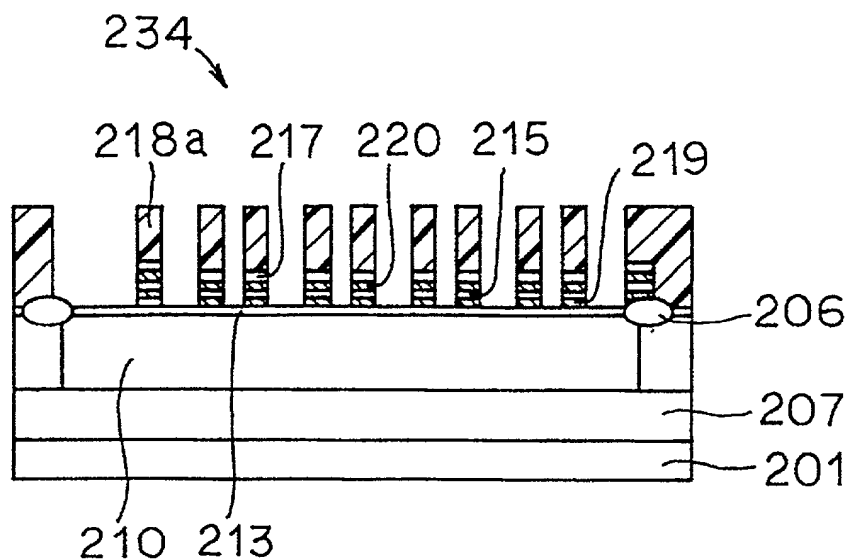


FIG. 87A

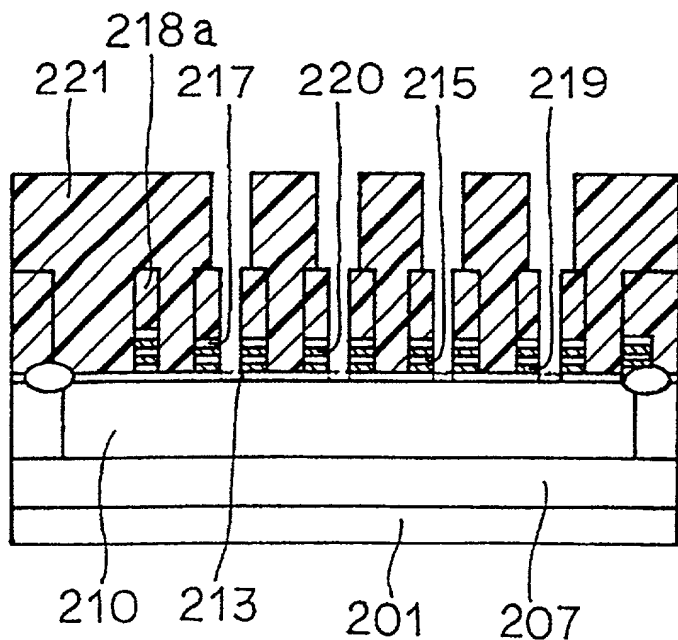


FIG. 87B

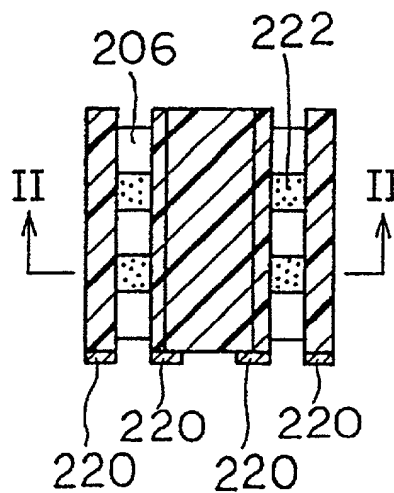


FIG. 88

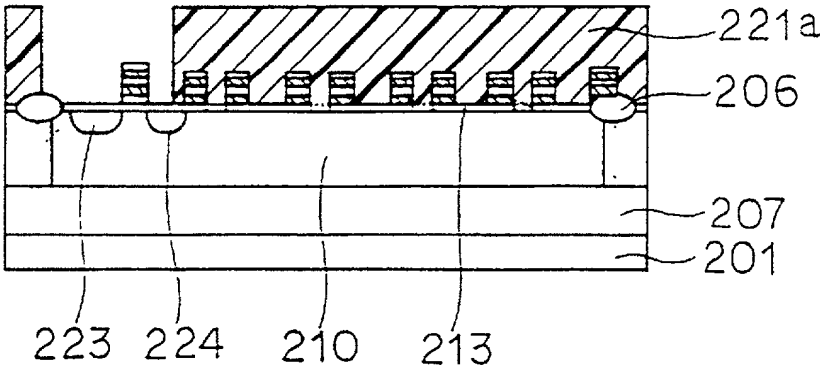


FIG. 89

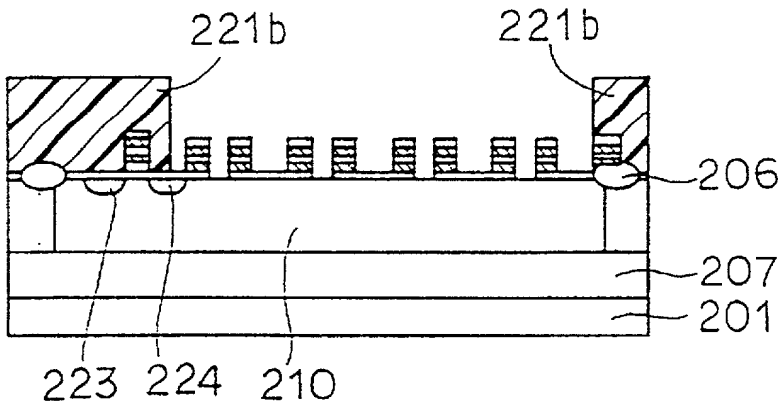


FIG. 90

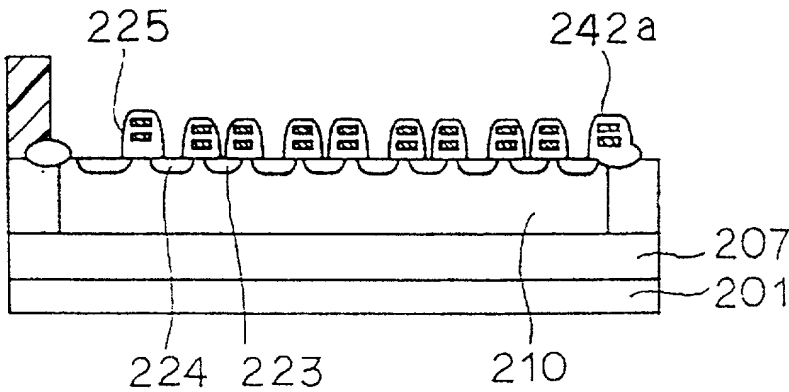


FIG. 91

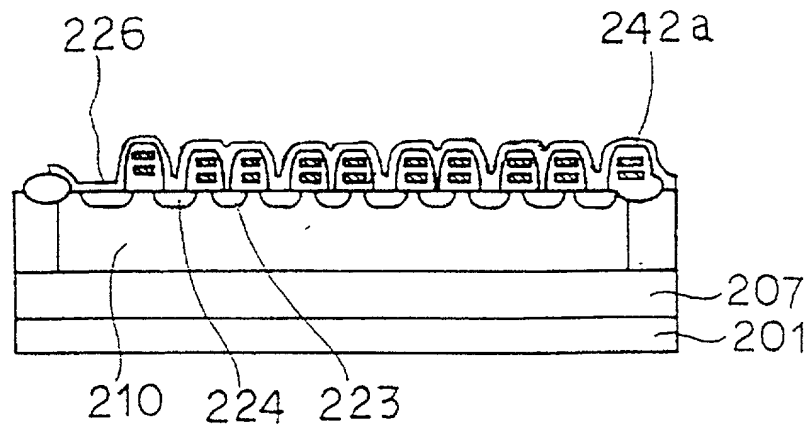


FIG. 92

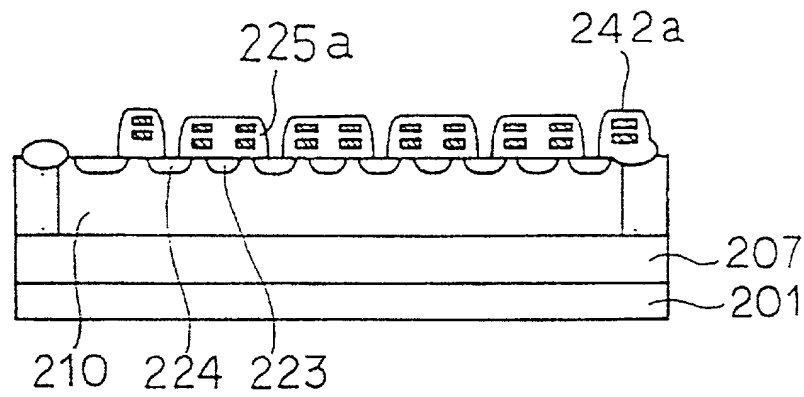


FIG. 93

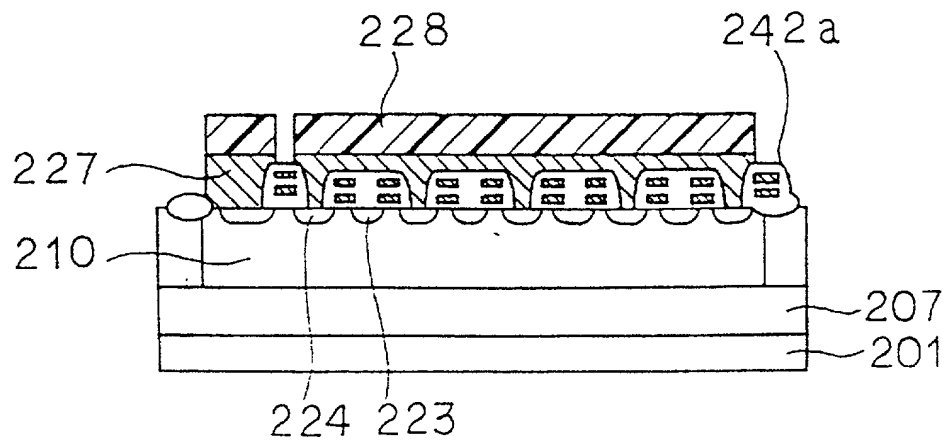




FIG. 94

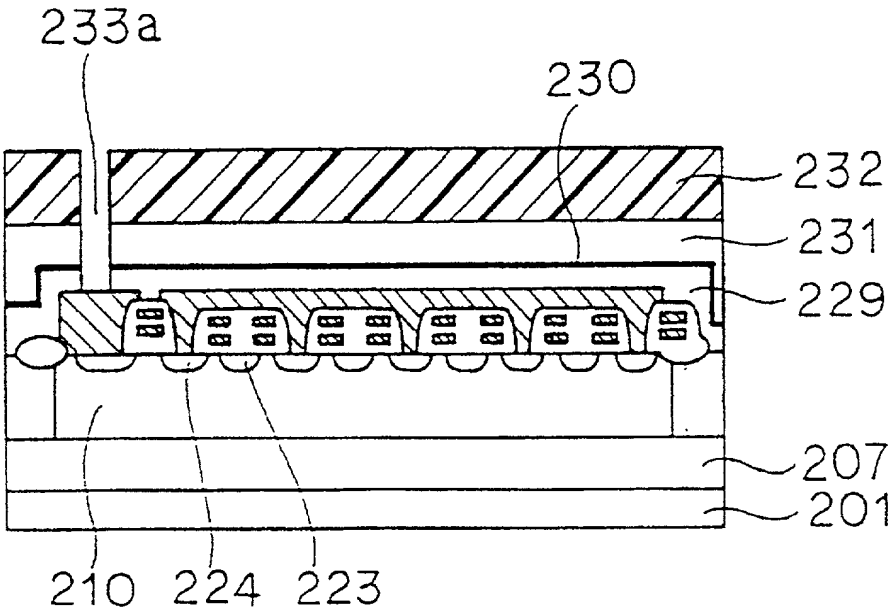


FIG. 95

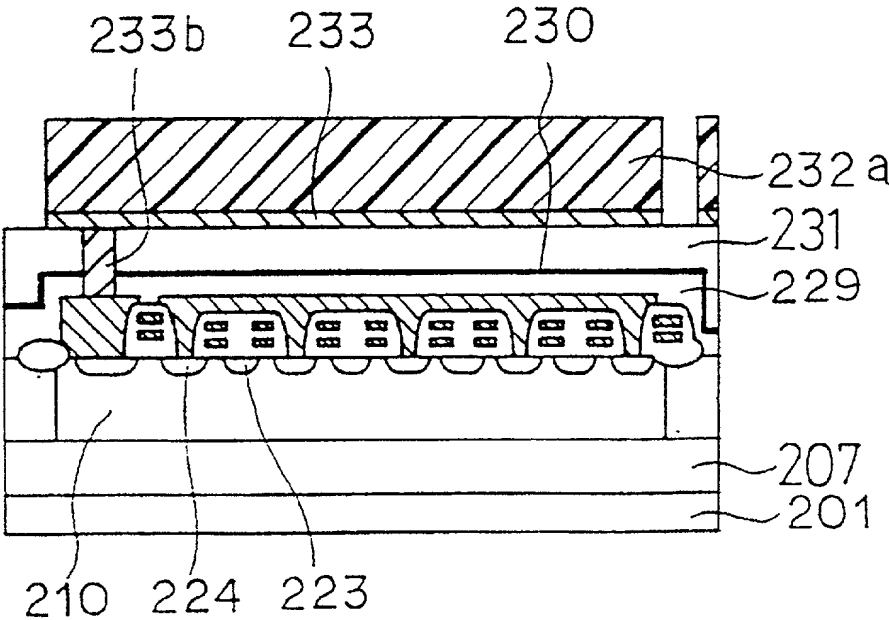


FIG. 96

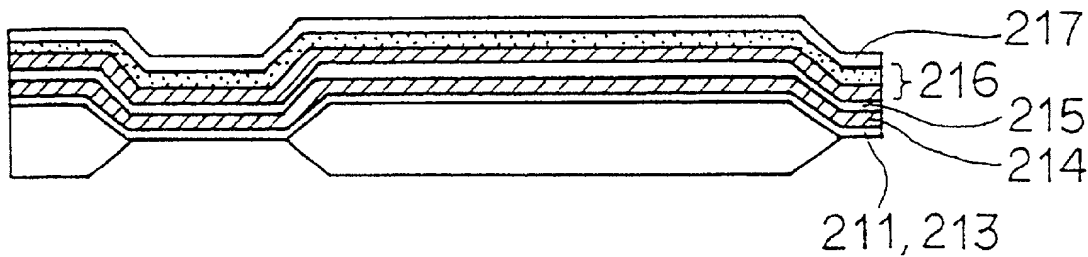


FIG. 97

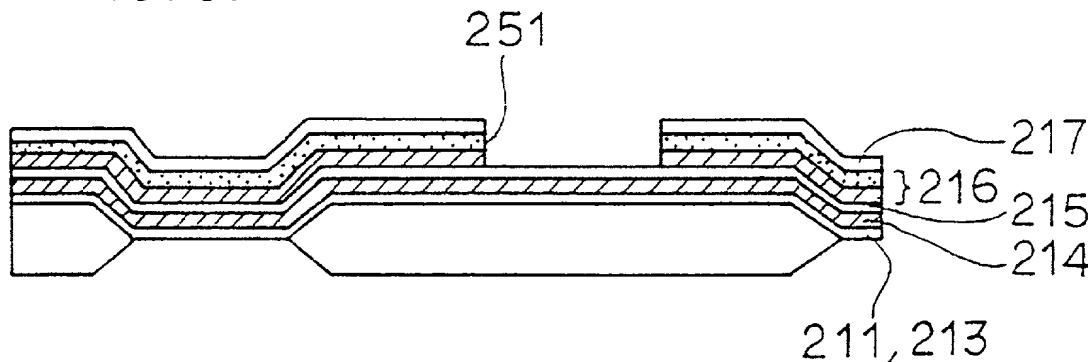


FIG. 98

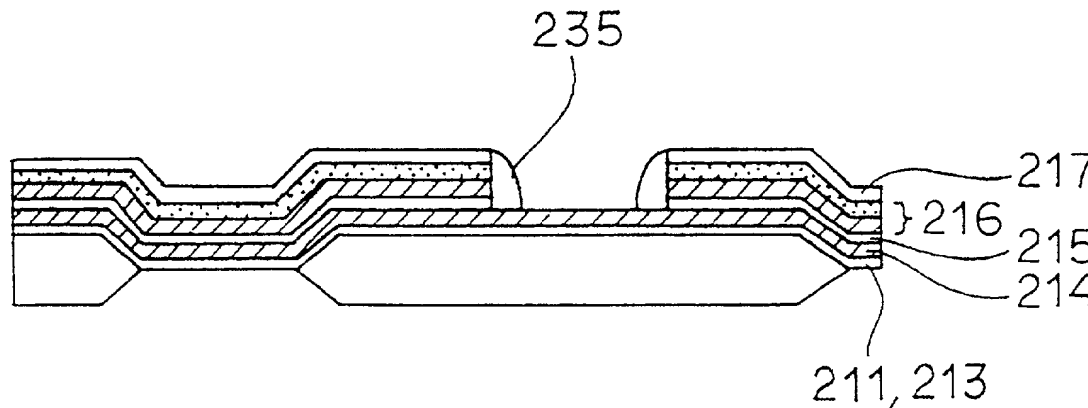


FIG. 99

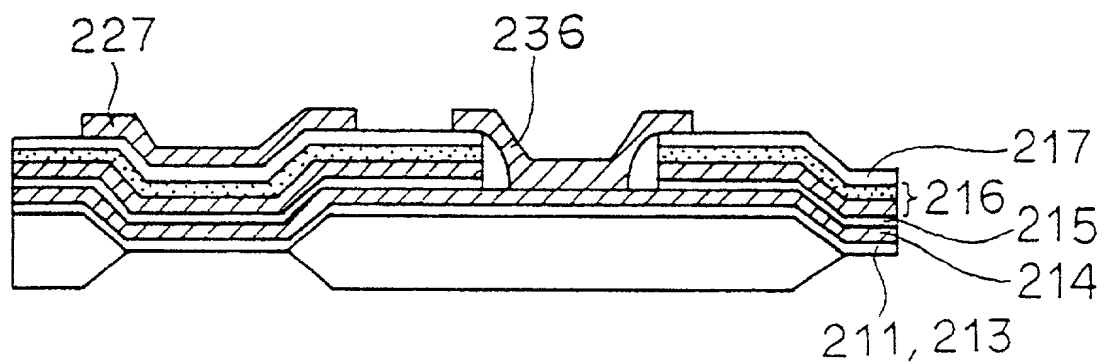


FIG. 100

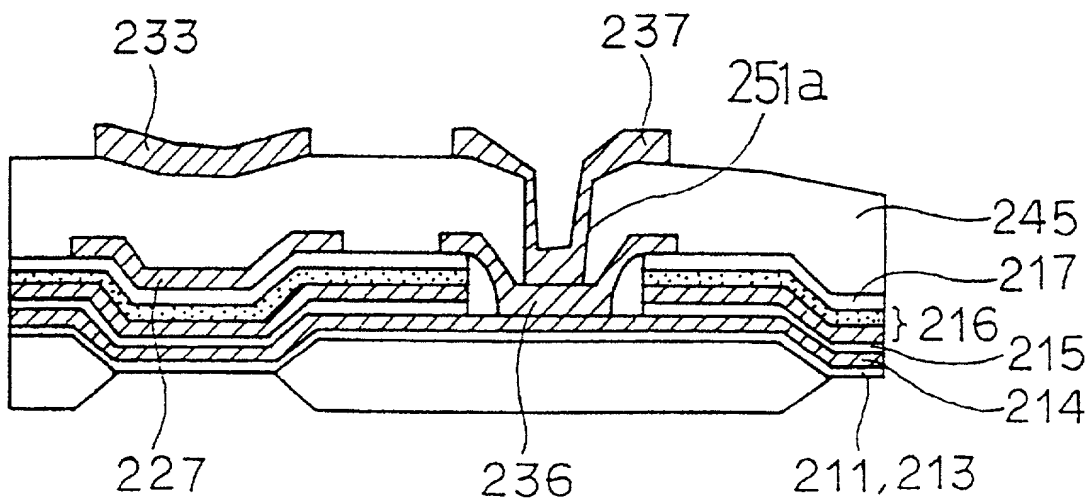


FIG. 101

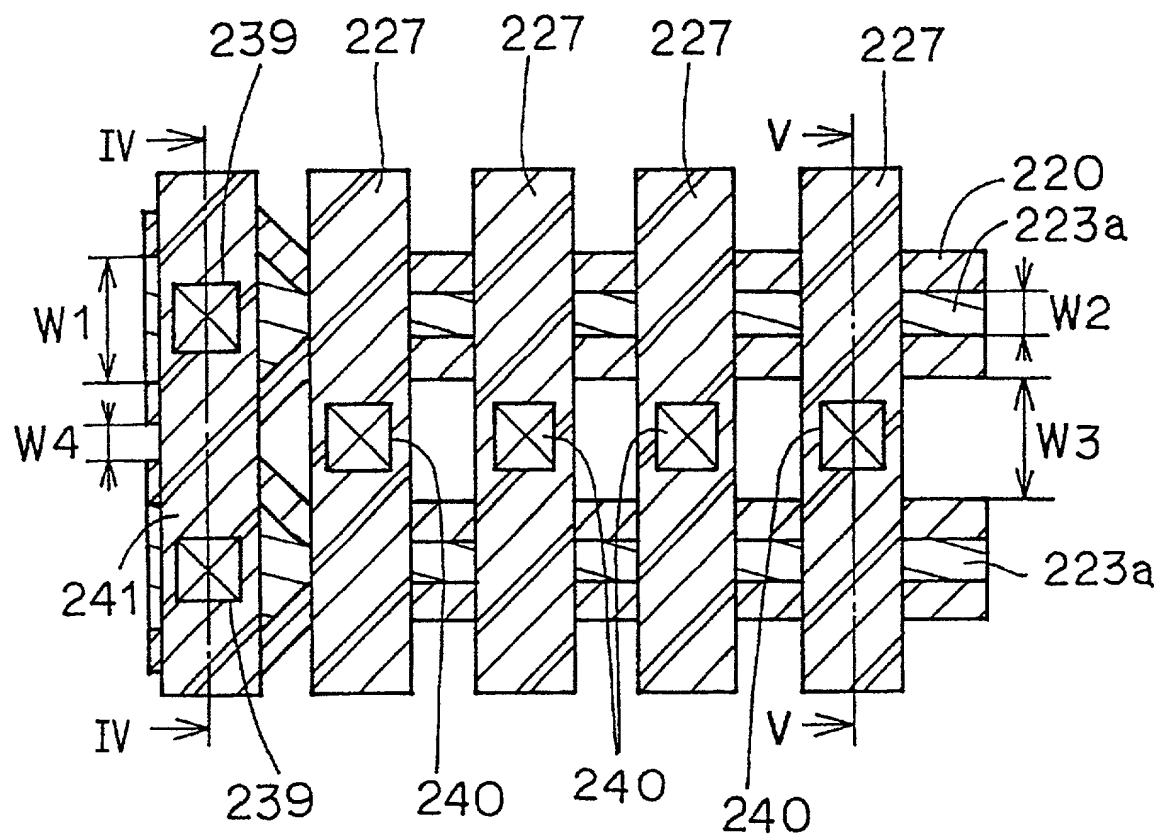


FIG. 102A

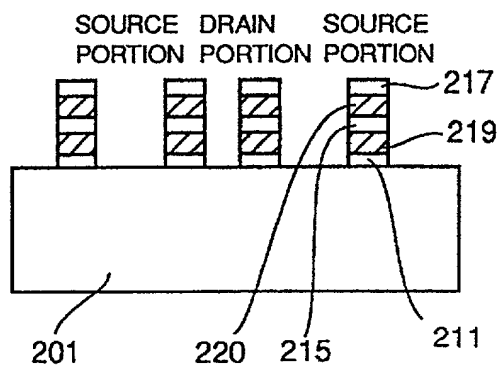


FIG. 102B

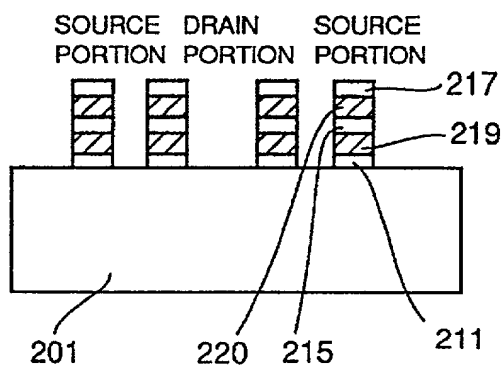


FIG. 103A

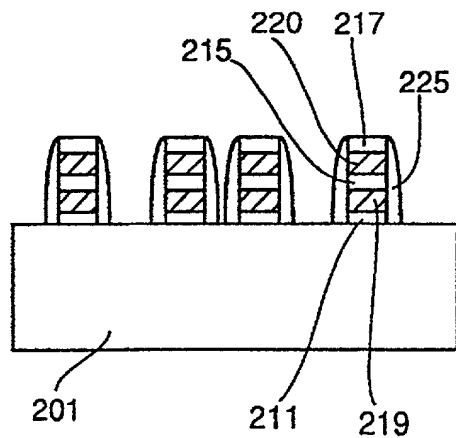


FIG. 103B

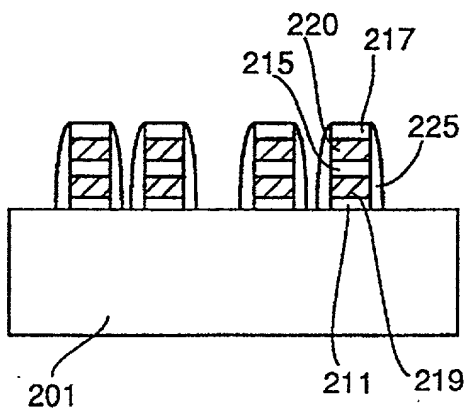


FIG. 104A

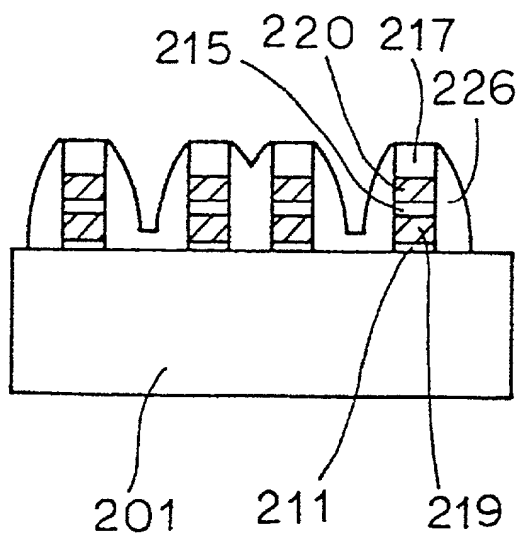


FIG. 104B

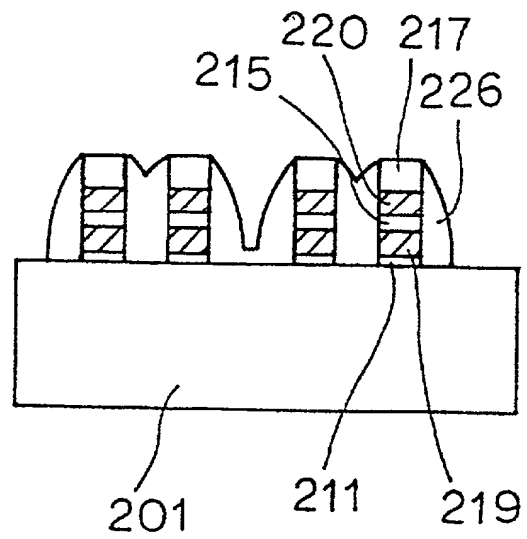


FIG. 105A

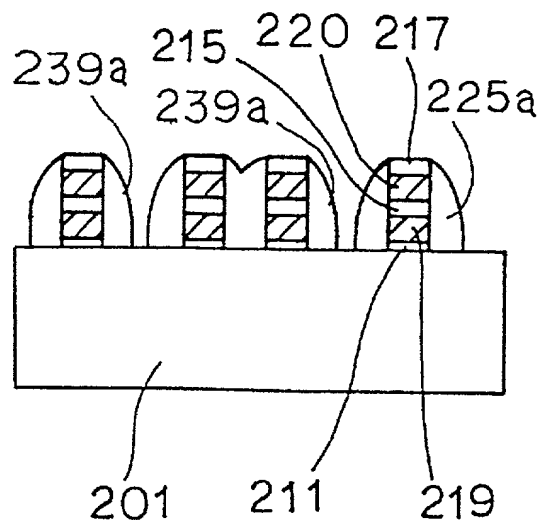


FIG. 105B

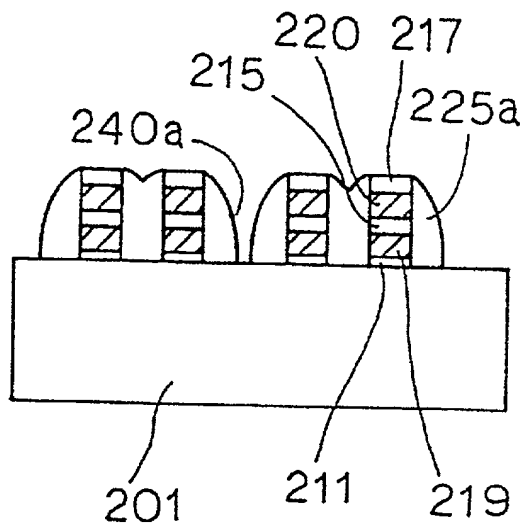


FIG. 106A

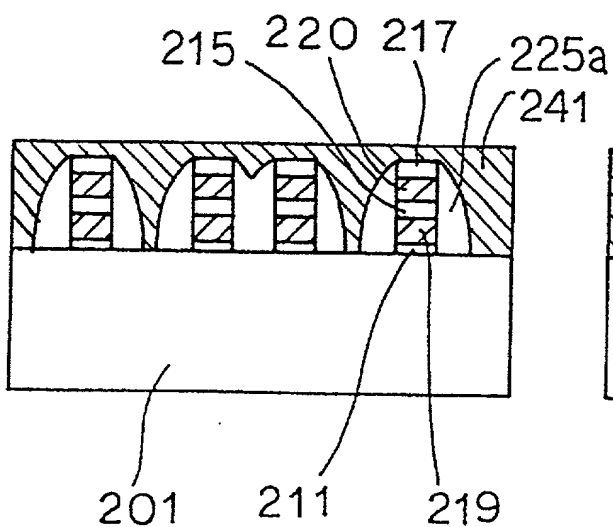


FIG. 106B

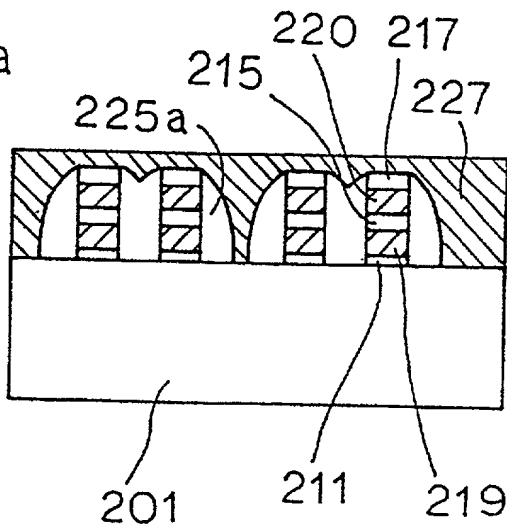


FIG. 107A

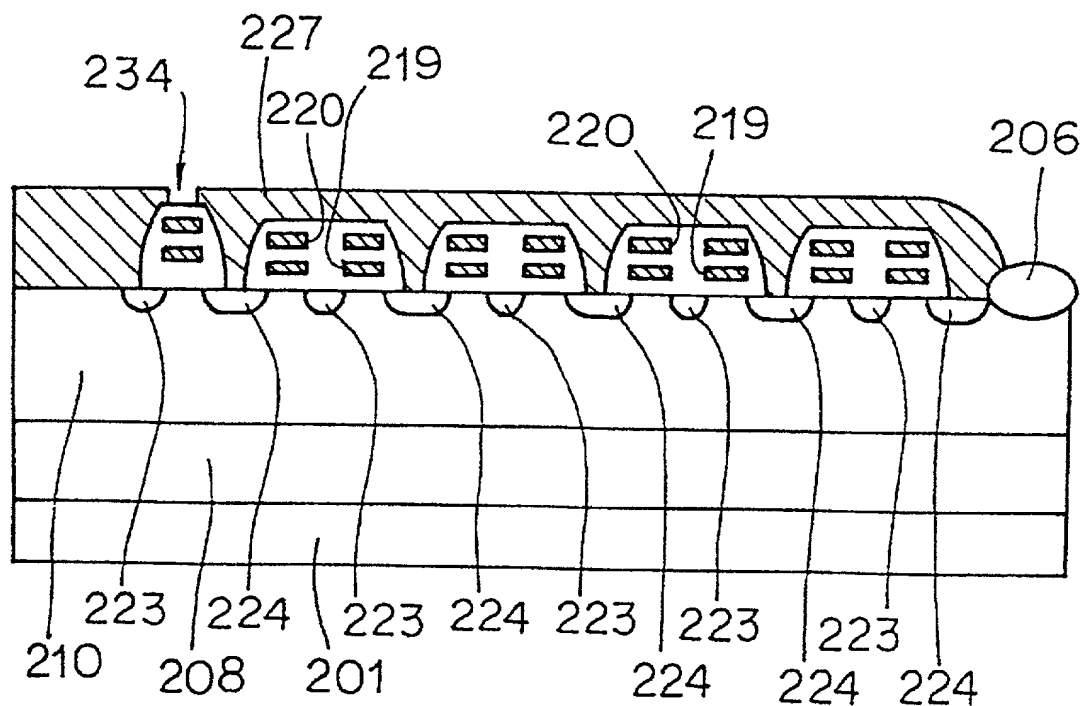


FIG. 107B

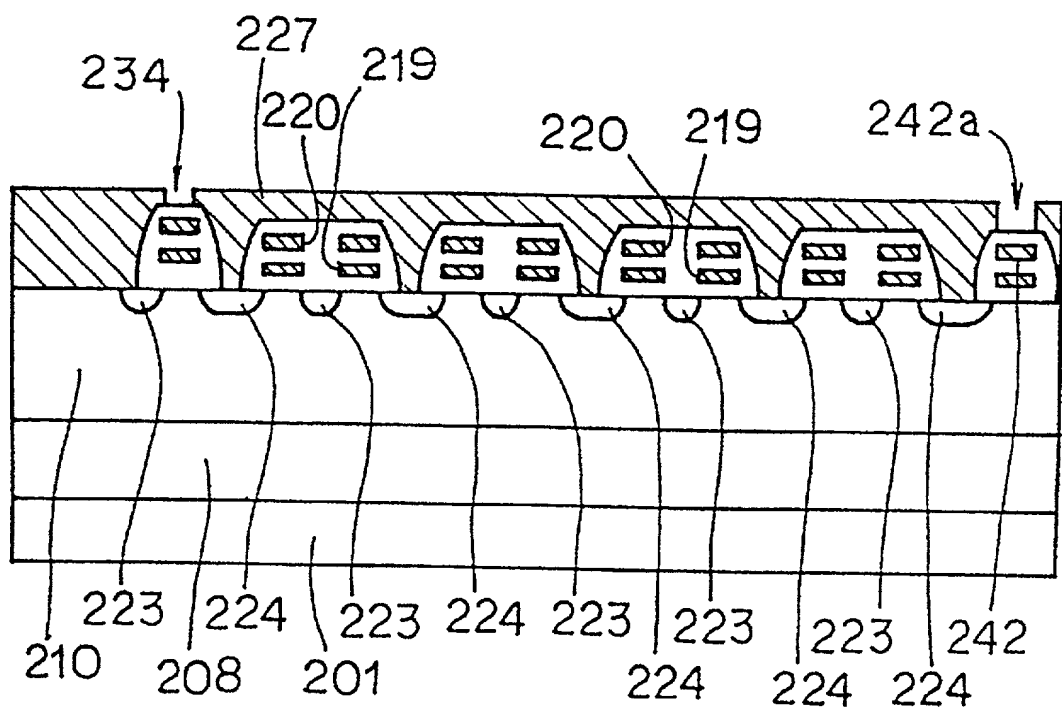






FIG. 109

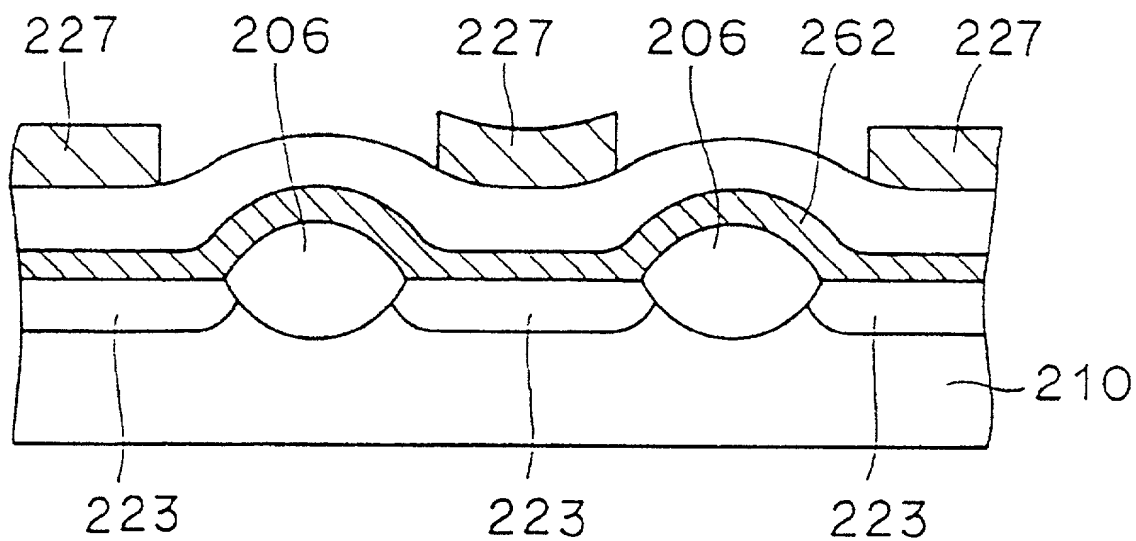


FIG. 110

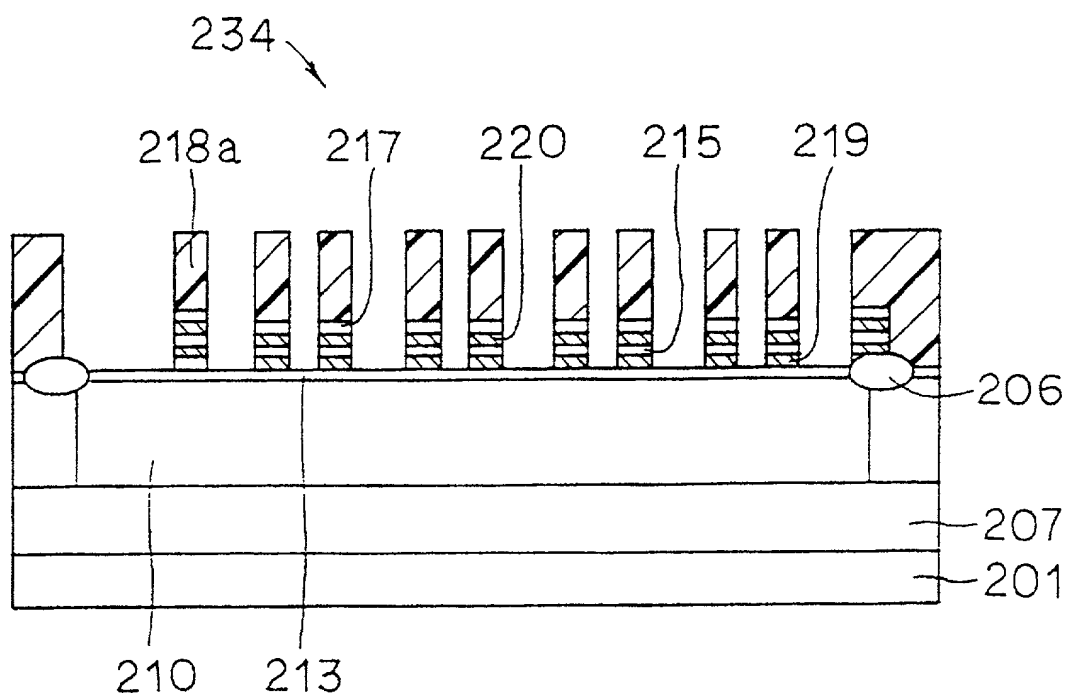


FIG. 111

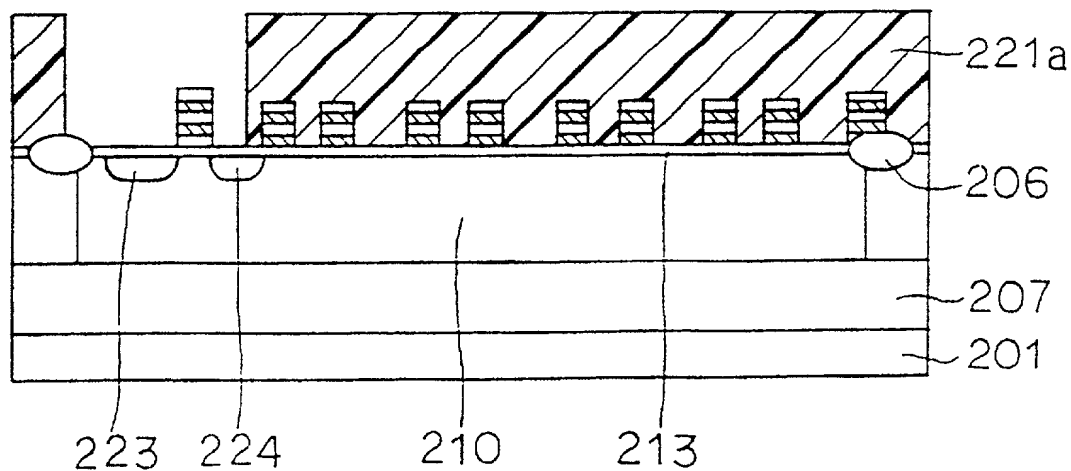


FIG. 112

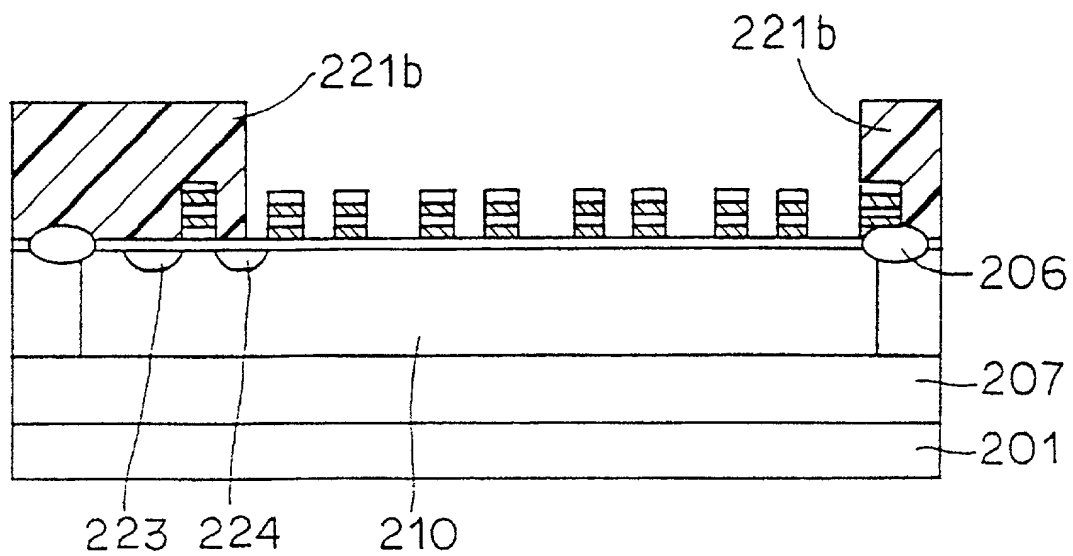


FIG. 113

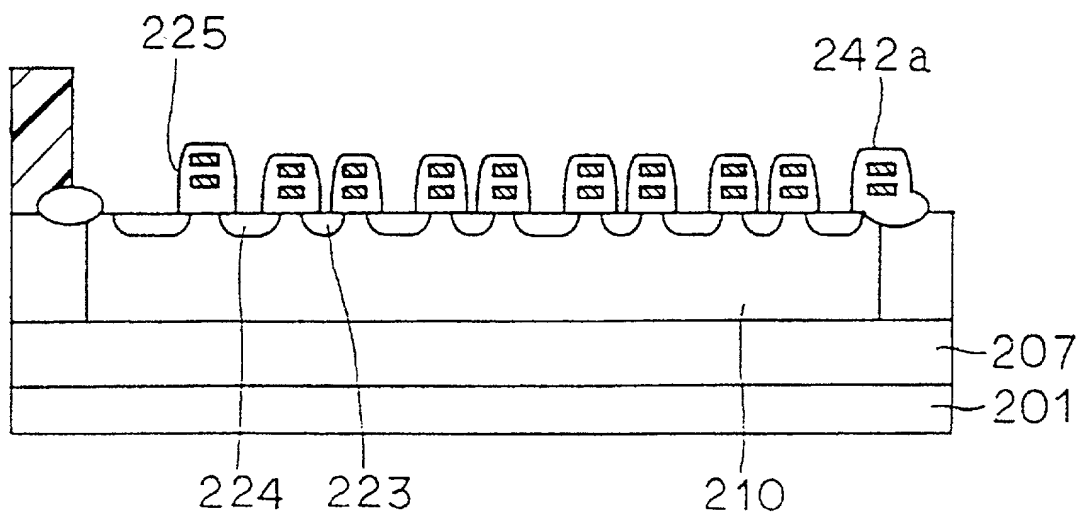


FIG. 114

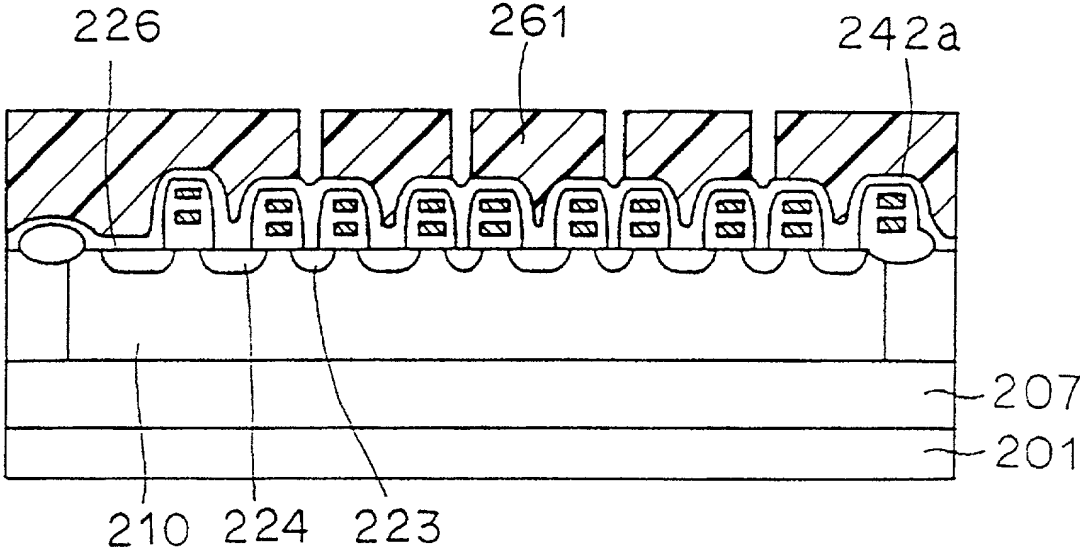


FIG. 115

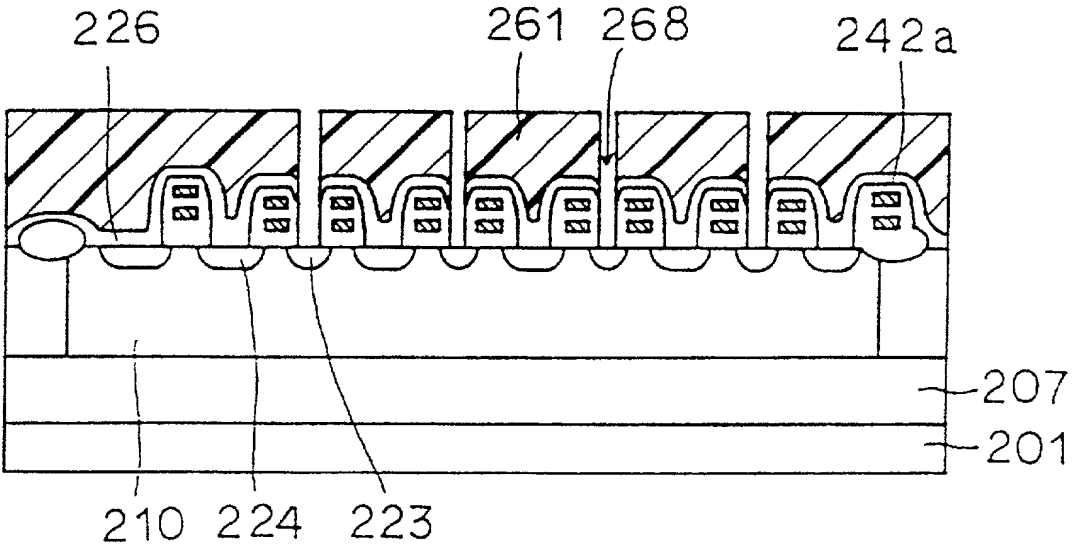


FIG. 116

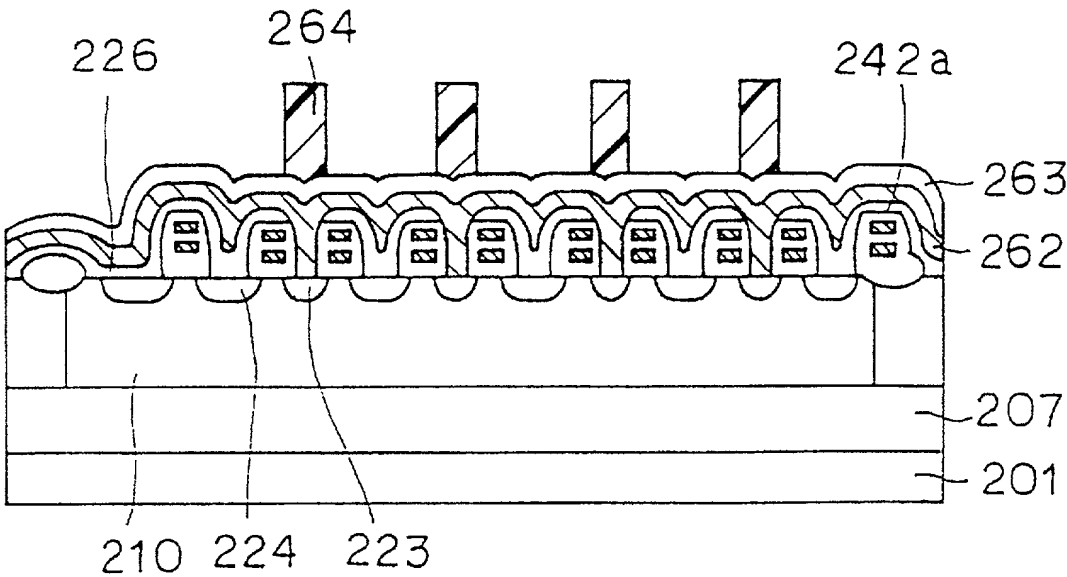


FIG. 117

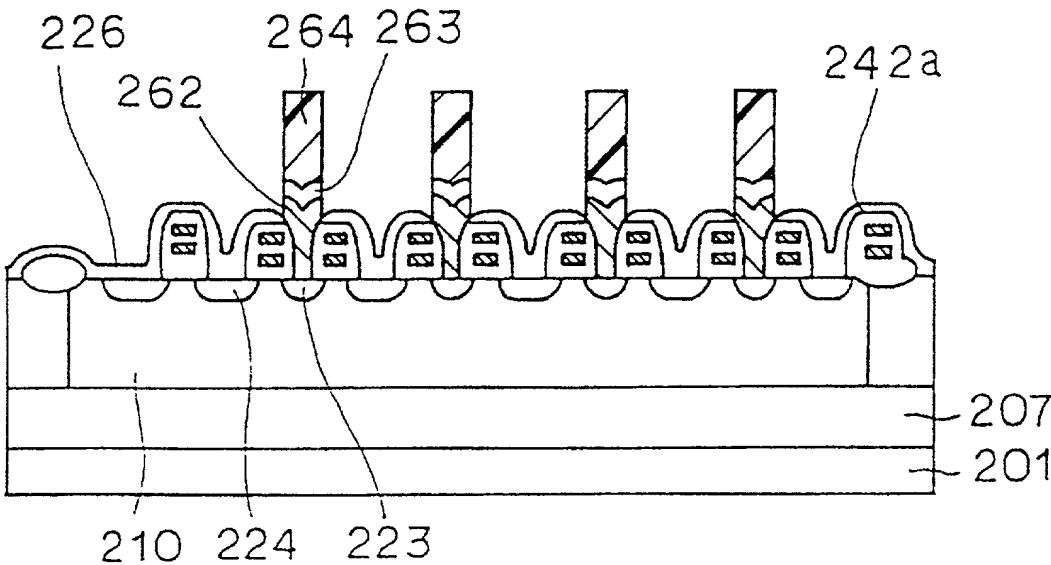


FIG. 118

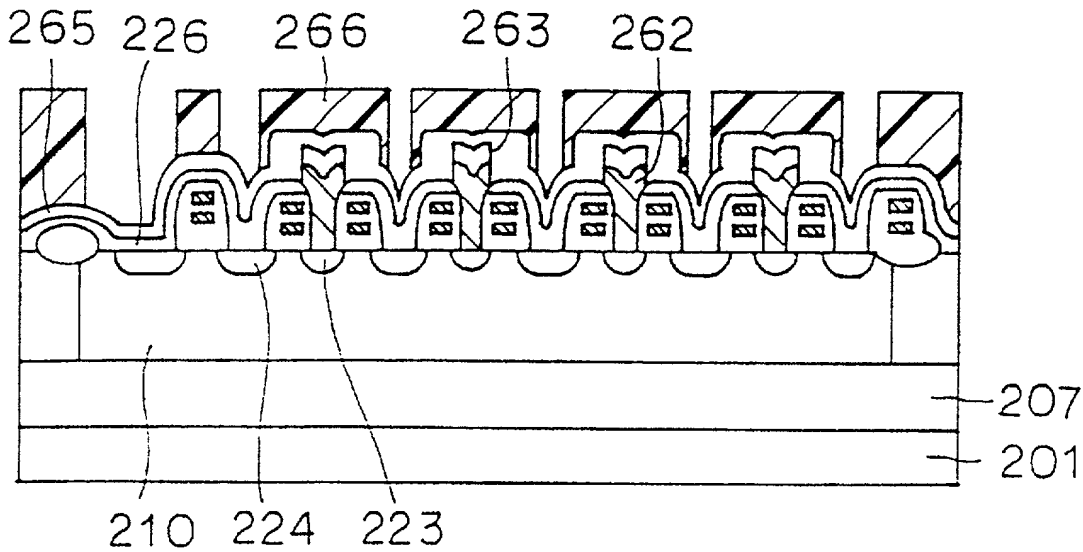


FIG. 119

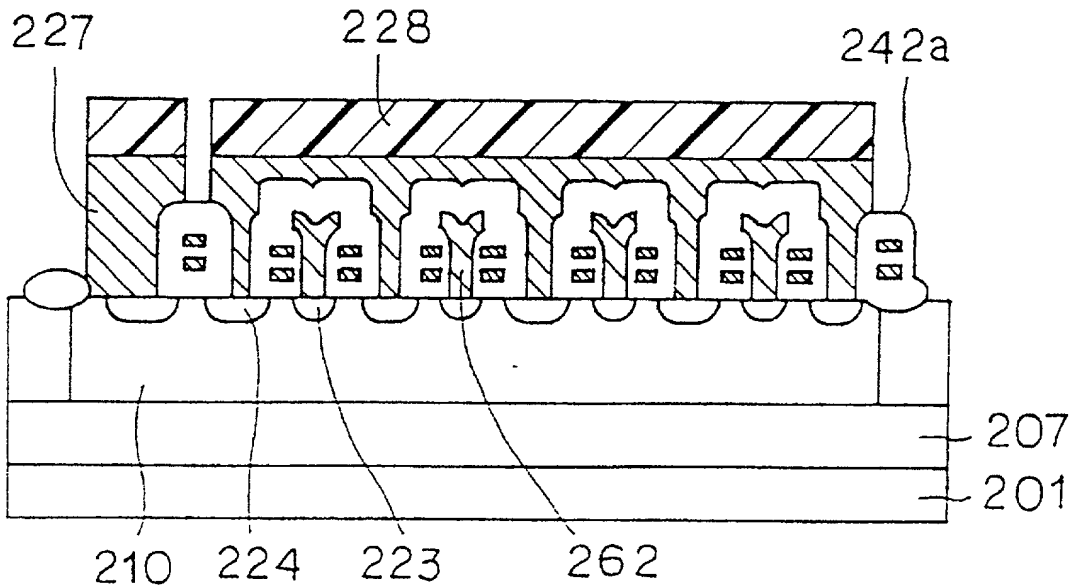


FIG. 120

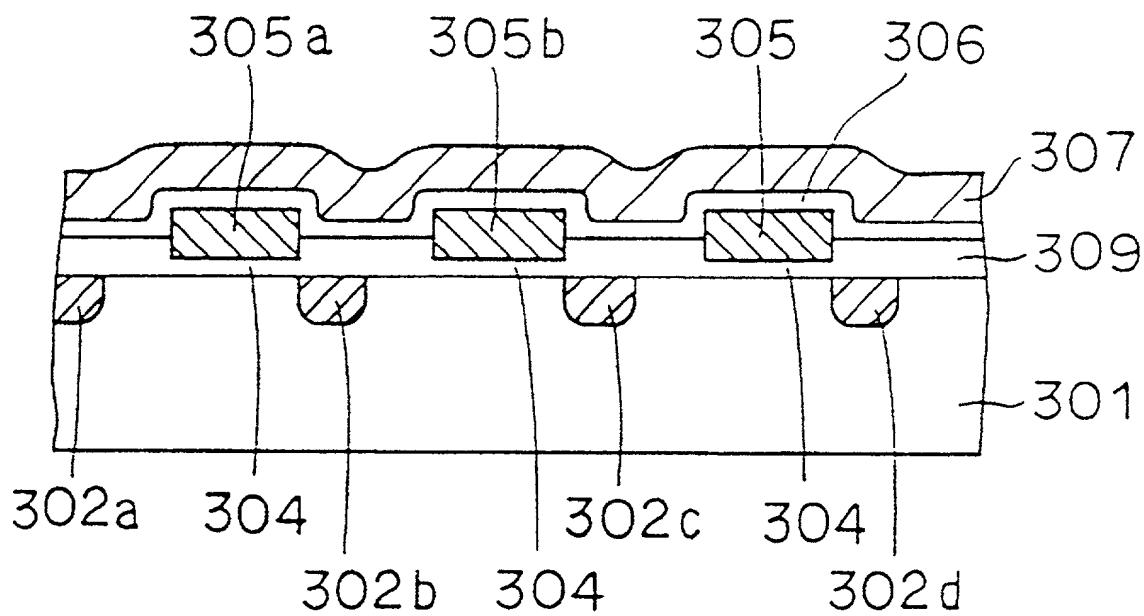




FIG. 121A

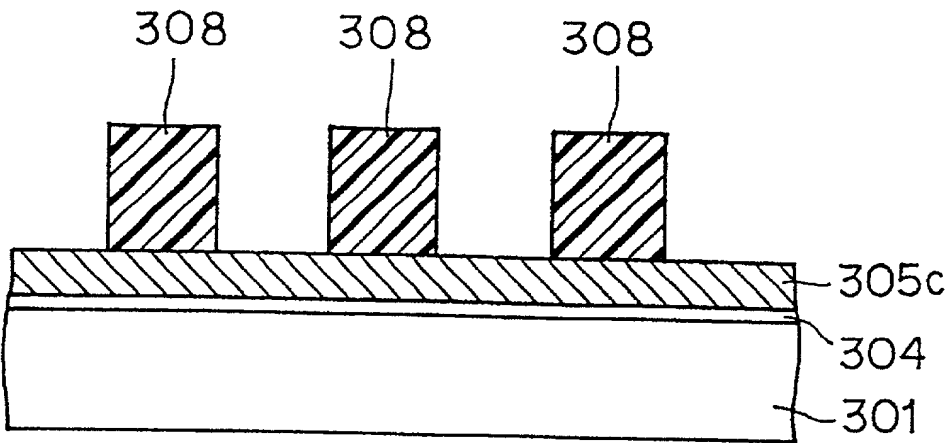


FIG. 121B

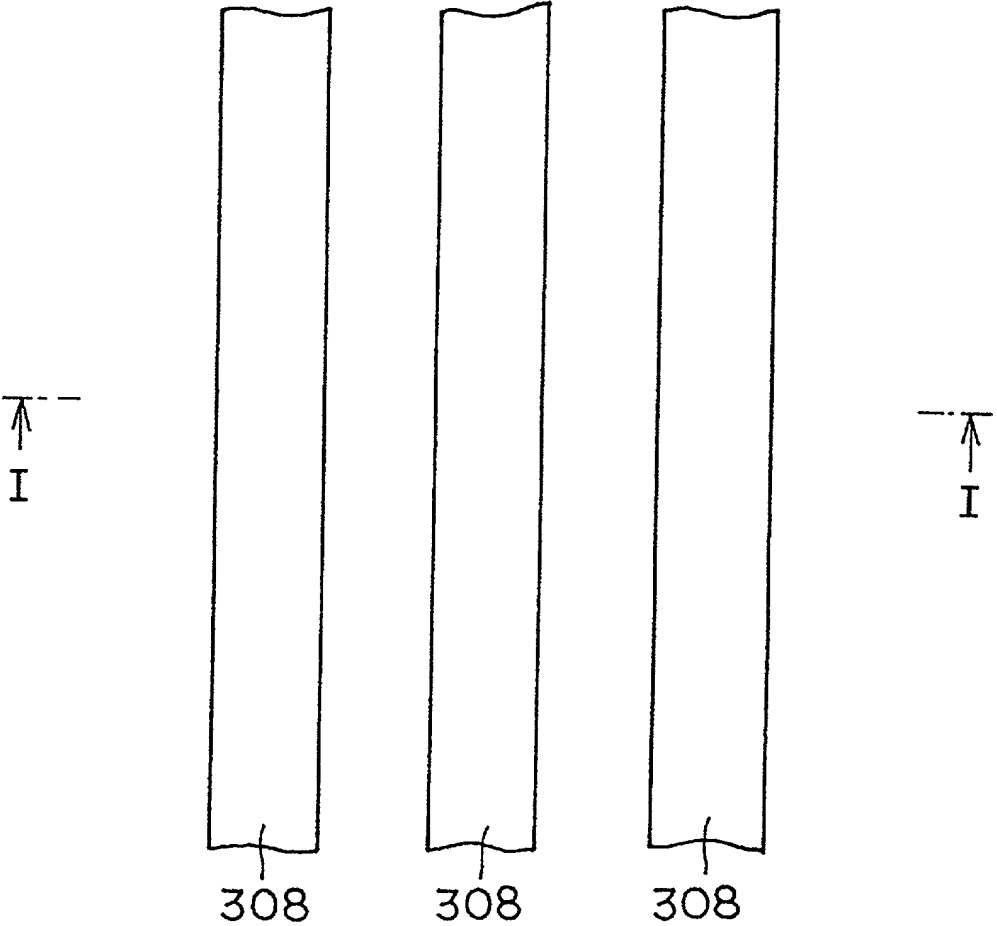


FIG. 122A

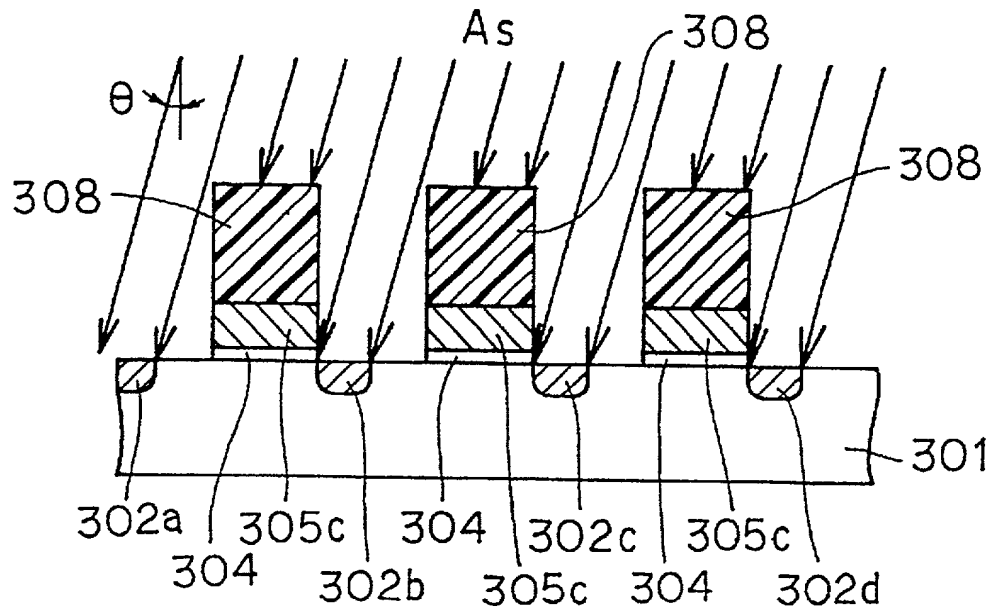


FIG. 122B

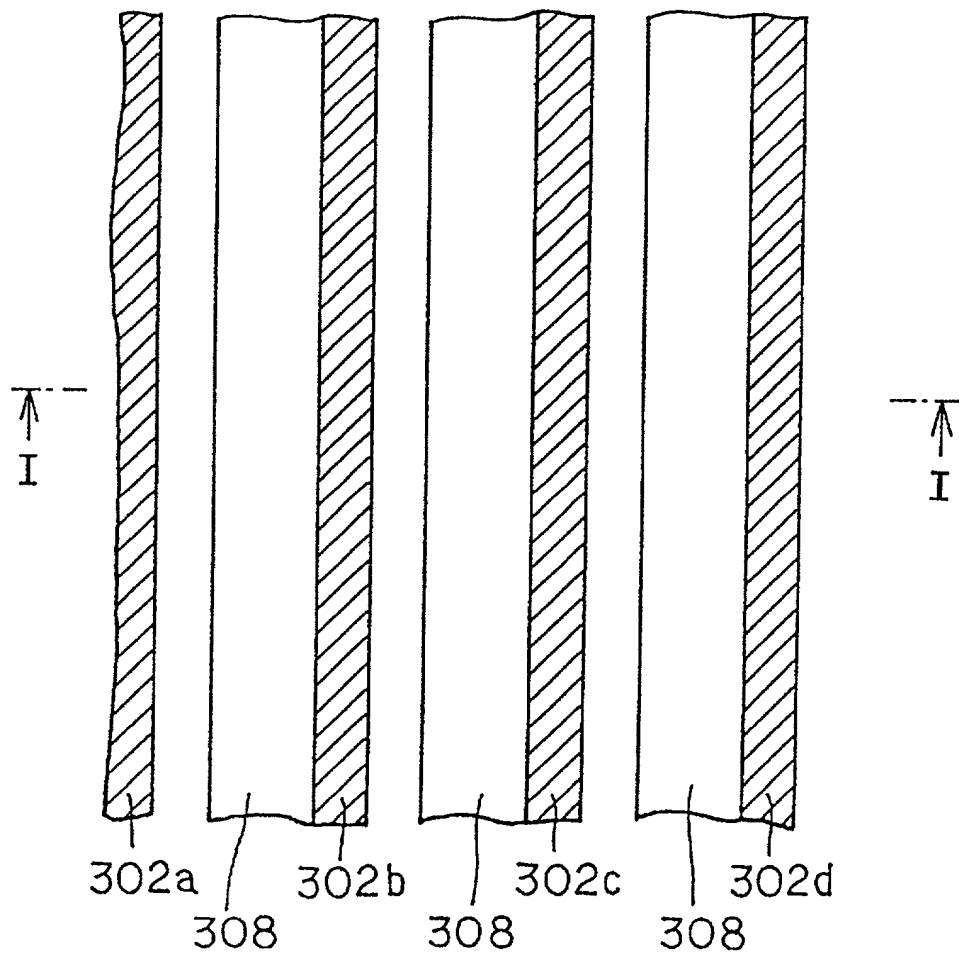


FIG. 123

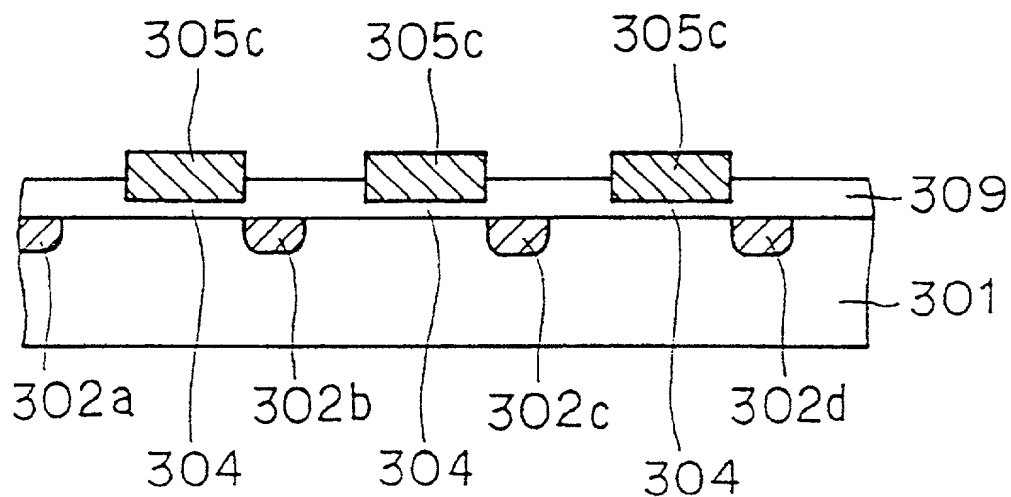


FIG. 124

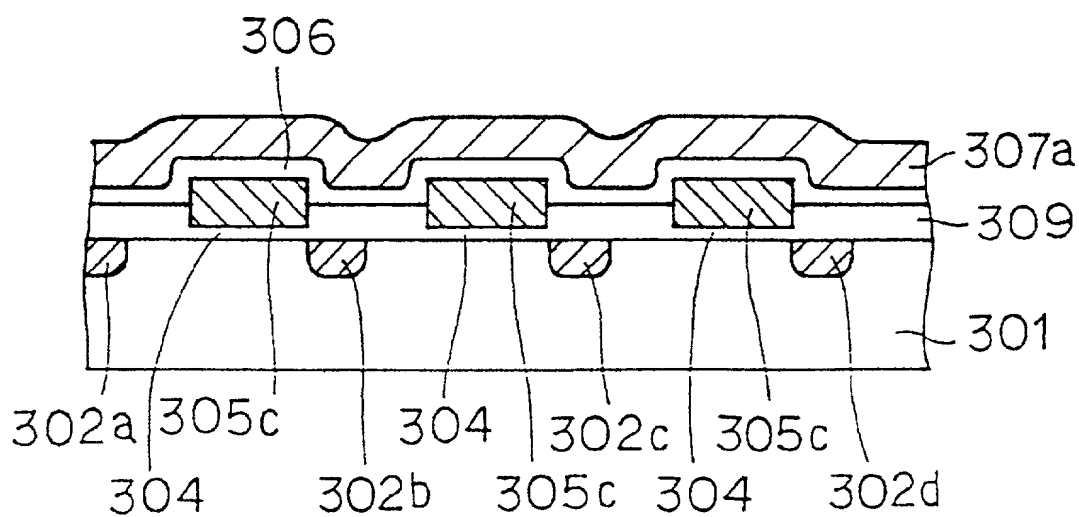


FIG. 125A

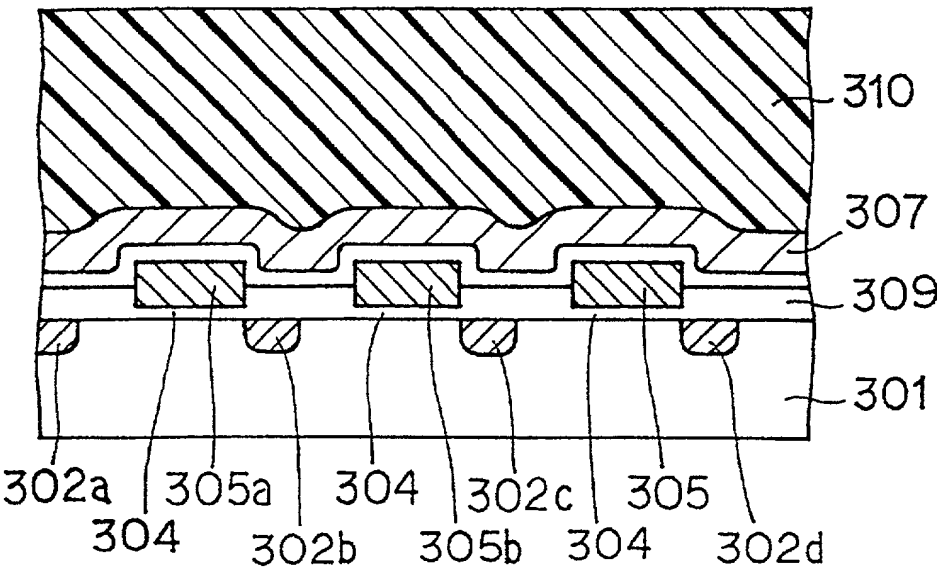


FIG. 125B

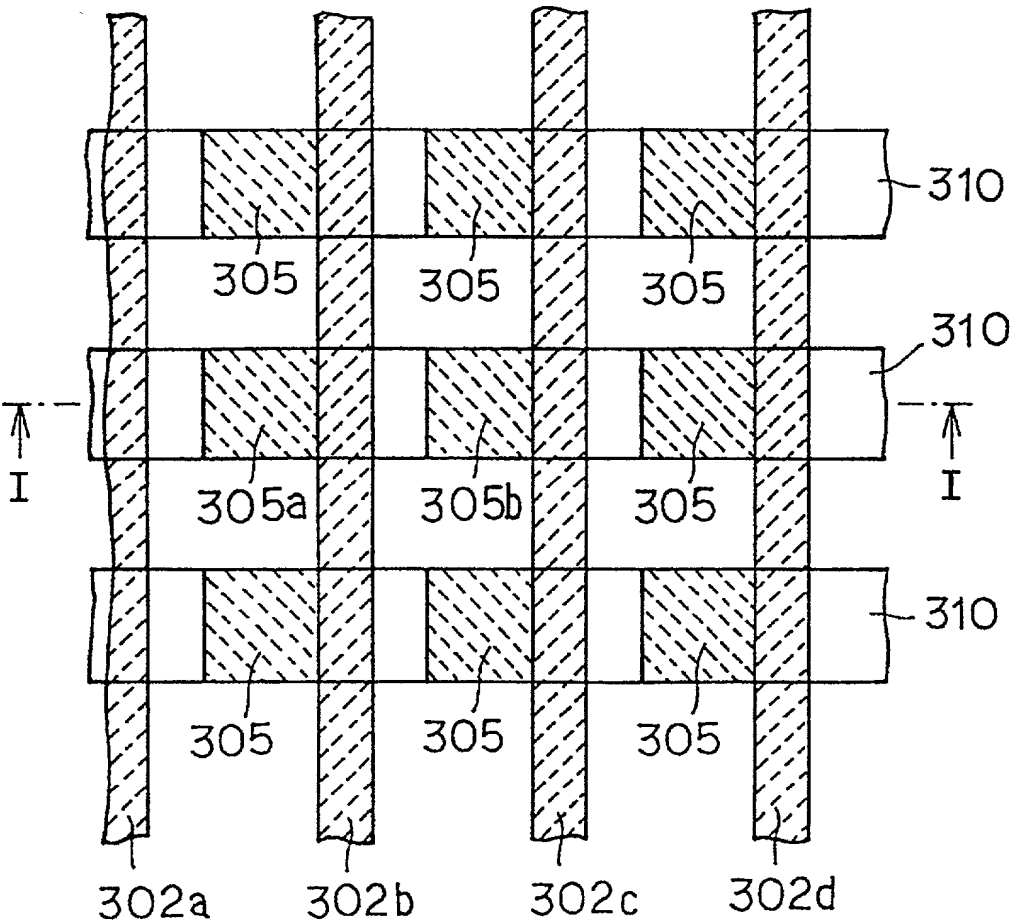


FIG. 126

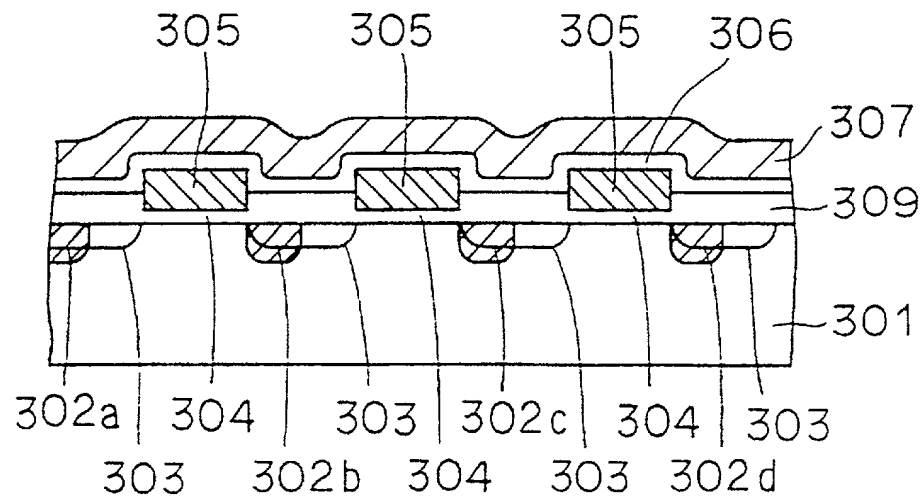


FIG. 127

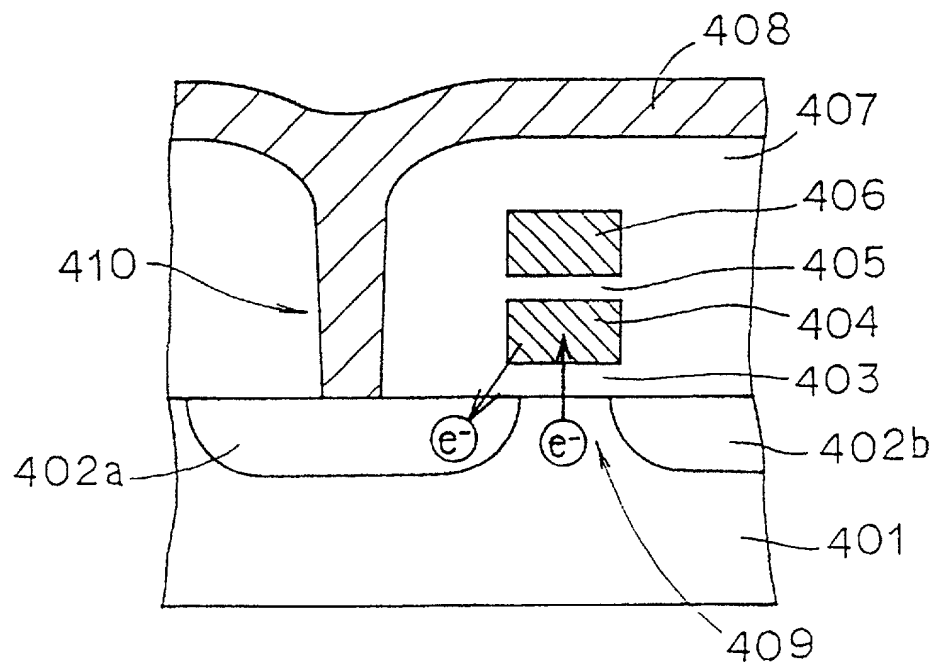


FIG. 128 PRIOR ART

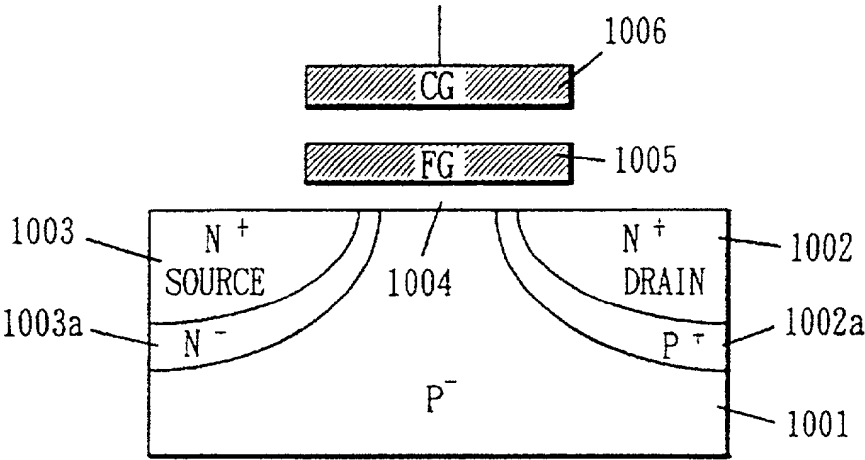


FIG. 129 PRIOR ART

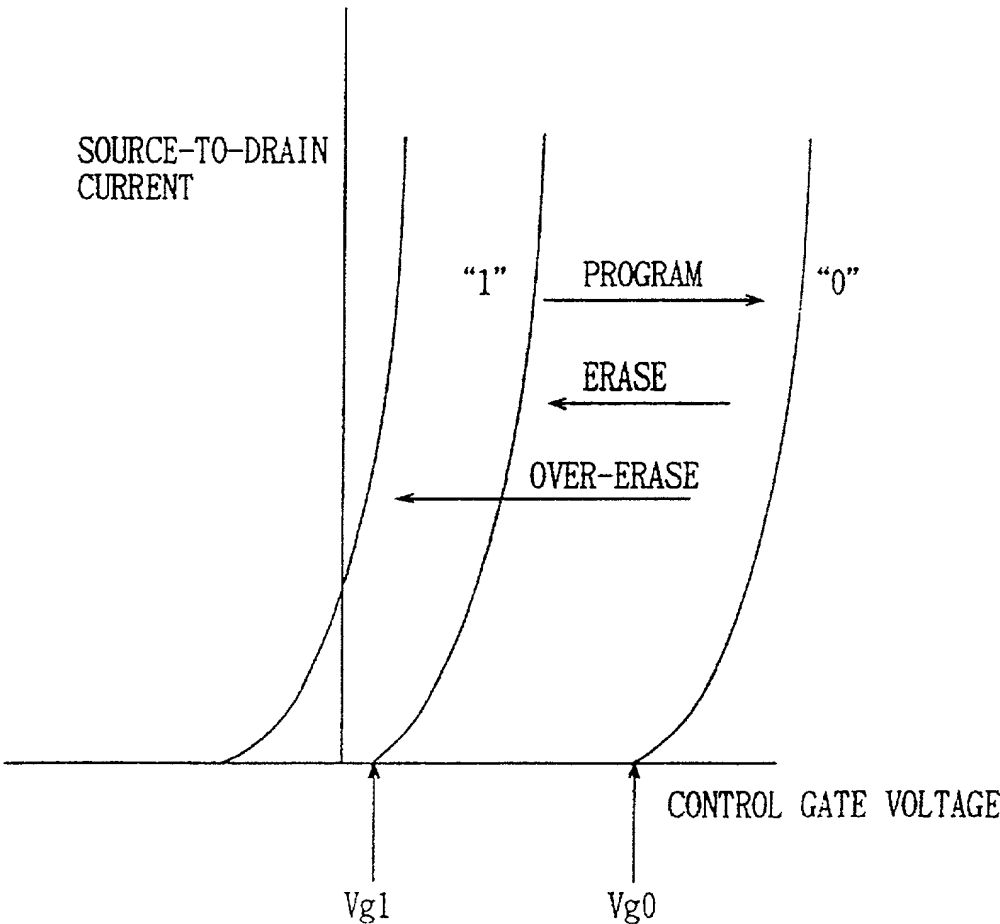


FIG. 130A

PRIOR ART

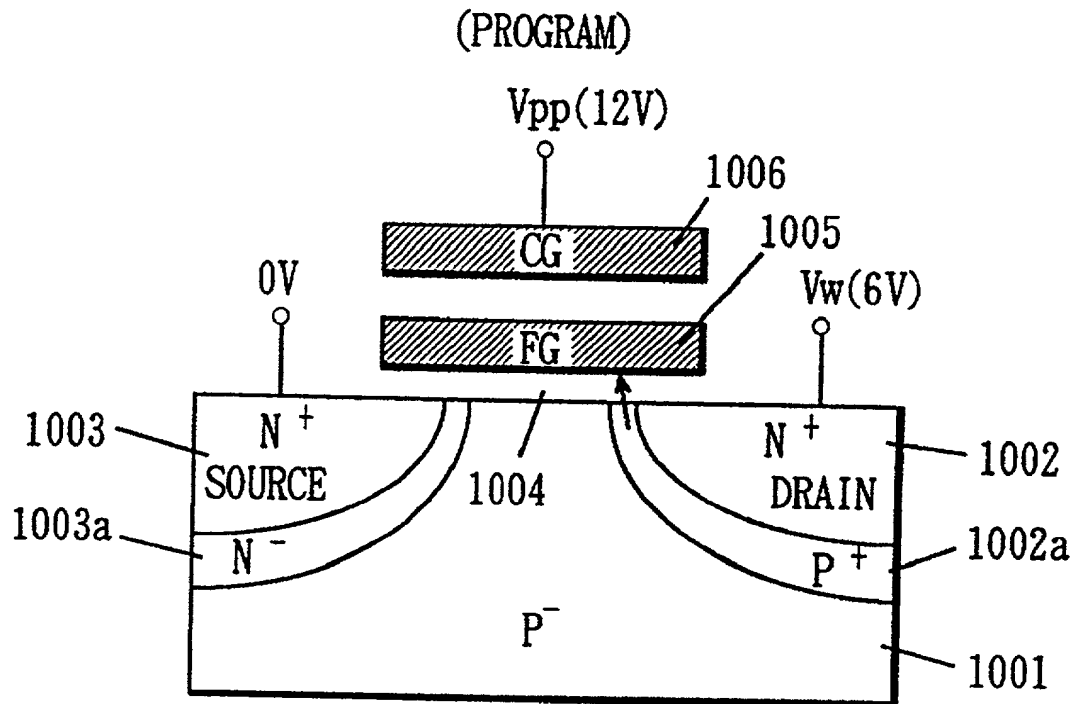


FIG. 130B

PRIOR ART

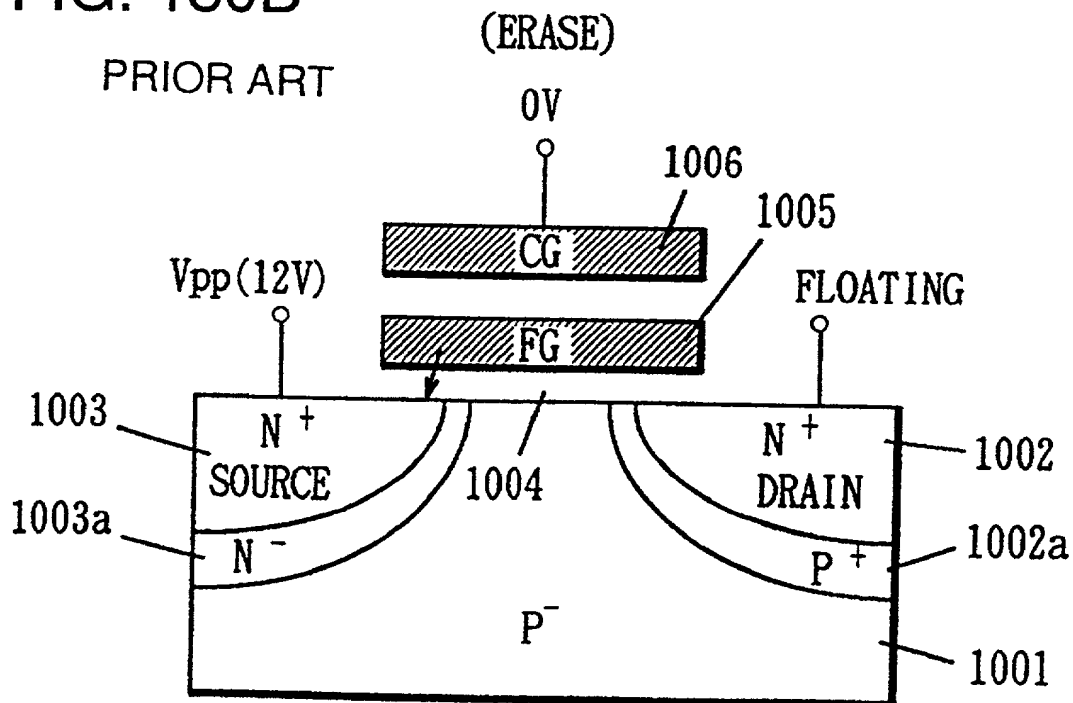


FIG. 131 PRIOR ART

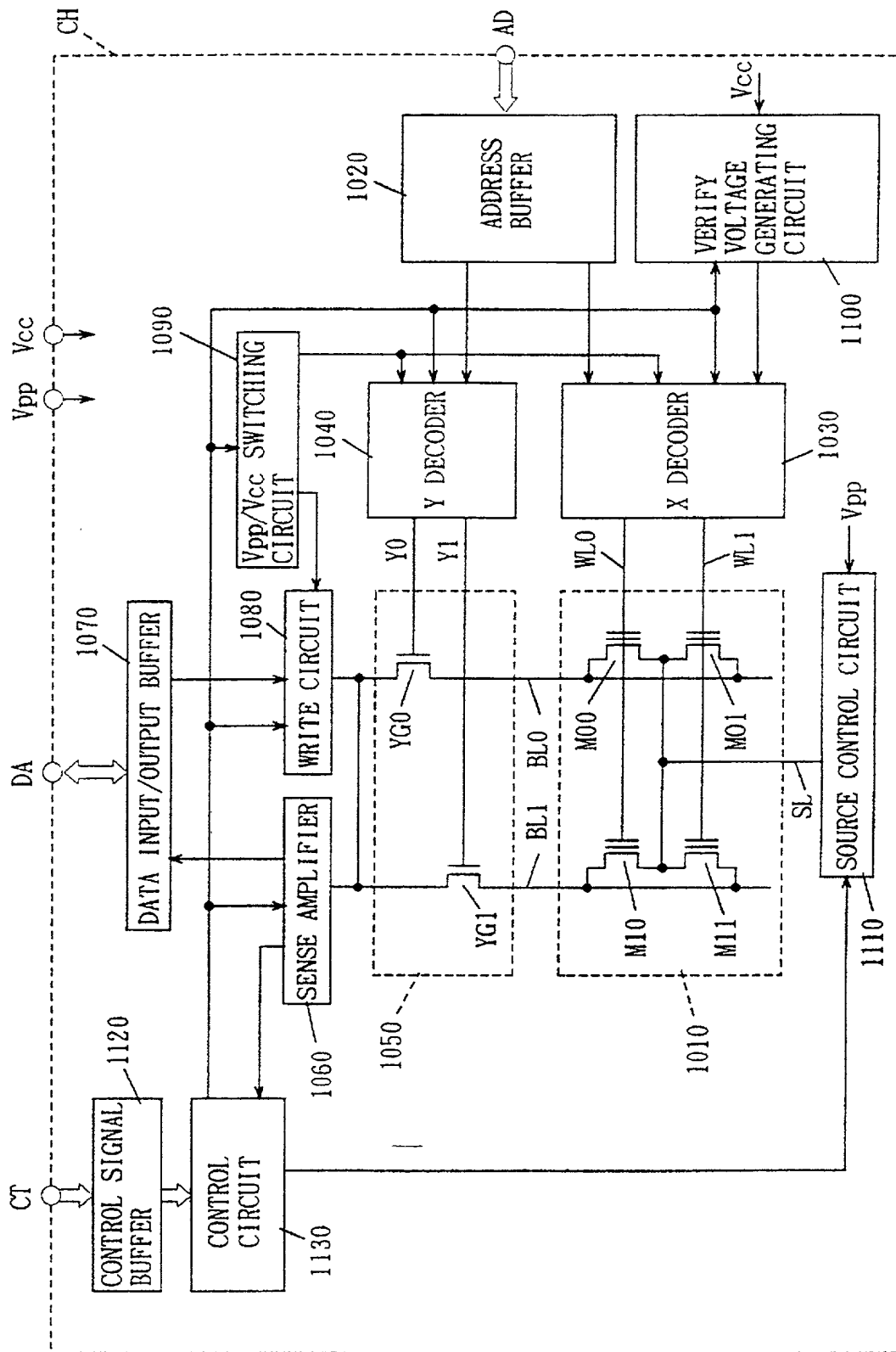
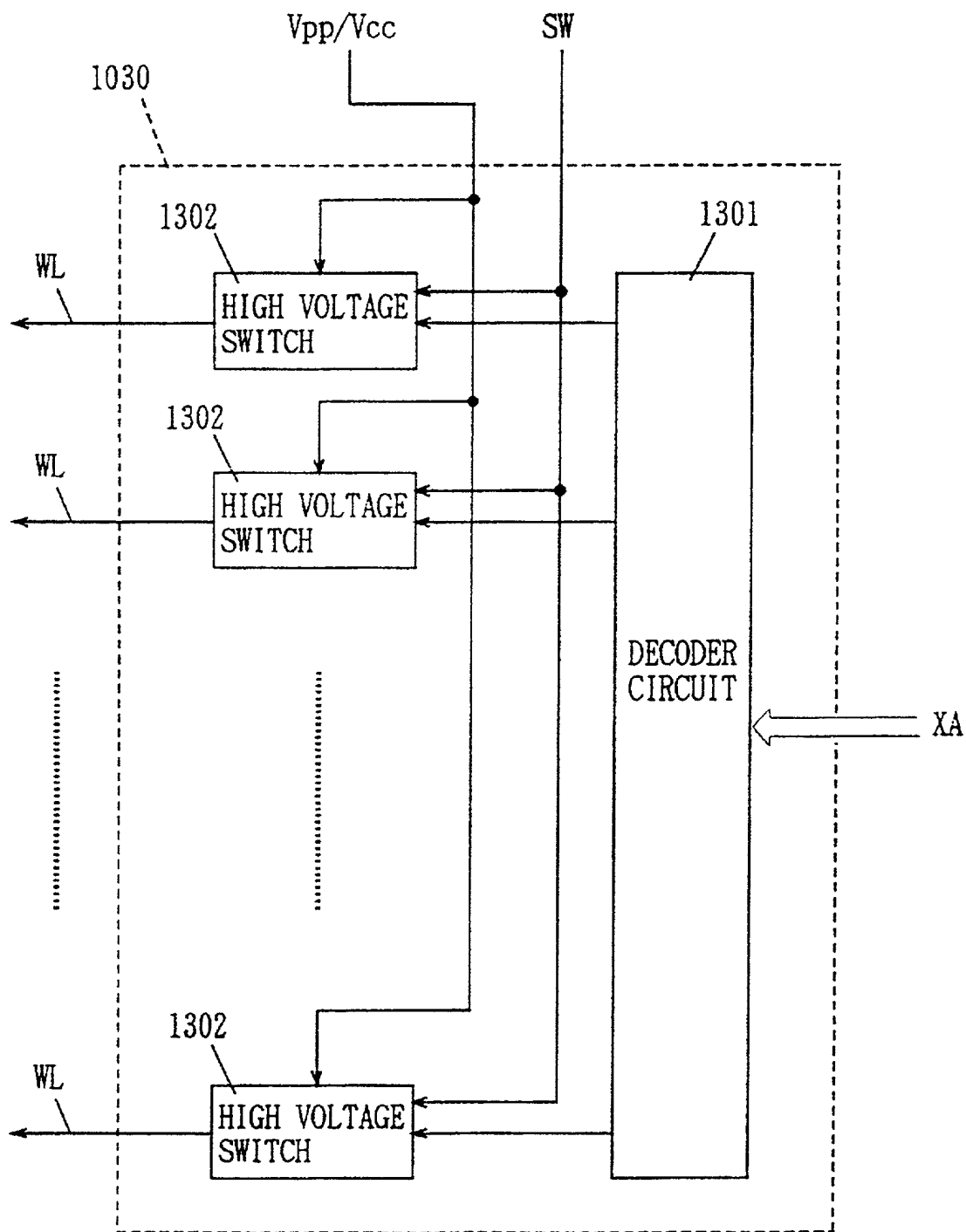




FIG. 132 PRIOR ART



## FIG. 13 3 PRIOR ART

(PROGRAM OPERATION)

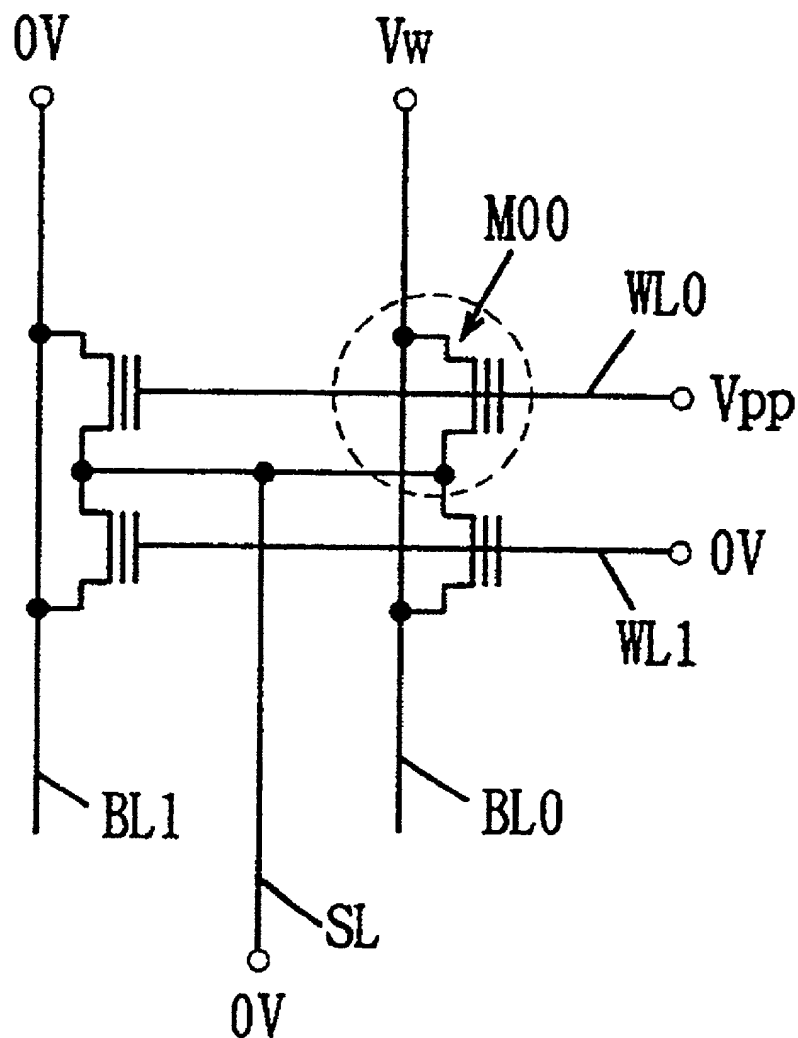


FIG. 134 PRIOR ART

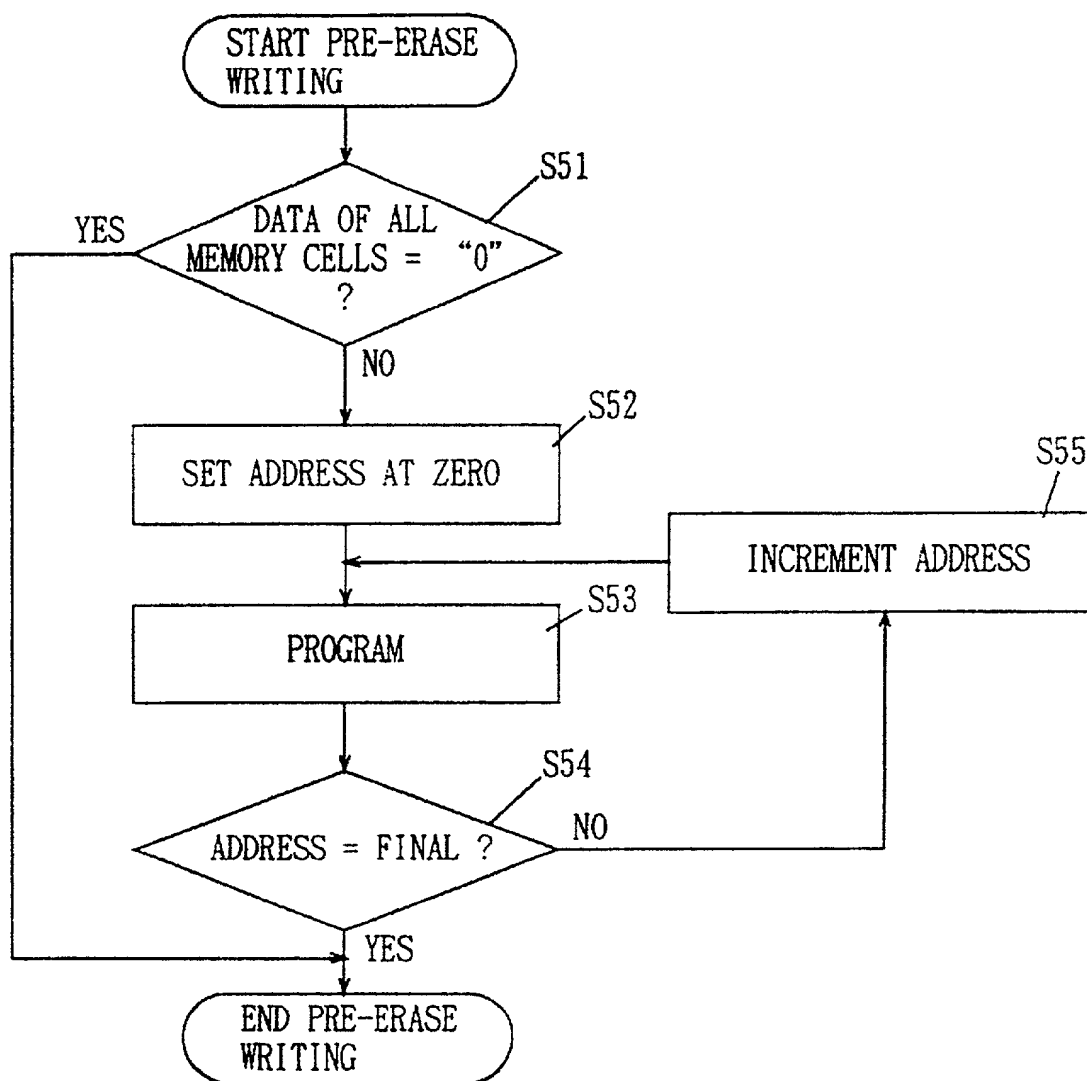


FIG. 135 PRIOR ART

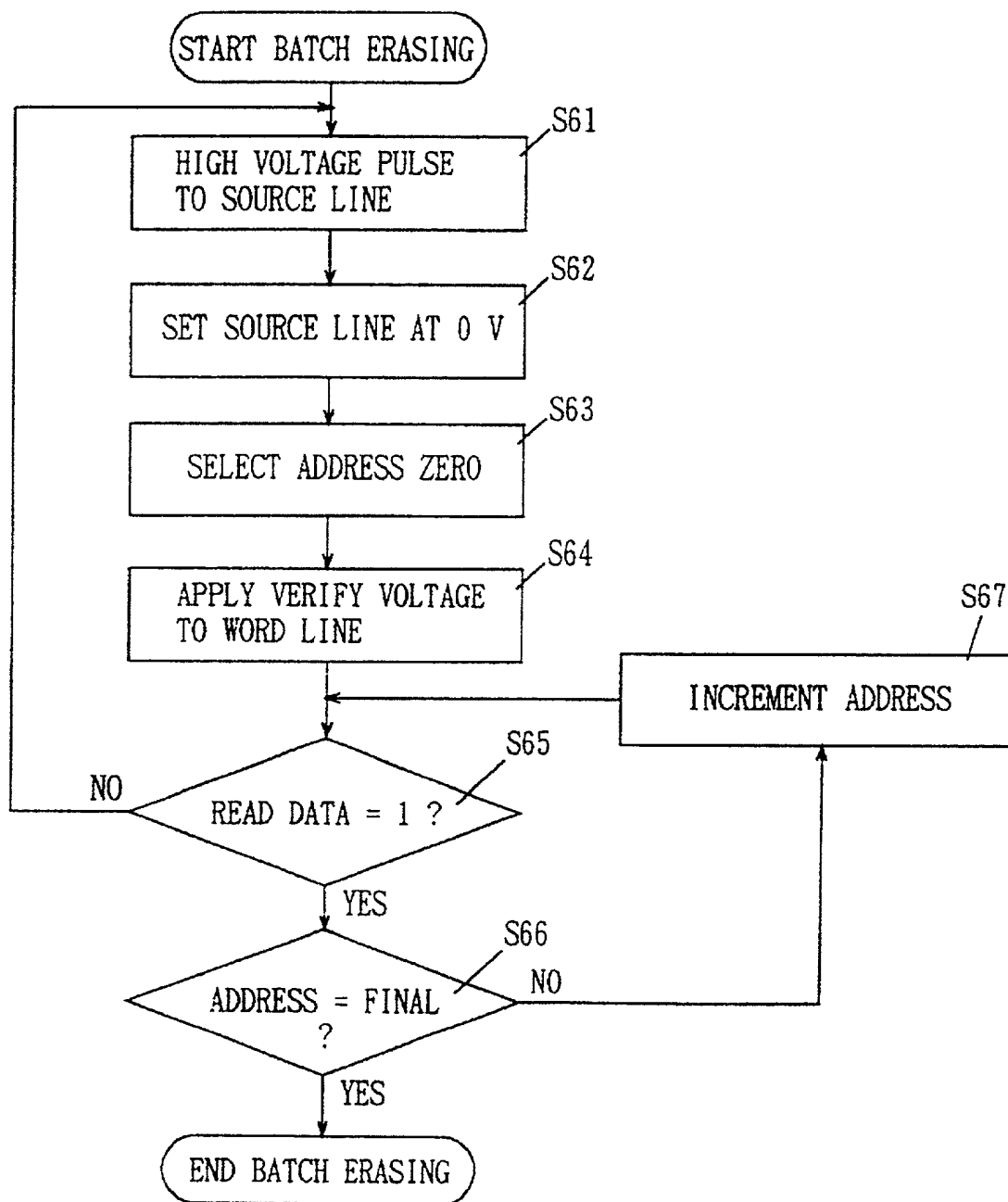


FIG. 136 PRIOR ART

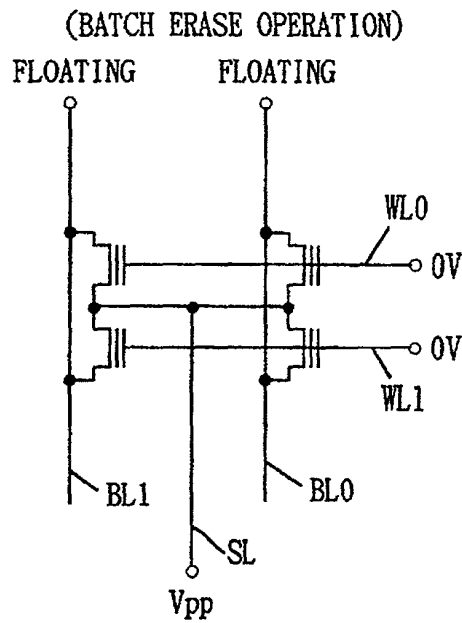


FIG. 137 PRIOR ART

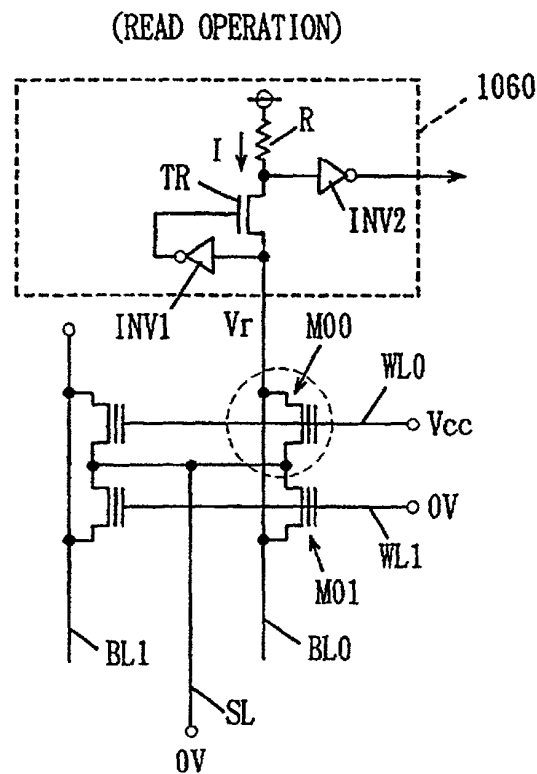


FIG. 138A

PRIOR ART

WORD LINE

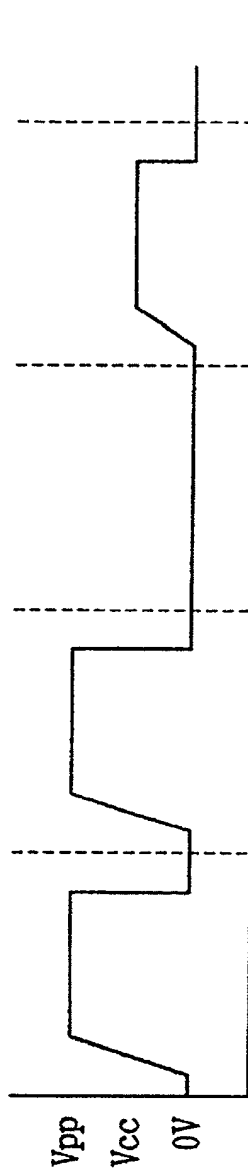


FIG. 138B

PRIOR ART

BIT LINE

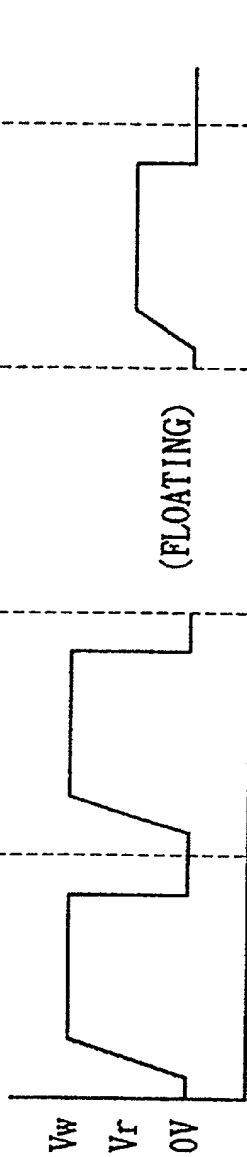


FIG. 138C

PRIOR ART

SOURCE LINE

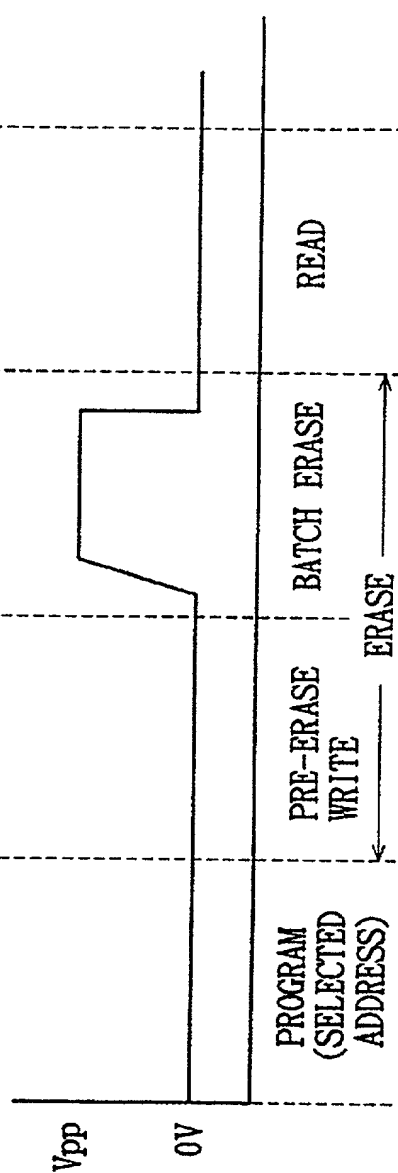


FIG. 139 PRIOR ART

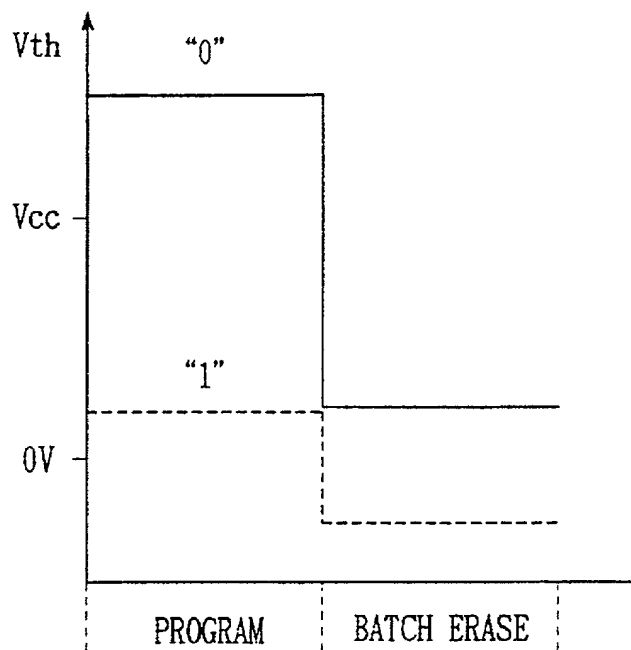


FIG. 140 PRIOR ART

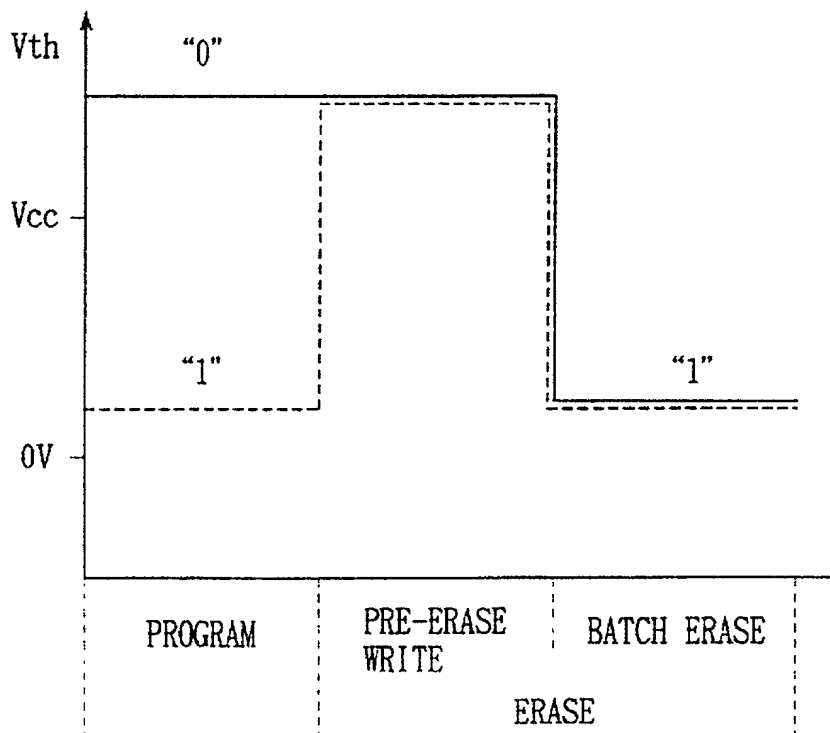


FIG. 141 PRIOR ART

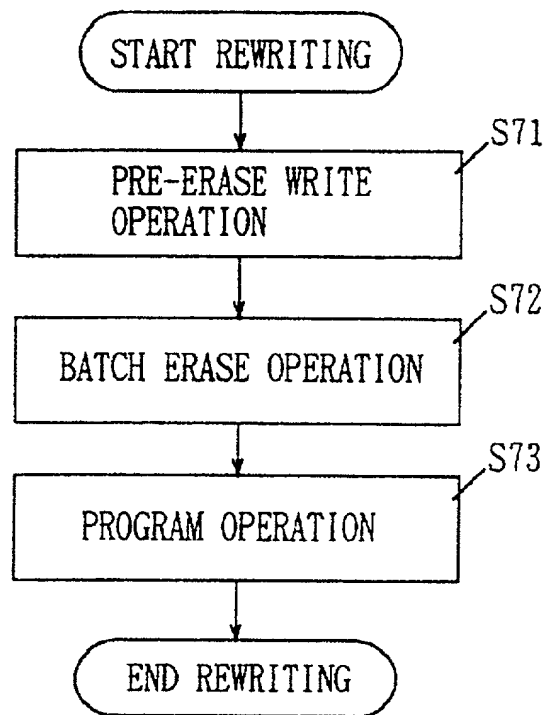


FIG. 142 PRIOR ART

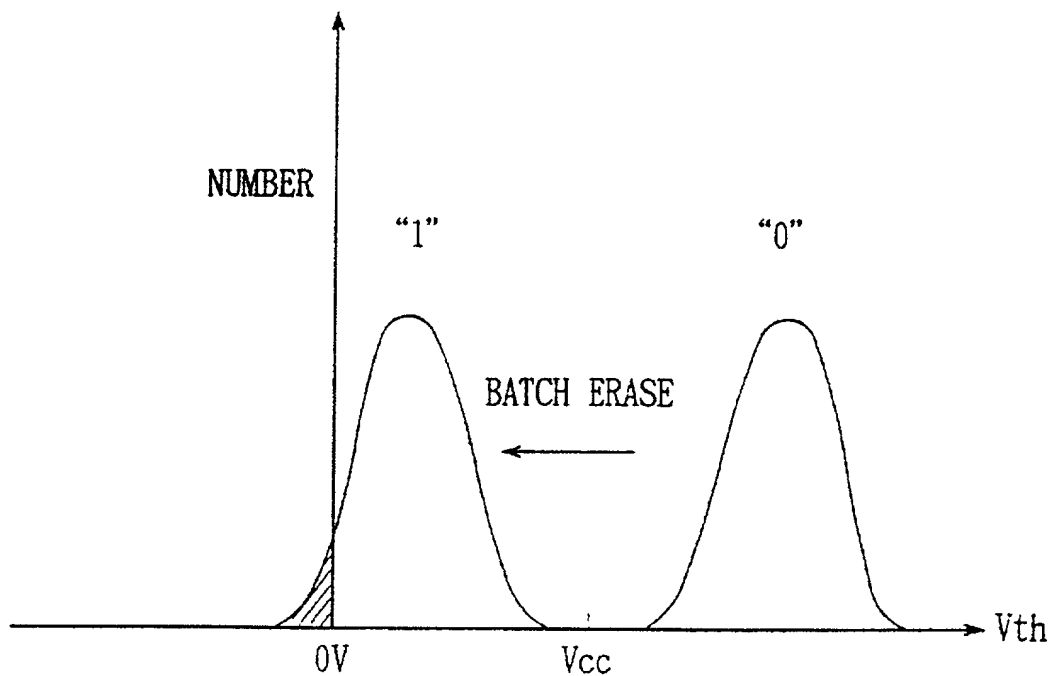




FIG. 143 PRIOR ART

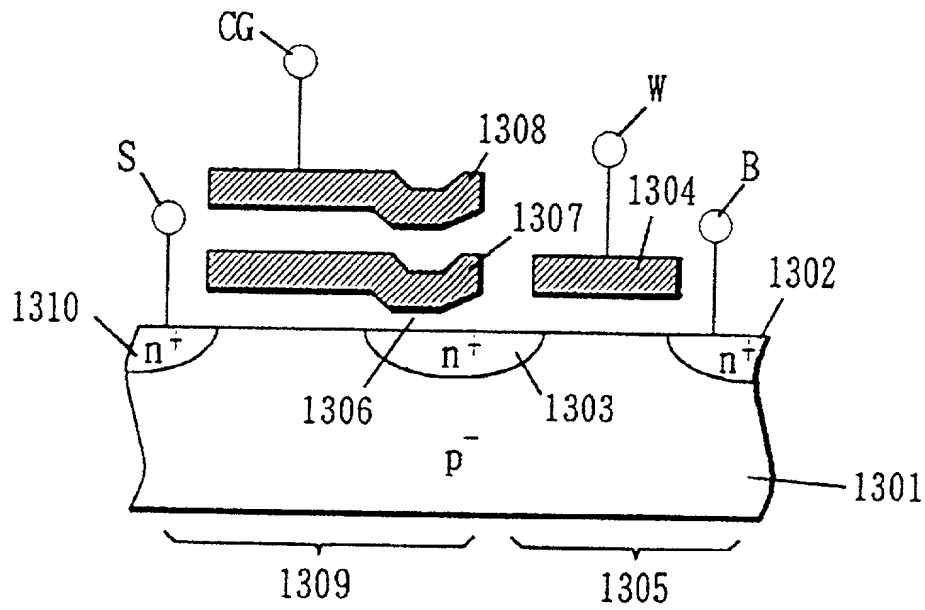
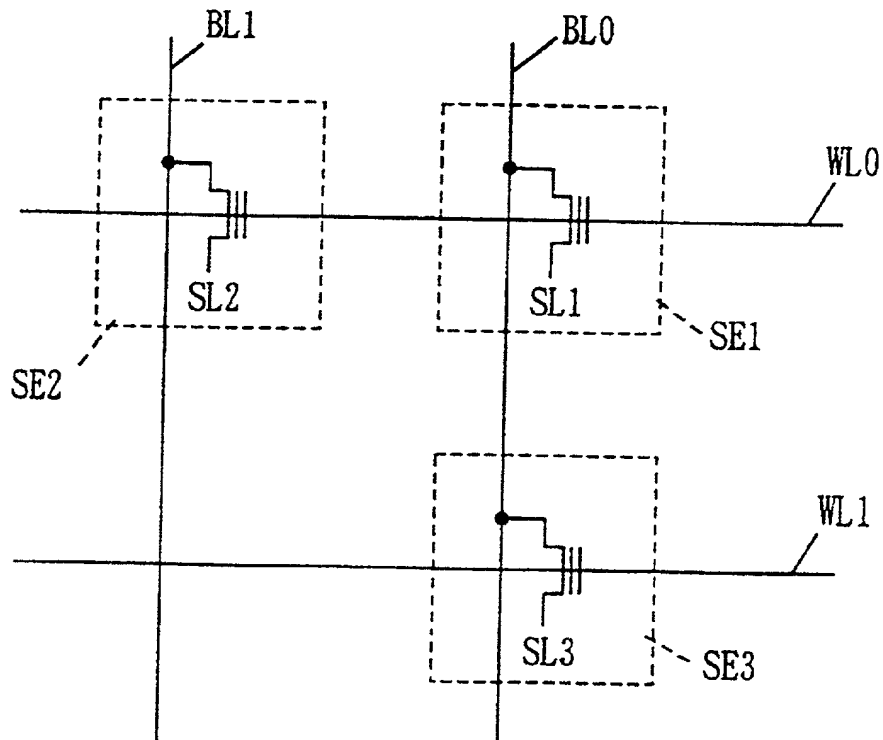


FIG. 144 PRIOR ART



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TOP SECRET 660

FIG. 145

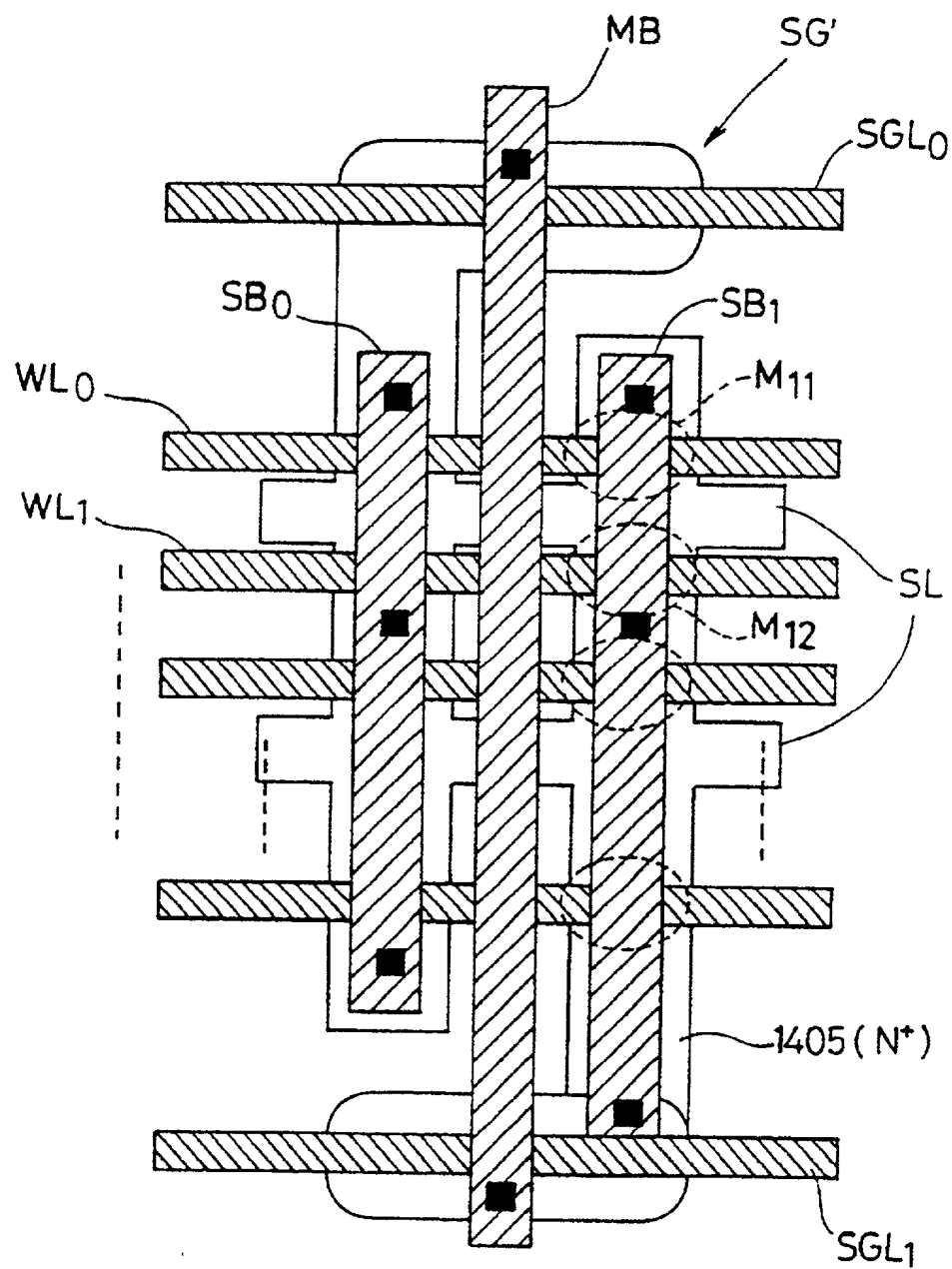


FIG. 146

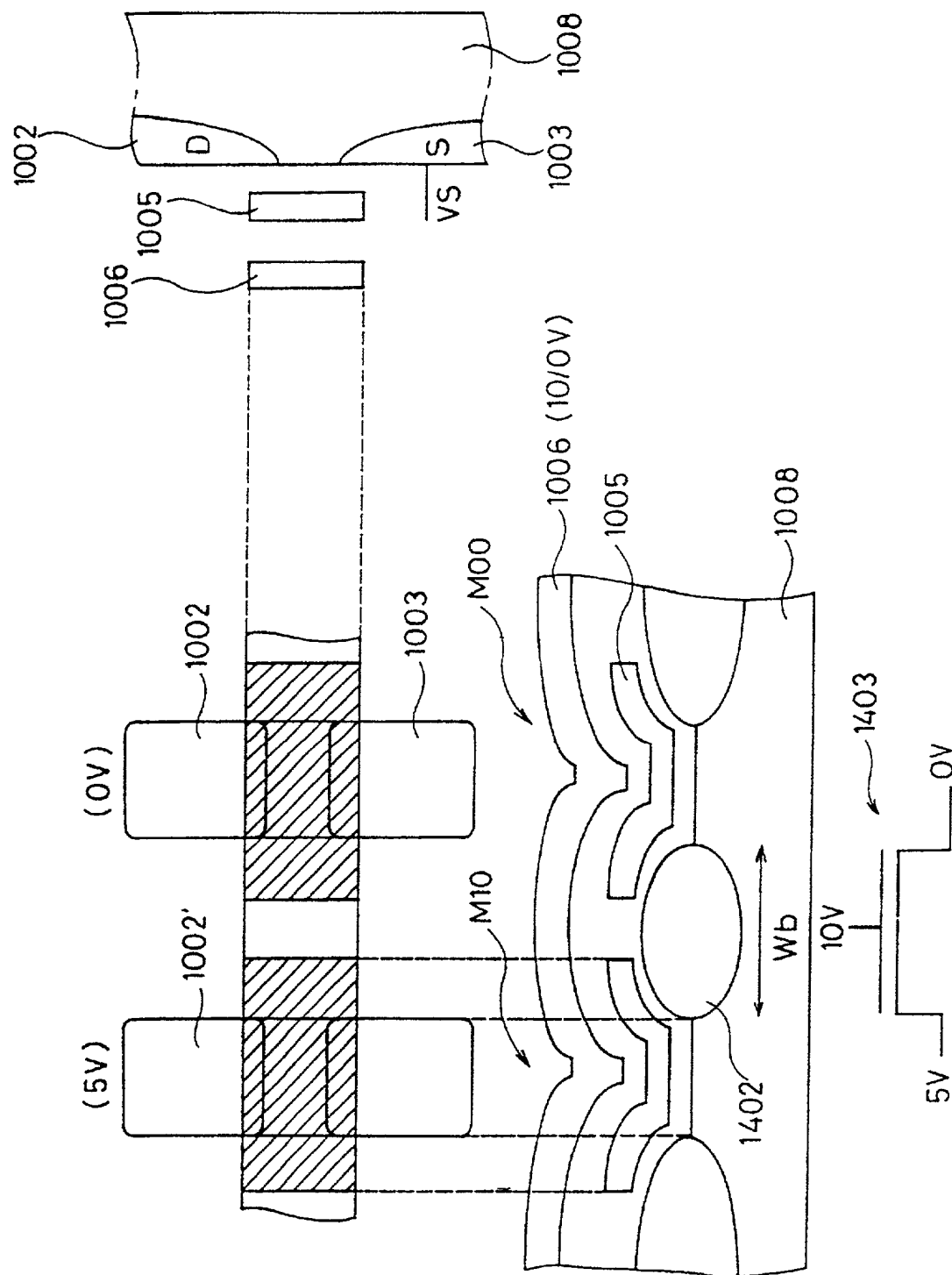


FIG. 147A

PROGRAM

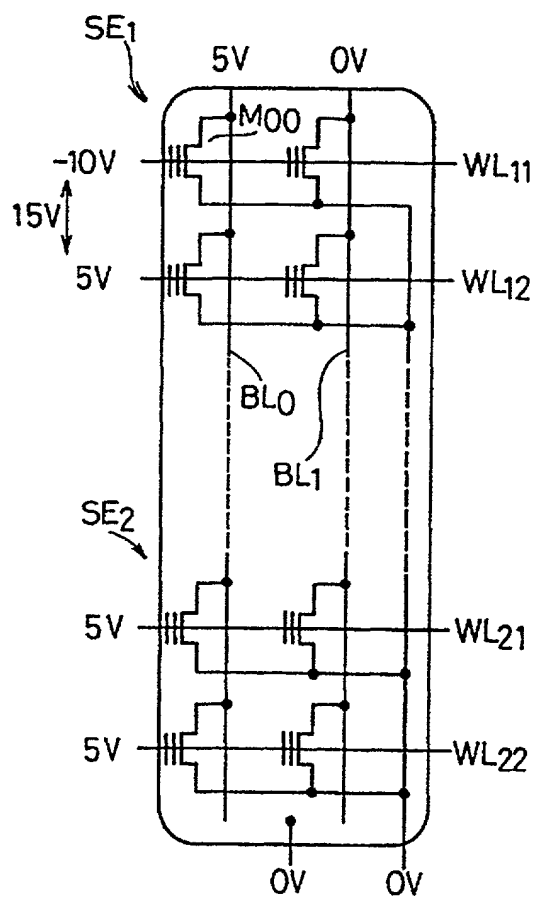


FIG. 147B

ERASE

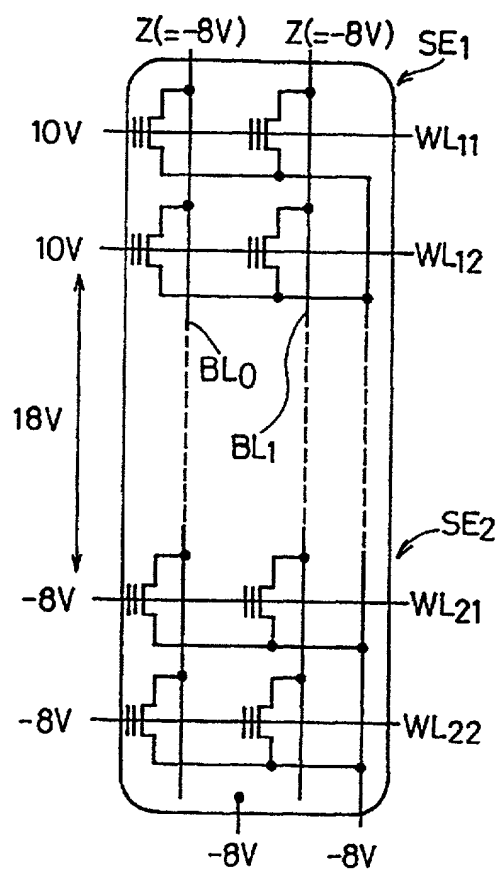


FIG. 148

PRIOR ART

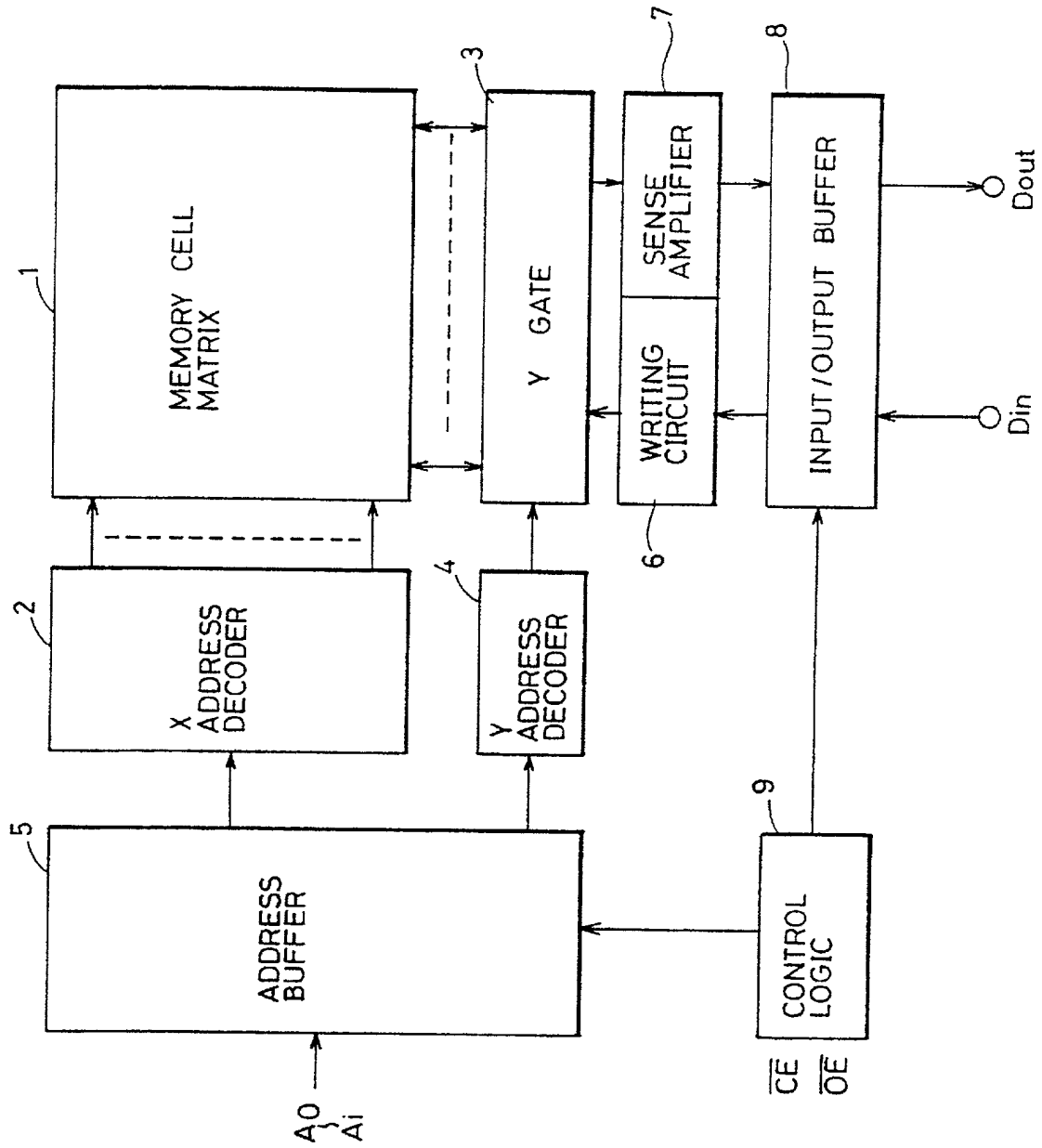


FIG. 148 "Prior Art"

FIG. 149 PRIOR ART

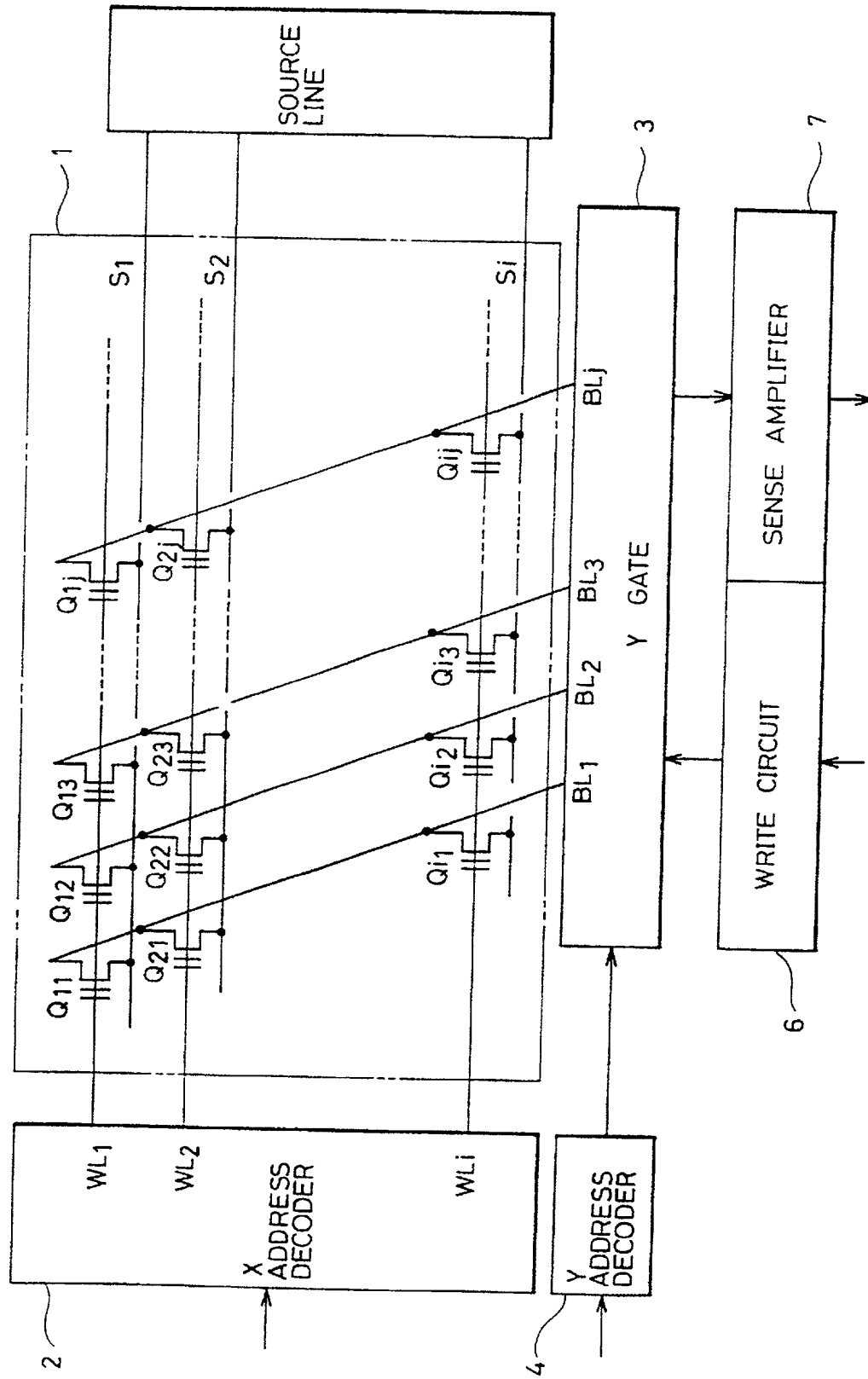


FIG. 150 PRIOR ART

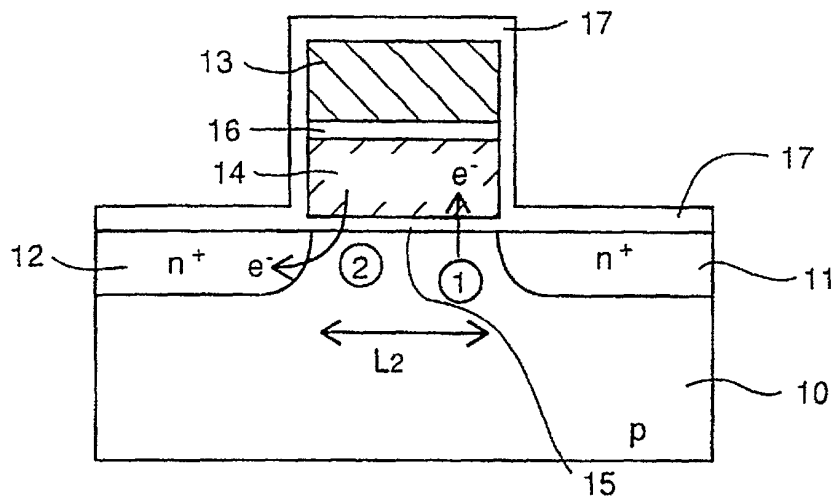


FIG. 151 PRIOR ART

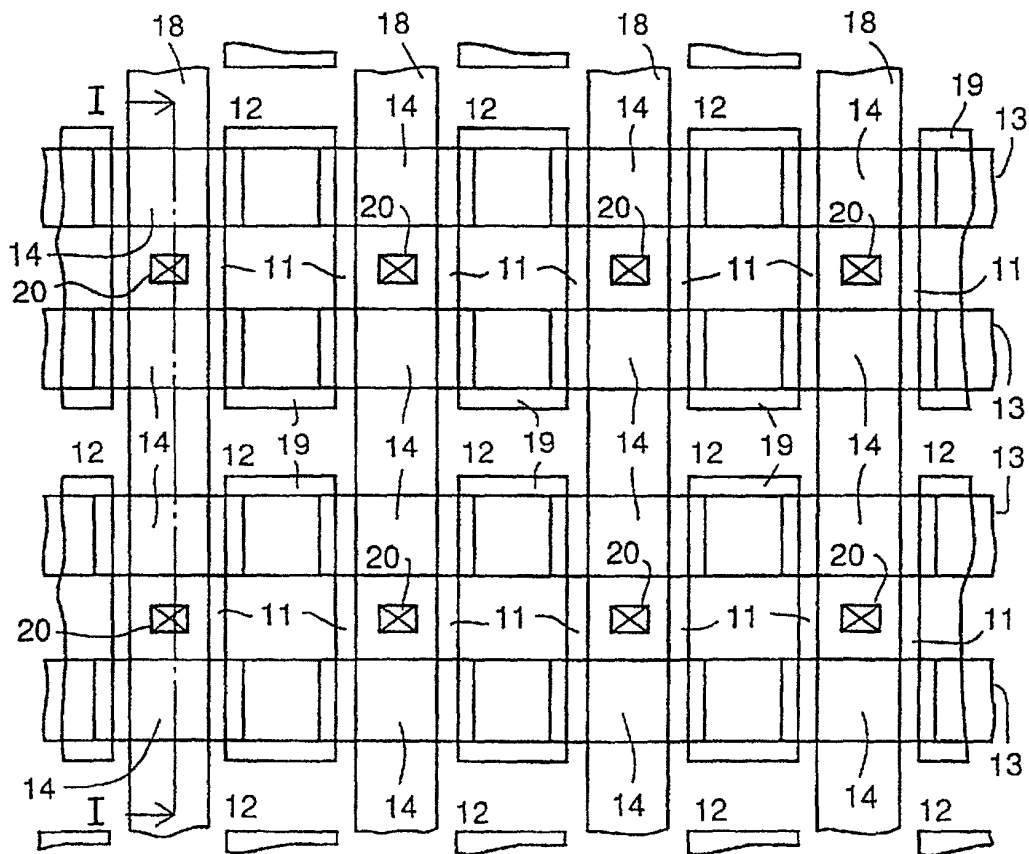


FIG. 152

PRIOR ART

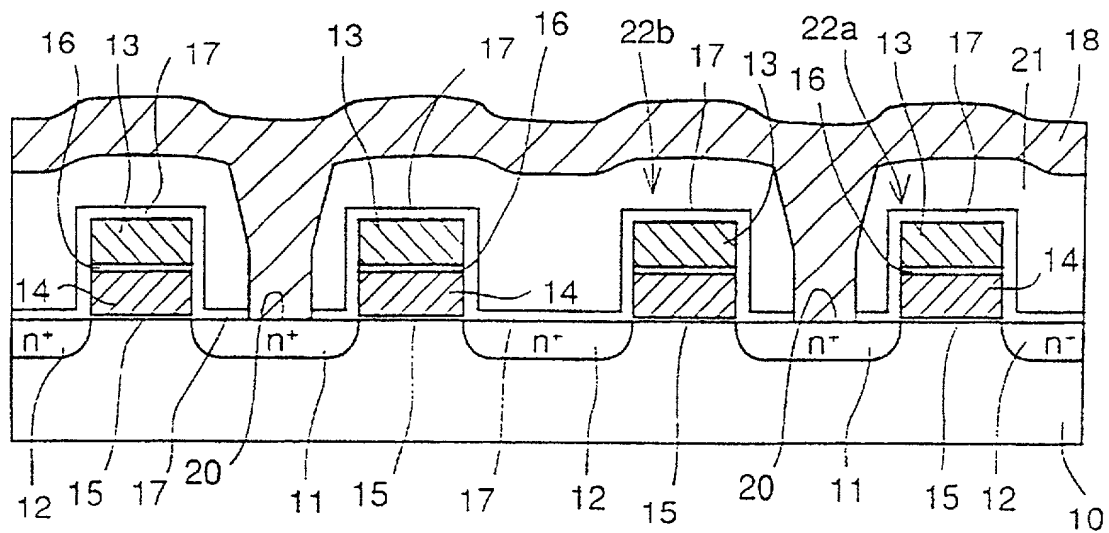




FIG. 153

PRIOR ART

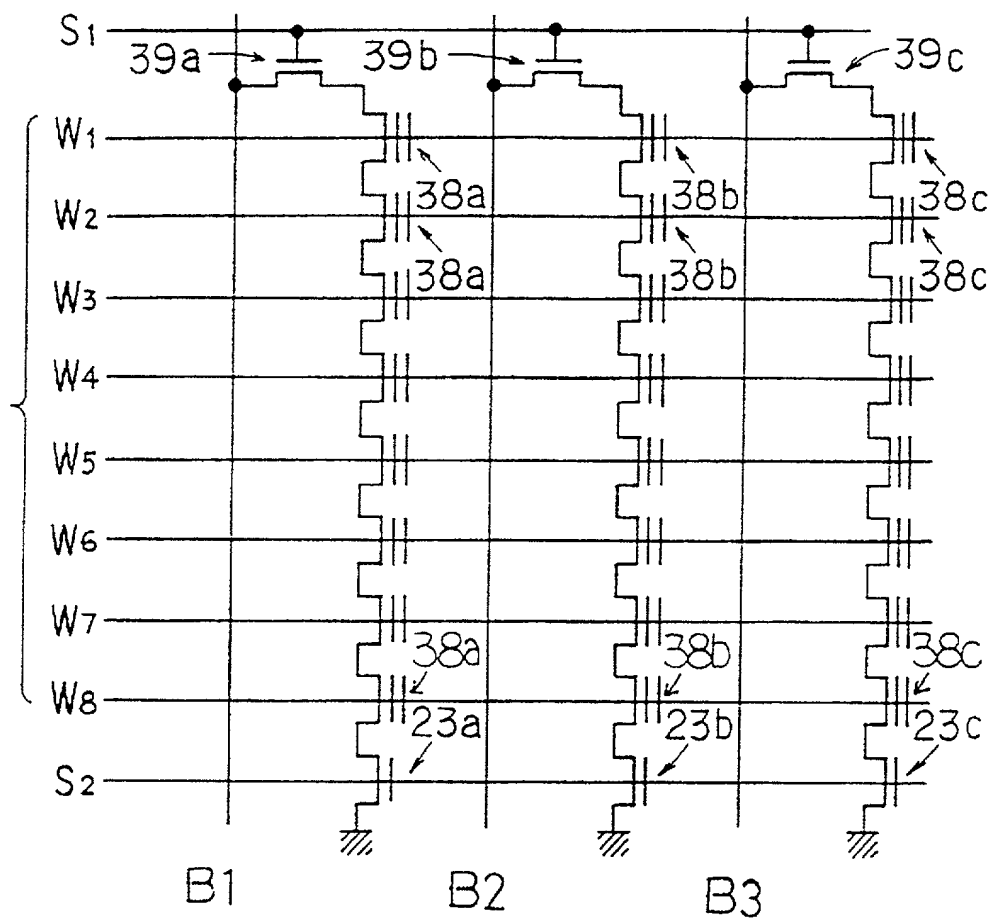


FIG. 154

PRIOR ART

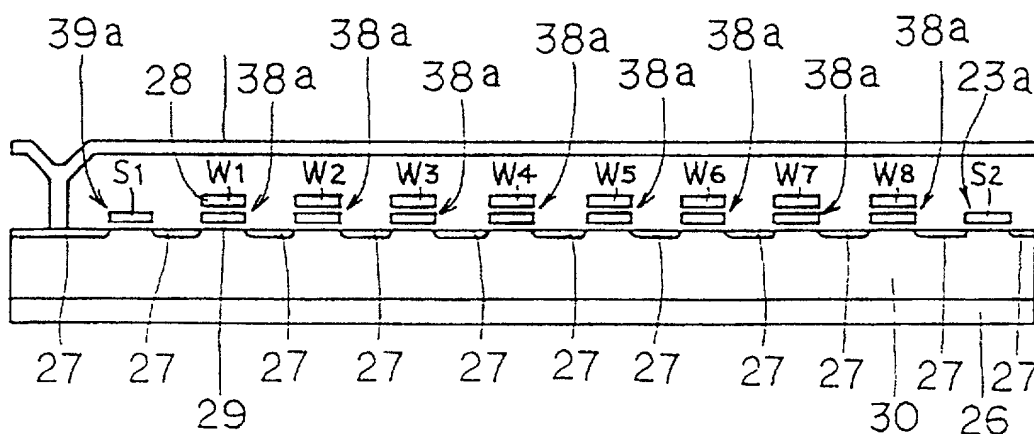


FIG. 155 PRIOR ART

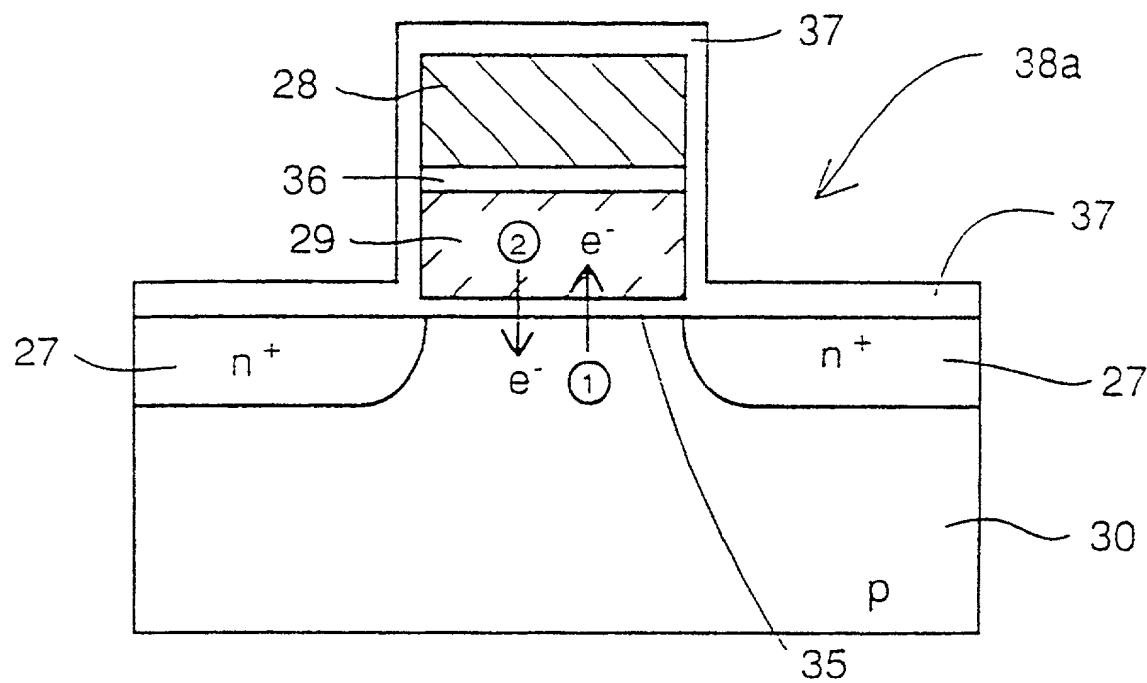


FIG. 156A

PRIOR ART

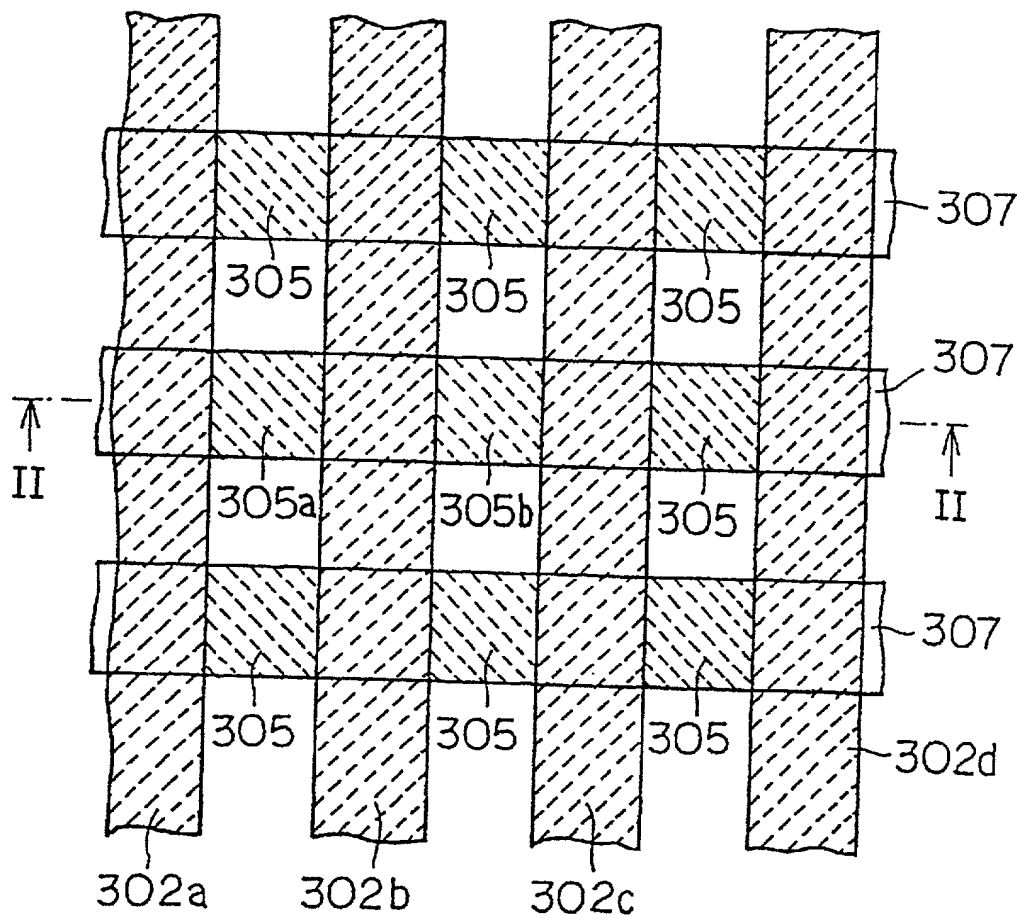


FIG. 156B

PRIOR ART

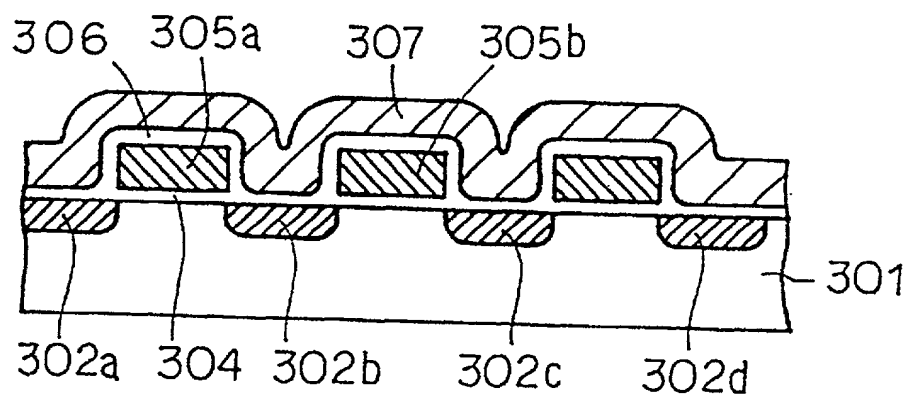


FIG. 157 PRIOR ART

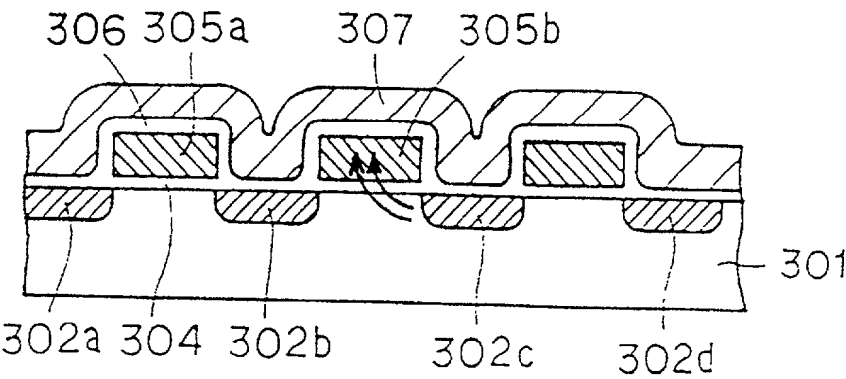


FIG. 158 PRIOR ART

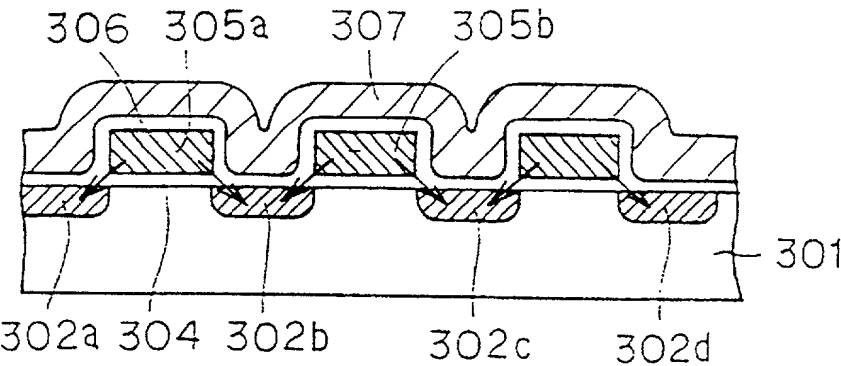


FIG. 159 PRIOR ART

